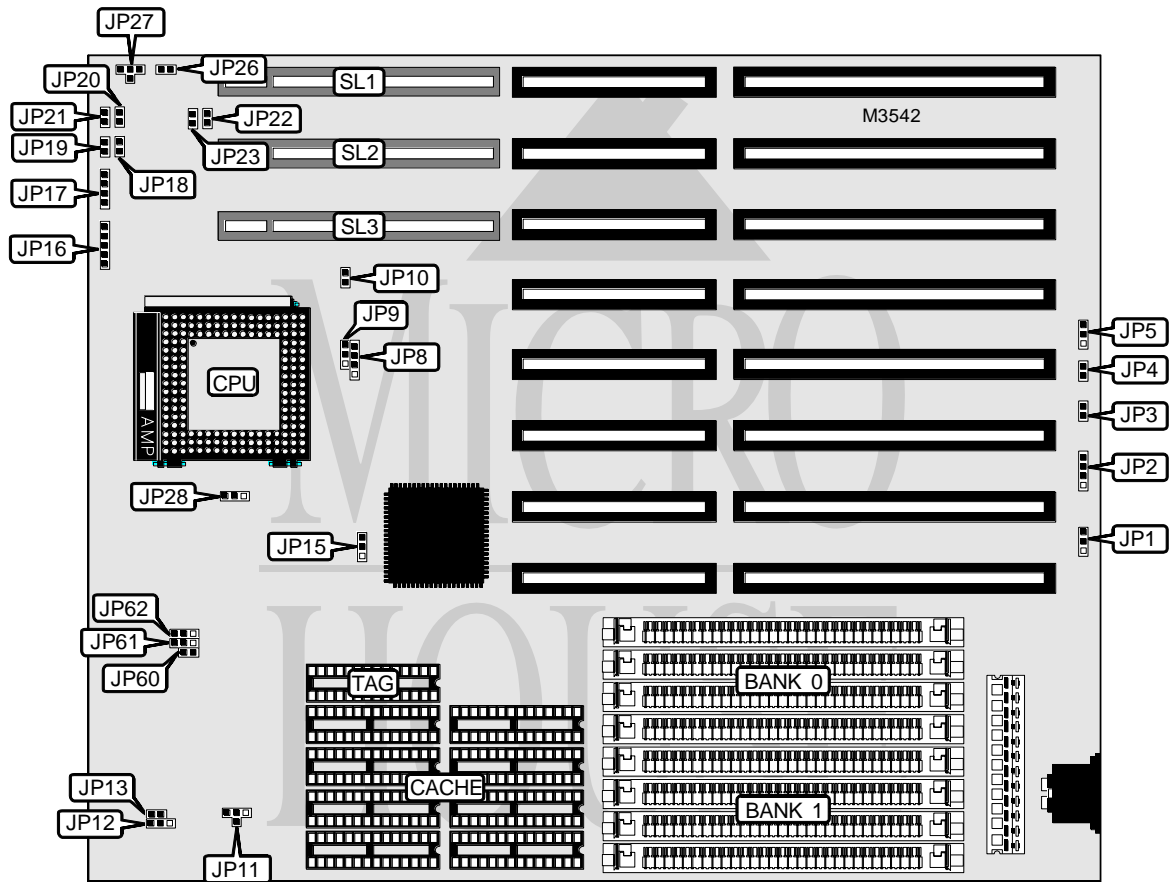


ADDTECH RESEARCH, INC.

4 G L X 4

Processor	CX486SX/AM486SX/80486SX/80487SX/CX486DX/AM486DX/80486DX/CX486DX2(M7)/AM486DX2/80486DX2/P24D/P24T
Processor Speed	25/33/40/50(internal)/66(internal)MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo LED	JP21
Power LED & keylock	JP16	Reset switch	JP26
Speaker	JP17	32-bit VESA local bus slots	SL1 - SL3
Turbo switch	JP19		

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed
Jumper information unavailable	JP3	Unidentified
Jumper information unavailable	JP4	Unidentified
Flash BIOS voltage select 12v	JP5	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP5	Pins 1 & 2 closed
Jumper information unavailable	JP18	Unidentified
Jumper information unavailable	JP20	Unidentified

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	None
65MB	(4) 256K x 9	(4) 16M x 9
65MB	(4) 16M x 9	(4) 256K x 9
68MB	(4) 1M x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 4M x 9	(4) 16M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	None	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

Note: The location of banks 0 & 1 are unidentified.

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CACHE JUMPER CONFIGURATION		
Size	JP11	JP12
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed	Pins 2 & 3 closed
1MB	Pins 2 & 4 closed	Pins 2 & 3 closed

CPU SPEED SELECTION							
Speed	JP13	JP22	JP27	JP28	JP60	JP61	JP62
25MHz	Closed	Open	2 & 4	2 & 3	Closed	1 & 2	2 & 3
33MHz	Open	Open	2 & 4	2 & 3	Closed	2 & 3	1 & 2
40MHz	Open	Closed	2 & 3	1 & 2	Open	1 & 2	2 & 3
50iMHz	Closed	Open	2 & 4	2 & 3	Closed	1 & 2	2 & 3
50MHz	Open	Closed	1 & 2	1 & 2	Open	2 & 3	1 & 2
66iMHz	Open	Open	2 & 4	2 & 3	Closed	2 & 3	1 & 2

CPU TYPE SELECTION			
Type	JP8	JP9	JP10
80486SX	Pins 2 & 3 closed	Open	Open
80487SX	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed
80486DX	Pins 1 & 2, 3 & 4 closed	Pins 2 & 3 closed	Open
80486DX2	Pins 1 & 2, 3 & 4 closed	Pins 2 & 3 closed	Open
P24D	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed
P24T	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed

CPU TYPE SELECTION	
Type	JP15
Cyrix	Pins 2 & 3 closed
AMD	Pins 1 & 2 closed
Intel	Pins 1 & 2 closed

VL BUS WAIT STATE SELECTION	
Setting	JP23
0	Open
1	Closed