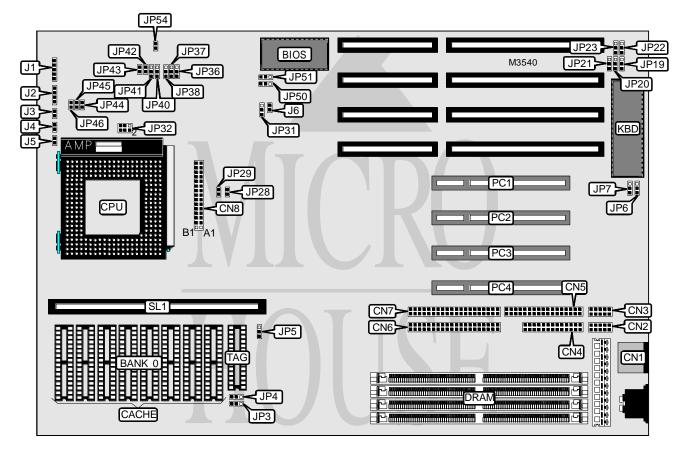
M TECHNOLOGY, INC. R525 PENTIUM PCI (VER.2)

Processor Pentium **Processor Speed** 75/90/100/120/133/150MHz Chip Set Intel **Video Chip Set** None **Maximum Onboard Memory** 128MB (EDO supported) **Maximum Video Memory** None Cache 256/512KB BIOS Award Dimensions 330mm x 218mm I/O Options 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VRM connector, cache slot **NPU Options** None



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M TECHNOLOGY, INC. R 5 2 5 PENTIUM PCI (VER.2)

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CONNECTIONS				
Purpose	Location	Purpose	Location	
PS/2 mouse port	CN1	Power LED & keylock	J1	
Serial port 1	CN2	Speaker	J2	
Serial port 2	CN3	Turbo switch	J3	
Parallel port	CN4	Turbo LED	J4	
Floppy drive interface	CN5	Reset switch	J5	
IDE interface 1	CN6	Green PC connector	JG	
IDE interface 2	CN7	32-bit PCI slots	PC1 - PC4	
VRM connector	CN8	Cache slot	SL1	

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
Pipeline disabled	JP42	Open		
Pipeline enabled	JP42	Closed		
í Cache type select write back	JP43	Open		
Cache type select write through	JP43	Closed		
í CMOS memory normal operation	JP54	Open		
CMOS memory clear	JP54	Closed		

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

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M TECHNOLOGY, INC. R525 PENTIUM PCI (VER.2)

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CACHE CONFIGURATION				
Size Bank 0 TAG SL1				
256KB (A)	(8) 32K x 8	(1) 8K/16K/32K x 8	Not installed	
256KB (B)	None	None	256KB module installed	
512KB (A)	(8) 64K x 8	(1) 16K/32K x 8	Not installed	
512KB (B)	None	None	512KB module installed	

CACHE JUMPER CONFIGURATION					
Size JP5 JP36 JP37					
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed		
512KB Pins 1 & 2 closed Pins 1 & 2 closed Pins 2 & 3 closed					
Note: These settings are only valid if standard cache is installed.					

CACHE CONFIGURATION			
Type JP38			
Standard cache	Pins 1 & 2 closed		
Pipeline burst cache	Pins 2 & 3 closed		

CACHE VOLTAGE CONFIGURATION				
Voltage JP3 JP4				
Mixed voltage	Pins 1 & 2 closed	Pins 1 & 2 closed		
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed		

CPU SPEED SELECTION					
Speed	JP28	JP29	JP31	JP40	JP41
75MHz	Open	Open	1&2	1&2	1&2
90MHz	Open	Closed	2&3	1&2	1&2
100MHz	Closed	Closed	2 & 3	1&2	1&2
120MHz	Open	Closed	2&3	1&2	2&3
133MHz	Closed	Closed	2&3	1&2	2&3
150MHz	Open	Closed	2 & 3	2 & 3	2 & 3
Note: Pins desig	nated should be in t	ne closed position.			

CPU TYPE SELECTION				
Туре	CN8	JP32		
P54CX	Pins A6 & A7, B6 & B7 closed	Open		
P55C 2.5v	Open	Pins 1 & 2, 3 & 4, 5 & 6 closed		
P55C 2.5v on VRM	VRM installed	Open		
P55CT	VRM installed	Open		

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M TECHNOLOGY, INC. R 5 2 5 PENTIUM PCI (VER.2)

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CPU VOLTAGE SELECTION				
Voltage JP44 JP45 JP46				
3.3v	Closed	Open	Open	
3.45v - 3.6v	Open	Closed	Open	

DMA CHANNEL SELECTION				
Channel JP6 JP7				
1	Pins 2 & 3 closed	Pins 2 & 3 closed		
í 3	Pins 1 & 2 closed	Pins 1 & 2 closed		

		PS/2 MOUS	E SELECTION			
Setting	JP19	JP20	JP21	JP22	JP23	
Disabled	2&3	2&3	2 & 3	2&3	2&3	
Enabled	1&2	1 & 2	1 & 2	1 & 2	1&2	
Note: Pins desig						

FLASH BIOS SELECTION		
Setting	JP50	JP51
Normal mode	Pins 2 & 3 closed	Pins 2 & 3 closed
Programming mode	Pins 1 & 2 closed	Pins 1 & 2 closed