Processor Pentium **Processor Speed** 60/66MHz Unidentified Chip Set **Video Chip Set** None **Maximum Onboard Memory** 128MB **Maximum Video Memory** None

Cache 64/128/256/512/1024KB

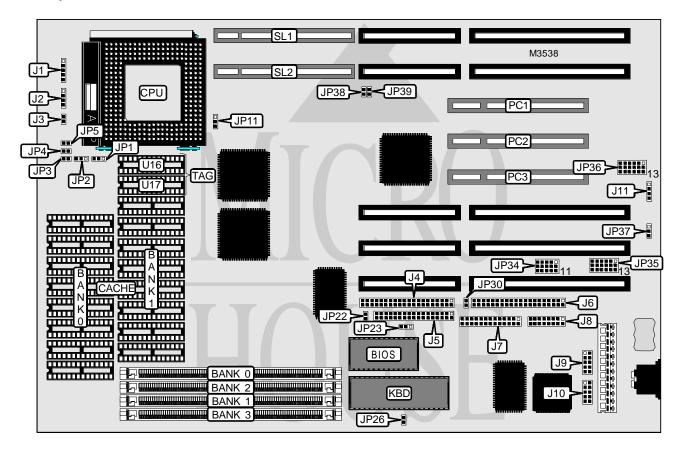
BIOS Phoenix

Dimensions 330mm x 218mm

I/O Options 32-bit PCI slots (3), floppy drive interface, game interface, IDE interfaces (2),

parallel port, serial ports (2)

NPU Options None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Serial port 2	J9
Speaker	J2	Serial port 1	J10
Reset switch	J3	External battery	J11
IDE interface	J4	ISA IDE interface LED	JP22
Floppy drive interface	J5	PCI IDE interface LED	JP30
IDE interface	J6	32-bit PCI slots	PC1 - PC3
Parallel port	J7	32-bit VESA local bus slots	SL1 & SL2
Game interface	J8		

USER CONFIGURABLE SETTINGS			
Function	Label	Position	
í BIOS type select EPROM	JP23	Pins 2 & 3 closed	
BIOS type select flash	JP23	Pins 1 & 2 closed	
í Monitor type select color	JP26	Closed	
Monitor type select monochrome	JP26	Open	
í A, B normal mode	JP34	Pins 1 & 6 closed	
A, B change mode	JP34	Pins 6 & 11 closed	
í Floppy drive address select 3F0h - 3F7h	JP34	Pins 7 & 12 closed	
Floppy drive address select 370h - 377h	JP34	Pins 2 & 7 closed	
í IDE interface enabled	JP34	Pins 3 & 8 closed	
IDE interface disabled	JP34	Pins 8 & 13 closed	
í IDE address select 3F8h, 3F7h, 1F0b - 1F7h	JP34	Pins 9 & 14 closed	
IDE address select 378h, 377h, 170h - 177h	JP34	Pins 4 & 9 closed	
í Floppy drive interface enabled	JP34	Pins 5 & 10 closed	
Floppy drive interface disabled	JP34	Pins 10 & 15 closed	
í PCI IDE address select default address	JP36	Pins 1 & 7 closed	
PCI IDE address select relocatable address	JP36	Pins 7 & 13 closed	
í PCI trigger select edge trigger	JP36	Pins 8 & 14 closed	
PCI trigger select level trigger	JP36	Pins 2 & 8 closed	
í PCI clock speed select 33MHz	JP36	Pins 3 & 9 closed	
PCI clock speed select 25MHz	JP36	Pins 9 & 15 closed	
í Prefetch off	JP36	Pins 12 & 18 closed	
Prefetch on	JP36	Pins 6 & 12 closed	
í CMOS memory normal operation	JP37	Pins 2 & 3 closed	
CMOS memory clear	JP37	Pins 1 & 2 closed	

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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 256K x 36	(1) 256K x 36	None	None
4MB	(1) 512K x 36	(1) 512K x 36	None	None
4MB	(1) 256K x 36			
6MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	(1) 512K x 36
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	(1) 512K x 36			
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	None	None
16MB	(1) 1M x 36			
18MB	(1) 256K x 36	(1) 256K x 36	(1) 2M x 36	(1) 2M x 36
20MB	(1) 512K x 36	(1) 512K x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 2M x 36			
34MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	(1) 4M x 36
36MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 8M x 36	(1) 4M x 36	None	None
48MB	(1) 2M x 36	(1) 2M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 4M x 36			
66MB	(1) 256K x 36	(1) 256K x 36	(1) 8M x 36	(1) 8M x 36
68MB	(1) 512K x 36	(1) 512K x 36	(1) 8M x 36	(1) 8M x 36
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
80MB	(1) 2M x 36	(1) 2M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 8M x 36			

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	U16	U17
64KB	(8) 8K x 8	None	None	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	None	(1) 8K x 8
256KB	(8) 32K x 8	None	None	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 8K x 8	(1) 8K x 8
1MB	(4) 128K x 8	None	None	(1) 32K x 8

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CACHE JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP5	JP11
64KB	1 & 2	1 & 2	Open	Open	Open	1 & 2
128KB	2 & 3	2 & 3	Closed	Open	Open	2 & 3
256KB	1 & 2	1 & 2	Closed	Closed	Open	1 & 2
512KB	2 & 3	2 & 3	Closed	Closed	Closed	2 & 3
1MB	2 & 3	2 & 3	Closed	Closed	Closed	2 & 3
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION			
Speed JP38 JP39			
60MHz	Closed	Open	
66MHz	Open	Open	

PARALLEL PORT ADDRESS SELECTION		
Setting	JP35	
LPT1 (378h)	Pins 6 & 12, 11 & 17 closed	
LPT2 (278h)	Pins 5 & 11, 12 & 18 closed	
LPT3 (3BCh)	Pins 5 & 11, 6 & 12 closed	
Disabled	Pins 11 & 17, 12 & 18 closed	

SERIAL PORT 1 ADDRESS SELECTION		
Setting	JP35	
í COM1 (3F8h)	Pins 1 & 7, 8 & 14 closed	
COM3 (3E8h)	Pins 2 & 8, 7 & 13 closed	
COM4 (2E8h)	Pins 1 & 7, 2 & 8 closed	
Disabled	Pins 7 & 13, 8 & 14 closed	

SERIAL PORT 2 ADDRESS SELECTION		
Setting	JP35	
í COM2 (2F8h)	Pins 3 & 9, 10 & 16 closed	
COM3 (3E8h)	Pins 3 & 9, 4 & 10 closed	
COM4 (2E8h)	Pins 4 & 10, 9 & 15 closed	
Disabled	Pins 9 & 15, 10 & 18 closed	

PCI MODE SELECTION		
Mode	JP36	
í O	Pins 4 & 10, 5 & 11 closed	
1	Pins 4 & 10, 5 & 11 closed	
2	Pins 4 & 10, 5 & 11 closed	
3	Pins 4 & 10, 5 & 11 closed	