M TECHNOLOGY, INC.
R527 PENTIUM PCI/ISA MTI-R527

Processor	CX M1/Pentium
Processor Speed	75/80/90/100/120/133/150/166MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), feature connector, VGA interface
NPU Options	None
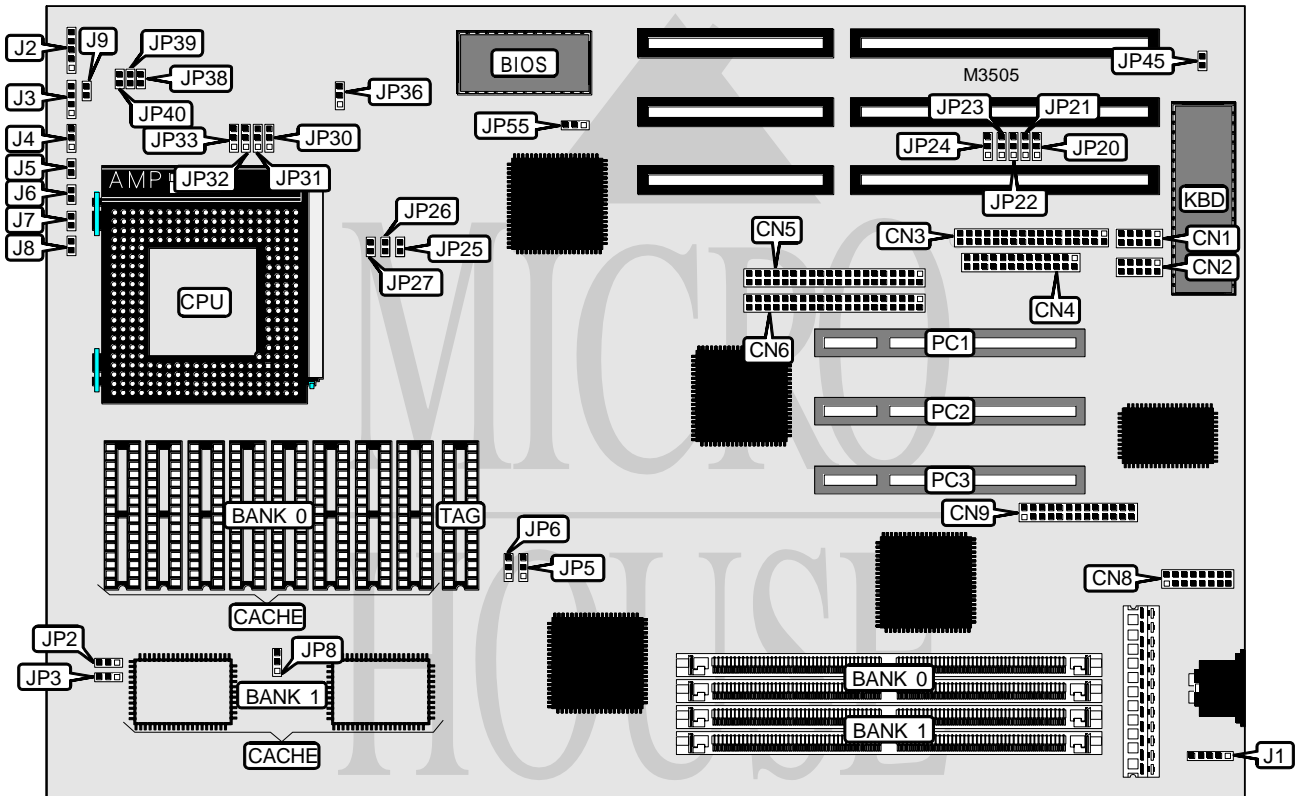
M TECHNOLOGY, INC.
R527 PENTIUM PCI/ISA MTI-R527

CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Power LED & keylock	J2
Serial port 1	CN2	Speaker	J3
Floppy drive interface	CN3	Turbo switch	J4
Parallel port	CN4	Turbo LED	J5
IDE interface 2	CN5	Reset switch	J6
IDE interface 1	CN6	IDE interface LED 2	J7
VGA interface	CN8	IDE interface LED 1	J8
Feature connector	CN9	Green PC connector	J9
PS/2 mouse interface	J1	32-bit PCI slots	PC1 - PC3

USER CONFIGURABLE SETTINGS		
Function	**Label**	Position
í Pipeline address enabled (only if burst cache is installed)	JP32	Pins 2 & 3 closed
Pipeline address disabled	JP32	Pins 1 & 2 closed
í Cache type select write back	JP33	Pins 1 & 2 closed
Cache type select write through	JP33	Pins 2 & 3 closed
í CMOS memory normal operation	JP36	Pins 1 & 2 closed
CMOS memory clear	JP36	Pins 2 & 3 closed
Monitor type select CGA	JP45	Closed
Monitor type select EGA/VGA/monochrome	JP45	Open
í Flash BIOS mode select normal operation	JP55	Pins 2 & 3 closed
Flash BIOS mode select programming mode	JP55	Pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 1M x 36	None
4MB	(2) 1M x 36	(2) 1M x 36
8MB	(2) 1M x 36	None
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 8M x 36	(2) 1M x 36

M TECHNOLOGY, INC.
R 5 2 7   P E N T I U M   P C I / I S A   M T I - R 5 2 7

DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
264MB	(2) 32M x 36	(2) 1M x 36
272MB	(2) 32M x 36	(2) 2M x 36
288MB	(2) 32M x 36	(2) 4M x 36
320MB	(2) 32M x 36	(2) 8M x 36
384MB	(2) 16M x 36	(2) 32M x 36
384MB	(2) 32M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.		

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB (A)	(8) 32K x 8	None	(1) 8K/16K/32K x 8
256KB (B)	None	(2) 32K x 32	(1) 8K/16K/32K x 8
512KB	(8) 64K x 8	None	(1) 16K/32K x 8
1MB	(8) 128K x 8	None	(1) 32K x 8
Note: Board will be installed with either standard or burst cache. Both types will not be present.			

CACHE JUMPER CONFIGURATION		
Size	JP5	JP6
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed	Pins 1 & 2 closed
1MB	Pins 2 & 3 closed	Pins 2 & 3 closed

CACHE VOLTAGE CONFIGURATION		
Voltage	JP2	JP3
Mixed voltage	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed

M TECHNOLOGY, INC.
R 5 2 7   P E N T I U M   P C I / I S A   M T I - R 5 2 7

CPU SPEED SELECTION (CYRIX)						
CPU speed	**Clock speed**	**JP25**	**JP26**	**JP27**	**JP30**	**JP31**
80MHz	40MHz	Closed	Open	Open	2 & 3	1 & 2
100MHz	50MHz	Closed	Closed	Closed	2 & 3	1 & 2
120MHz	60MHz	Open	Closed	Open	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (INTEL)						
CPU speed	**Clock speed**	**JP25**	**JP26**	**JP27**	**JP30**	**JP31**
75MHz	50MHz	Open	Closed	Closed	1 & 2	1 & 2
90MHz	60MHz	Open	Closed	Open	1 & 2	1 & 2
100MHz	66MHz	Open	Open	Closed	1 & 2	1 & 2
120MHz	60MHz	Open	Closed	Open	2 & 3	1 & 2
133MHz	66MHz	Open	Open	Closed	2 & 3	1 & 2
150MHz	50MHz	Open	Closed	Closed	1 & 2	2 & 3
150MHz	60MHz	Open	Closed	Open	2 & 3	2 & 3
166MHz	66MHz	Open	Open	Closed	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

CPU TYPE CONFIGURATION	
Type	JP8
Cyrix	pins 1 & 2 closed
Intel	pins 2 & 3 closed
Note: This setting only valid if burst cache is installed.	

CPU VOLTAGE SELECTION			
Voltage	JP38	JP39	JP40
3.3v	Closed	Open	Open
3.45v - 3.6v	Open	Closed	Open

PS/2 MOUSE CONFIGURATION					
Setting	JP20	JP21	JP22	JP23	JP24
í Enabled	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Disabled	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.					