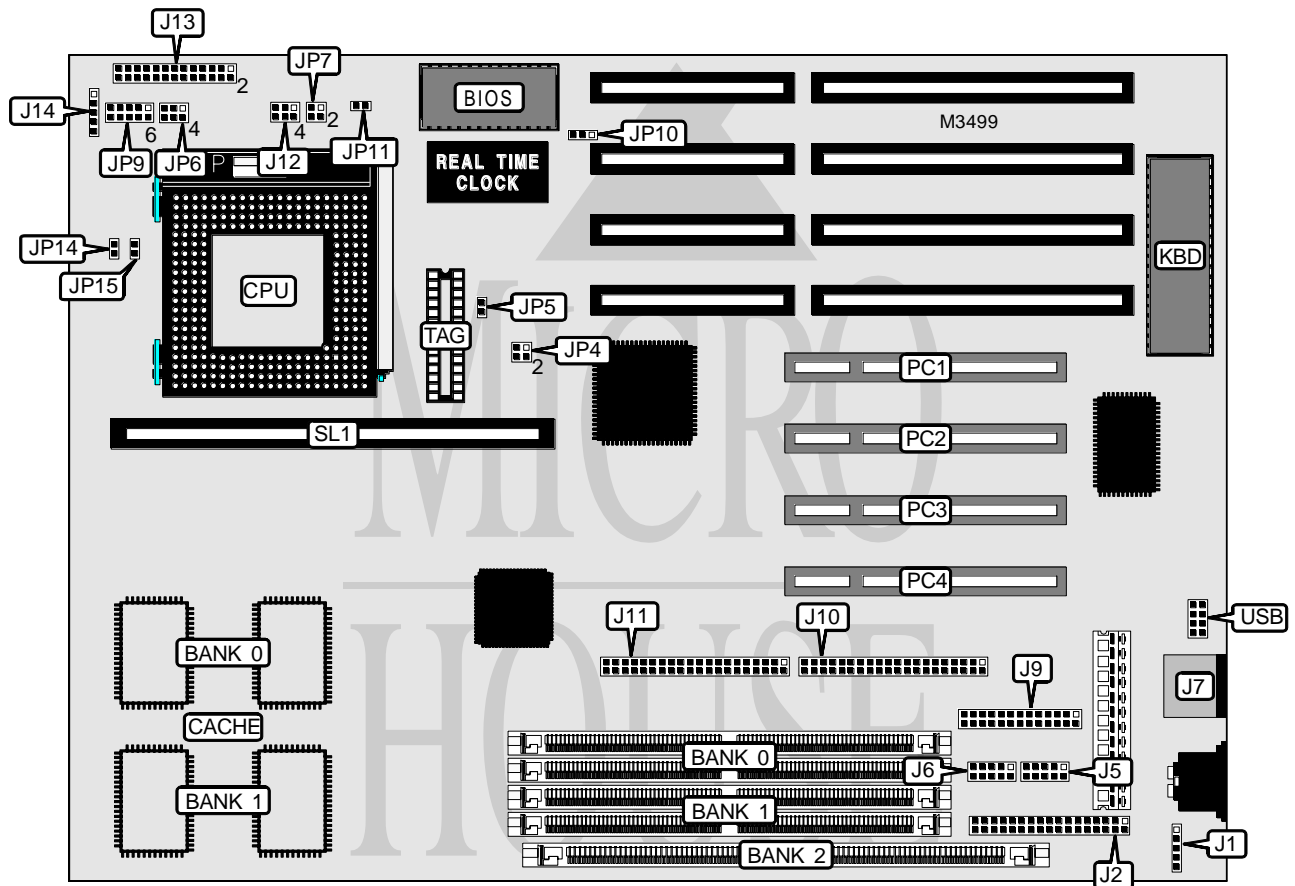


BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-8500 TUC-A (VER. 1)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/83/90/100/110/120/133/150/166/180/200MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI
Dimensions	290mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 mouse interface, serial ports (2), cache slot, IR connector, USB connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	J1	Power LED & keylock	J13 pins 5 - 9
Floppy drive interface	J2	Turbo LED	J13 pins 10 & 11
Serial port 1	J5	Reset switch	J13 pins 12 & 13
Serial port 2	J6	Green PC connector	J13 pins 17 & 18
PS/2 mouse port	J7	IDE interface LED	J13 pins 20 & 21
Parallel port	J9	+5v ground	J13 pins 25 & 26
IDE interface 1	J10	IR connector (optional)	J14
IDE interface 2	J11	32-bit PCI slots	PC1 - PC4
Chassis fan power	J12	Cache slot	SL1
Speaker	J13 pins 1 - 4	USB connector	USB

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Flash BIOS voltage select 12v	JP10	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP10	Pins 2 & 3 closed
EPROM or 0v flash BIOS used	JP10	Open
CMOS memory normal operation	JP11	Open
CMOS memory clear	JP11	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
32MB	(2) 4M x 36	None
64MB	(2) 8M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Bank 2 uses DIMM modules. The chip size and configuration is unidentified.

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CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	SL1
256KB	(2) 32K x 32	None	(1) 8K x 8	Not installed
512KB (A)	(2) 32K x 32	(2) 32K x 32	(1) 32K x 8	Not installed
512KB (B)	(2) 32K x 32	None	(1) 8K x 8	256KB module installed

CACHE JUMPER CONFIGURATION	
Size	JP5
256KB	Open
512KB (A)	Closed
512KB (B)	Closed

CPU SPEED SELECTION (CYRIX)				
CPU speed	Clock speed	Multiplier	JP4	JP7
100MHz	50MHz	2x	Pins 1 & 2, 3 & 4 closed	Open
110MHz	55MHz	2x	Open	Open
120MHz	60MHz	2x	Pins 3 & 4 closed	Open
133MHz	66MHz	2x	Pins 1 & 2 closed	Open

CPU SPEED SELECTION (AMD)				
CPU speed	Clock speed	Multiplier	JP4	JP7
66MHz	50MHz	1x	Pins 1 & 2 closed	Pins 1 & 2 closed
75MHz	60MHz	1.5x	Pins 1 & 2, 3 & 4 closed	Open
83MHz	66MHz	1.5x	Open	Open
90MHz	60MHz	1.5x	Pins 3 & 4 closed	Open
100MHz	66MHz	1.5x	Pins 1 & 2 closed	Open

CPU SPEED SELECTION (PENTIUM)				
CPU speed	Clock speed	Multiplier	JP4	JP7
75MHz	50MHz	1.5x	Pins 1 & 2, 3 & 4 closed	Open
90MHz	60MHz	1.5x	Pins 3 & 4 closed	Open
100MHz	66MHz	1.5x	Pins 1 & 2 closed	Open
120MHz	60MHz	2x	Pins 3 & 4 closed	Pins 1 & 2 closed
133MHz	66MHz	2x	Pins 1 & 2 closed	Pins 1 & 2 closed
150MHz	60MHz	2.5x	Pins 3 & 4 closed	Pins 1 & 2, 3 & 4 closed
166MHz	66MHz	2.5x	Pins 1 & 2 closed	Pins 1 & 2, 3 & 4 closed
180MHz	60MHz	3x	Pins 3 & 4 closed	Pins 3 & 4 closed
200MHz	66MHz	3x	Pins 1 & 2 closed	Pins 3 & 4 closed

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CPU TYPE SELECTION			
Type	JP6	JP9	JP15
Cyrix	1 & 2, 4 & 5	2 & 3, 4 & 5, 7 & 8, 9 & 10	Open
AMD	1 & 2, 4 & 5	2 & 3, 4 & 5, 7 & 8, 9 & 10	Open
Intel P54C/CQS/CT	1 & 2, 4 & 5	2 & 3, 4 & 5, 7 & 8, 9 & 10	Open
Intel P55C/CT	2 & 3, 5 & 6	1 & 2, 3 & 4, 6 & 7, 8 & 9	Closed

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION	
Voltage	JP14
3.4v	Closed
3.5	Open