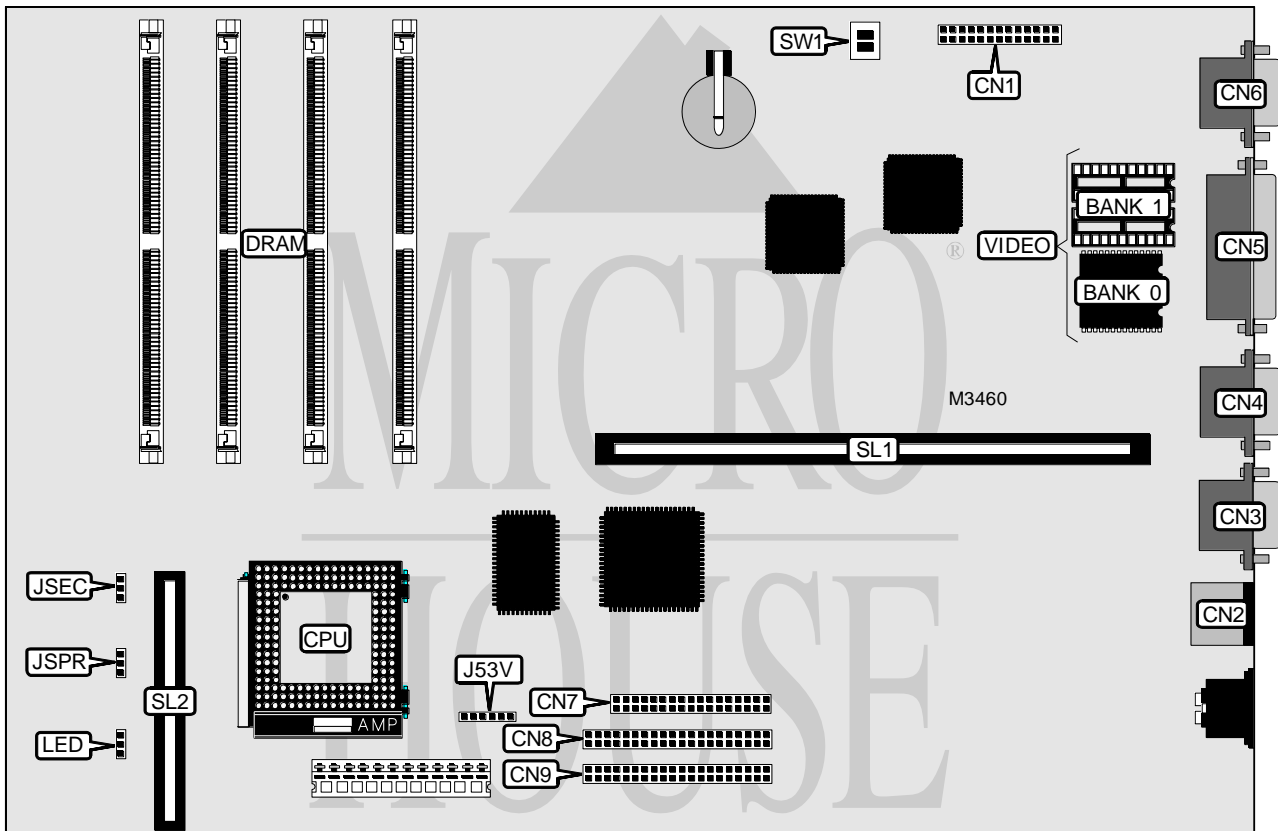


AT & T, INC.

AT & T, INC. GLOBALYST 550

AT & T, INC. GLOBALYST 575

| | |
|-------------------------------|--------------------------------------------------------------------------------------------------------------------------------|
| Processor | SL80486SX/SL80486SX2/SL80486DX2/SL80486DX4/P24C/ 486Overdrive |
| Processor Speed | 33/50(internal)/66(internal)100(internal)MHz |
| Chip Set | Unidentified |
| Video Chip Set | S3 |
| Maximum Onboard Memory | 128MB |
| Maximum Video Memory | 2MB |
| Cache | 256KB |
| BIOS | IBM |
| Dimensions | 330mm x 218mm |
| I/O Options | Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), VGA port, riser slot, cache slot |
| NPU Options | None |



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| CONNECTIONS | | | |
|------------------------|----------|-----------------------------|----------|
| Purpose | Location | Purpose | Location |
| PS/2 mouse port | CN2 | IDE interface 1 | CN9 |
| Serial port | CN3 | Front panel security switch | JSEC |
| Serial port | CN4 | Speaker | JSPR |
| Parallel port | CN5 | Front panel LEDs | LED |
| VGA port | CN6 | Riser slot | SL1 |
| Floppy drive interface | CN7 | Cache slot | SL2 |
| IDE interface 2 | CN8 | | |

| USER CONFIGURABLE SETTINGS | | |
|--------------------------------|-------|--------------|
| Function | Label | Position |
| Jumper information unavailable | CN1 | Unidentified |
| Monitor type select color | SW1/2 | Off |
| Monitor type select monochrome | SW1/2 | On |

Note: If a VGA processor is enabled, SW1/2 has no function. If the VGA controller is disabled, and a CGA or MDA controller is installed, set the switch to the above setting.

| DRAM CONFIGURATION | | | | |
|--------------------|-------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 4MB | (1) 1M x 36 | None | None | None |
| 8MB | (1) 1M x 36 | (1) 1M x 36 | None | None |
| 8MB | (1) 2M x 36 | None | None | None |
| 12MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | None |
| 12MB | (1) 1M x 36 | (1) 2M x 36 | None | None |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 2M x 36 | None |
| 16MB | (1) 2M x 36 | (1) 2M x 36 | None | None |
| 16MB | (1) 4M x 36 | None | None | None |
| 20MB | (1) 1M x 36 | (1) 4M x 36 | None | None |
| 20MB | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 | None |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 24MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | None |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | None |
| 28MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 1M x 36 |
| 28MB | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 32MB | (1) 2M x 36 | (1) 2M x 36 | (1) 4M x 36 | None |
| 32MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 |

Note: The location of banks 0, 1, 2, & 3 are unidentified. This table is for the Globalyst 550 system and early revisions of Globalyst 575 systems.

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| DRAM CONFIGURATION (CON'T) | | | | |
|----------------------------|-------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 32MB | (1) 4M x 36 | (1) 4M x 36 | None | None |
| 36MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | None |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 48MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | None |
| 52MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |

Note: The location of banks 0, 1, 2, & 3 are unidentified. This table is for the Globalyst 550 system and early revisions of Globalyst 575 systems.

| DRAM CONFIGURATION | | | | |
|--------------------|-------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 4MB | (1) 1M x 36 | None | None | None |
| 8MB | (1) 1M x 36 | (1) 1M x 36 | None | None |
| 8MB | (1) 2M x 36 | None | None | None |
| 12MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | None |
| 12MB | (1) 1M x 36 | (1) 2M x 36 | None | None |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 2M x 36 | None |
| 16MB | (1) 2M x 36 | (1) 2M x 36 | None | None |
| 16MB | (1) 4M x 36 | None | None | None |
| 20MB | (1) 1M x 36 | (1) 4M x 36 | None | None |
| 20MB | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 | None |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 24MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | None |
| 24MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | None |
| 28MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 1M x 36 |
| 28MB | (1) 1M x 36 | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 32MB | (1) 2M x 36 | (1) 2M x 36 | (1) 4M x 36 | None |
| 32MB | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 | (1) 2M x 36 |
| 32MB | (1) 4M x 36 | (1) 4M x 36 | None | None |
| 32MB | (1) 8M x 36 | None | None | None |
| 36MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | None |
| 36MB | (1) 1M x 36 | (1) 8M x 36 | None | None |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 48MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | None |
| 48MB | (1) 4M x 36 | (1) 8M x 36 | None | None |
| 52MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | (1) 8M x 36 | (1) 8M x 36 | None | None |
| 68MB | (1) 1M x 36 | (1) 8M x 36 | (1) 8M x 36 | None |

Note: The location of banks 0, 1, 2, & 3 are unidentified. This table is for the Globalyst 575 system revisions released after the end of 1994..

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| DRAM CONFIGURATION (CON'T) | | | | |
|----------------------------|-------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 80MB | (1) 4M x 36 | (1) 8M x 36 | (1) 8M x 36 | None |
| 96MB | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 | None |
| 100MB | (1) 1M x 36 | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 |
| 112MB | (1) 4M x 36 | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 |
| 128MB | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 | (1) 8M x 36 |

Note: The location of banks 0, 1, 2, & 3 are unidentified. This table is for the Globalyst 575 system revisions released after the end of 1994..

| CACHE CONFIGURATION | |
|---------------------|------------------------|
| Size | SL2 |
| None | Not installed |
| 256KB | 256KB module installed |

| VIDEO MEMORY CONFIGURATION | | |
|----------------------------|--------|--------|
| Size | Bank 0 | Bank 1 |
| 1MB | 1MB | None |
| 2MB | 1MB | 1MB |

Note: Bank 0 is factory installed and is not configurable. The size of the upgrade ships are unidentified.

| CPU SPEED SELECTION | |
|---------------------|-------|
| Speed | SW1/1 |
| 33MHz | Off |
| 50iMHz | On |
| 66iMHz | Off |
| 100iMHz | Off |

| CPU TYPE SELECTION | |
|--------------------|-------------------|
| Type | J53V |
| 1 | Pins 1 & 2 closed |
| 2 | Pins 5 & 6 closed |

Note: This jumper set the 3.3v for the P24C CPU. Type 1 & 2 are unidentified.