JC INFORMATION SYSTEMS CORPORATION HIGH PERFORMANCE 486 SX/DX (MODEL 2270)

Processor 80486SX/80486DX/80486DX2

Processor Speed 25/33/40/50/50(internal)/66(internal)MHz

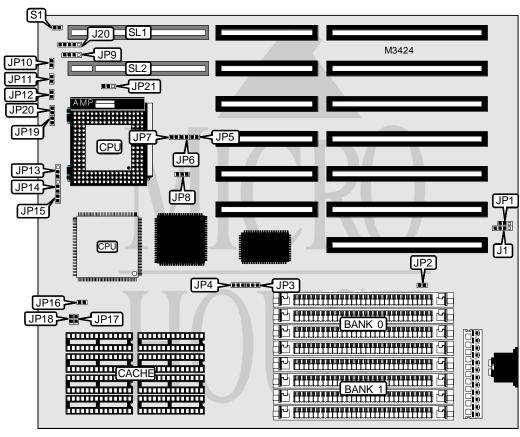
Unidentified Chip Set

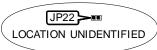
Video Chip Set None **Maximum Onboard Memory** 64MB **Maximum Video Memory** None 64/256KB Cache **BIOS** AMI

Dimensions 254mm x 218mm

I/O Options 32-bit VESA local bus slots (2)

NPU Options None





CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J1	Turbo LED	JP20	
Power LED & keylock	J20	Reset switch	S1	
Speaker	JP9	32-bit VESA local bus slots	SL1 & SL2	
Turbo switch	JP19			



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USER CONFIGURABLE SETTINGS				
Function	Label	Position		
í CMOS memory normal operation	JP1	Pins 2 & 3 closed		
CMOS memory clear	JP1	Pins 1 & 2 closed		
External battery installed at J1	JP1	Open		
Monitor type select monochrome/EGA	JP2	Open		
Monitor type select CGA	JP2	Closed		
í Factory configured - do not alter	JP10	Closed		
Jumper information unavailable	JP11	Unidentified		
Jumper information unavailable	JP12	Unidentified		
í Factory configured - do not alter	JP21	Open		
í Factory configured - do not alter	JP22	Open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	None		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	None		
5MB	(4) 1M x 9	(4) 256K x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	None		
20MB	(4) 4M x 9	(4) 1M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		
64MB	(4) 16M x 9	None		

CACHE CONFIGURATION				
Size	Bank 0	Bank 1		
64KB	(4) 8K x 8	(4) 8K x 8		
256KB	(4) 32K x 8	(4) 32K x 8		
Note: The location of banks 0 & 1 are unidentified.				

CACHE JUMPER CONFIGURATION						
Size JP3 JP4 JP17 JP18						
64KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Open	Open		
128KB	Pins 1 & 2 closed	Pins 1 & 2 closed	Closed	Open		
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed	Closed	Closed		

CPU SPEED SELECTION				
Speed	JP5	JP6	JP7	
25MHz	Open	Open	Closed	
33MHz	Closed	Open	Open	
40MHz	Closed	Closed	Open	
50iMHz	Open	Open	Closed	
50MHz	Open	Closed	Open	
66iMHz	Closed	Open	Open	

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CPU TYPE SELECTION					
Туре	JP8	JP13	JP14	JP15	JP16
80486SX	2 & 3	Open	2 & 3	Open	Open
80486DX	2 & 3	1 & 2	1 & 2	Closed	Open

Note: Pins designated should be in the closed position. If PQFP is installed, JP8 must be set to 1 & 2 and JP16 must be closed if an 80486DX or 80486DX2 is installed