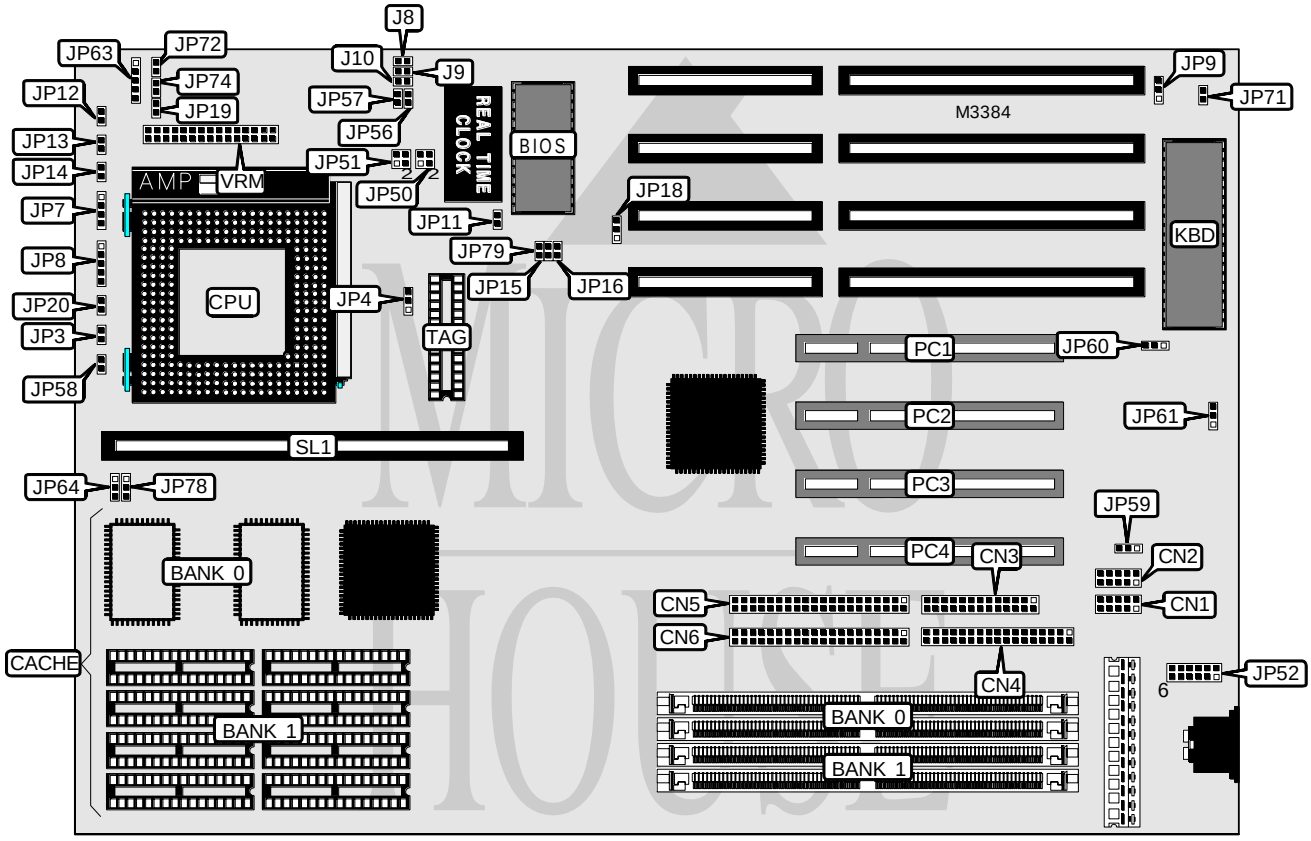
SHUTTLE COMPUTER INTERNATIONAL, INC.
H O T - 5 4 1 (V E R . 2 . 2)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/80/90/100/120/133/150/180MHz
Chip Set	Intel
Maximum Onboard Memory	128MB (EDO supported)
Cache	256/512KB
BIOS	AMI/Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VRM connector, cache slot, IR connector
NPU Options	None



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CONNECTIONS			
Function	**Label**	**Function**	**Label**
Serial port 1	CN1	Turbo LED	JP14
Serial port 2	CN2	Green PC connector	JP19
Parallel port	CN3	IDE interface LED	JP20
Floppy drive interface	CN4	PS/2 mouse interface	JP52
IDE interface 2	CN5	IR connector	JP63
IDE interface 1	CN6	Green PC LED	JP74
Speaker	JP7	32-bit PCI slots	PC1 - PC4
Power LED & keylock	JP8	VRM connector	VRM
Reset switch	JP12	Cache slot	SL1
Turbo switch	JP13		

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Flash BIOS voltage select 5v	JP9	Open
Flash BIOS voltage select 12v	JP9	Pins 2 & 3 closed
í CMOS memory normal operation	JP11	Open
CMOS memory clear	JP11	Closed
í Monitor type select color	JP71	Closed
Monitor type select monochrome	JP71	Open
í Password normal operation	JP72	Open
Password clear	JP72	Closed

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
8MB	None	(2) 1M x 36
16MB	(2) 2M x 36	None
16MB	None	(2) 2M x 36
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	None	(2) 4M x 36
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 1M x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	None	(2) 8M x 36

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DRAM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 1M x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 4M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**TAG**	**SL1**
256KB (A)	None	(8) 32K x 8	(1) 32K x 8	Not installed
256KB (B)	None	None	None	Installed
256KB (C)	(2) 32K x 32	None	(1) 32K x 8	Not installed
512KB (A)	None	(8) 64K x 8	(1) 32K x 8	Not installed
512KB (B)	(2) 32K x 32	None	None	Installed

CACHE JUMPER CONFIGURATION		
Size	**JP4**	**JP64**
256KB (A)	Pins 2 & 3 closed	N/A
256KB (B)	Pins 2 & 3 closed	Pins 2 & 3 closed
256KB (C)	Pins 2 & 3 closed	Pins 2 & 3 closed
512KB (A)	Pins 1 & 2 closed	N/A
512KB (B)	Pins 1 & 2 closed	Pins 1 & 2 closed

CACHE VOLTAGE CONFIGURATION	
Setting	**JP78**
Mixed mode	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed

CPU SPEED SELECTION			
Setting	**JP15**	**JP16**	**JP79**
75MHz	Open	Open	Open
80MHz	Open	Closed	Closed
90MHz	Closed	Closed	Open
100MHz	Closed	Open	Open
120MHz	Closed	Closed	Open
133MHz	Closed	Open	Open
150MHz	Open	Open	Open
180MHz	Closed	Closed	Open

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AT BUS CLOCK SPEED SELECTION		
System clock	**AT bus clock**	**JP18**
40MHz	5MHz	Pins 2 & 3 closed
40MHz	6.67MHz	Pins 1 & 2 closed
50MHz	6.25MHz	Pins 2 & 3 closed
50MHz	8.33MHz	Pins 1 & 2 closed
60MHz	7.5MHz	Pins 2 & 3 closed
60MHz	10MHz	Pins 1 & 2 closed
66MHz	8.25MHz	Pins 2 & 3 closed
66MHz	11MHz	Pins 1 & 2 closed

CPU MULTIPLIER SELECTION		
Setting	**JP3**	**JP58**
1.5x	Open	Open
2x	Closed	Open
2.5x	Closed	Closed
3x	Open	Closed
Note: If CX M1 is installed, JP3 & JP58 must be open.		

CPU VOLTAGE SELECTION			
Setting	**J8**	**J9**	**J10**
3.3v (STD/VR)	Closed	Open	Open
3.4v (VR/VRE)	Open	Closed	Open
3.6v (VRE)	Open	Open	Closed

VRM CONFIGURATION				
Setting	**JP50**	**JP51**	**JP56**	**JP57**
On board regulator installed	1 & 3, 2 & 4	Open	Closed	Closed
Add on VRM installed at VRM	Open	1 & 3, 2 & 4	Open	Open
On board regulator and add on VRM installed at VRM	Open	Open	Closed	Closed
Note: Pins designated should be in the closed position.				

DMA CHANNEL SELECTION			
Channel	**JP59**	**JP60**	**JP61**
í 1	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
3	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

PS/2 MOUSE CONFIGURATION	
Setting	**JP52**
12-pin header	Pins 2, 3, 4, 5, 6, 8, 9, 10, 11, 12 closed
6-pin mini	Pins 1 - 6 closed