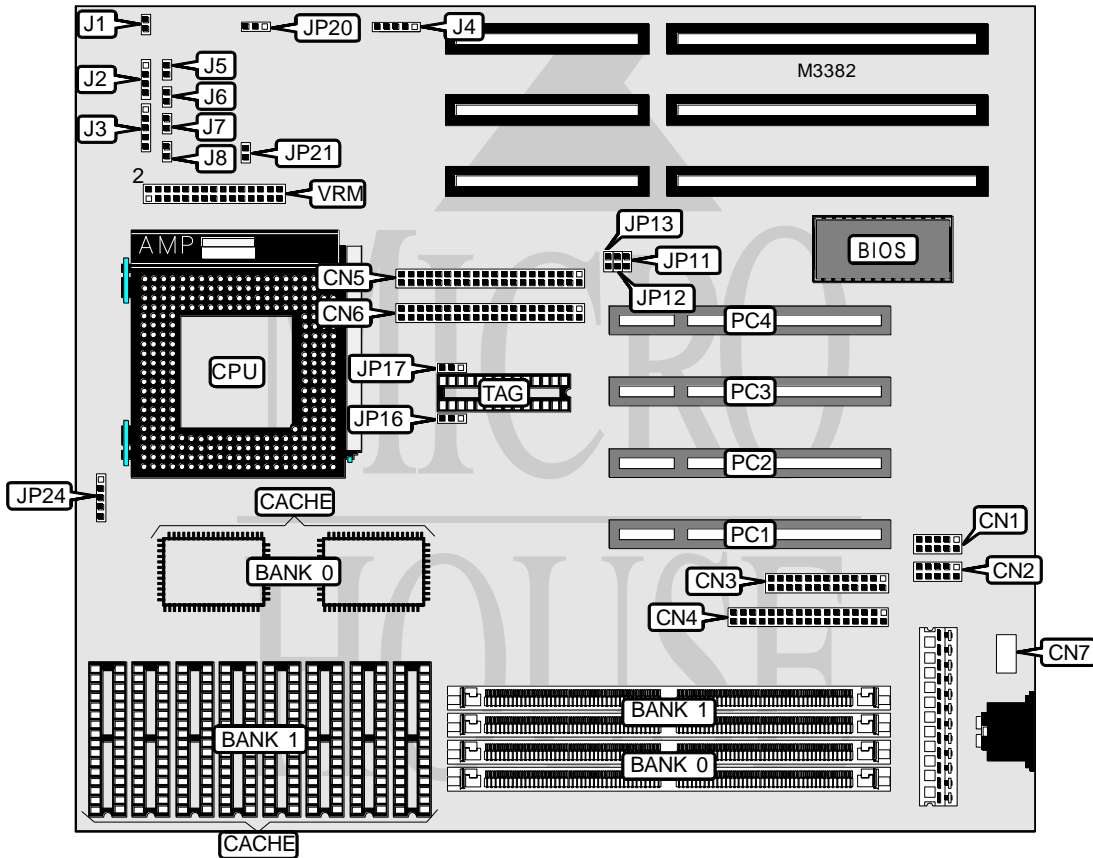


FREE COMPUTER TECHNOLOGY, INC.

586F56 (VER 2.0) 586F60 (VER 2.0)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512/1024KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, VRM connector
NPU Options	None



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FREE COMPUTER TECHNOLOGY, INC.
586F56 (VER 2.0) 586F60 (VER 2.0)

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Power LED & keylock	J3
Serial port 1	CN2	IR connector	J4
Parallel port	CN3	Reset switch	J5
Floppy drive interface	CN4	Turbo switch	J6
IDE interface 1	CN5	Turbo LED	J7
IDE interface 2	CN6	IDE interface LED	J8
PS/2 mouse interface	CN7	32-bit PCI slots	PC1 - PC4
Green PC connector	J1	VRM connector	VRM
Speaker	J2		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP20	Pins 1 & 2 closed
CMOS memory clear	JP20	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB *	(1) 1M x 36	None
8MB	(2) 1M x 36	None
16MB *	(1) 4M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB *	(1) 16M x 36	None
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36

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FREE COMPUTER TECHNOLOGY, INC.
586F56 (VER 2.0) 586F60 (VER 2.0)

... continued from previous page

DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36
136MB	(2) 1M x 36	(2) 16M x 36
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 2M x 36	(2) 16M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 4M x 36	(2) 16M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 8M x 36	(2) 16M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
256MB	(2) 32M x 36	None
264MB	(2) 1M x 36	(2) 32M x 36
264MB	(2) 32M x 36	(2) 1M x 36
272MB	(2) 2M x 36	(2) 32M x 36
272MB	(2) 32M x 36	(2) 2M x 36
288MB	(2) 4M x 36	(2) 32M x 36
288MB	(2) 32M x 36	(2) 4M x 36
320MB	(2) 8M x 36	(2) 32M x 36
320MB	(2) 32M x 36	(2) 8M x 36
384MB	(2) 32M x 36	(2) 16M x 36
384MB	(2) 16M x 36	(2) 32M x 36
512MB	(2) 32M x 36	(2) 32M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. * = These three configurations may be installed in SIMM 1 on Bank 0. SIMM 1 is not shown on the diagram.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB (A)	None	(8) 32K x 8	(1) 8K/16K/32K x 8
256KB (B)	(2) 32K x 32	None	None
512KB (A)	None	(8) 64K x 8	(1) 16K/32K x 8
512KB (B)	(2) 64K x 32	None	None
1MB (A)	None	(8) 128K x 8	(1) 32K x 8
1MB (B)	(2) 128K x 32	None	None

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FREE COMPUTER TECHNOLOGY, INC.
586F56 (VER 2.0) 586F60 (VER 2.0)

... continued from previous page

CACHE JUMPER CONFIGURATION		
Size	JP16	JP17
256KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB (A)	Pins 1 & 2 closed	Pins 2 & 3 closed
1MB (A)	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION						
CPU Speed	Clock Speed	Multiplier	JP11	JP12	JP13	JP24
75MHz	50MHz	1.5x	Closed	Open	Open	3 & 4
90MHz	60MHz	1.5x	Open	Closed	Open	3 & 4
100MHz	66MHz	1.5x	Closed	Open	Closed	3 & 4
100MHz	50MHz	2x	Closed	Open	Open	1 & 2, 3 & 4
120MHz	60MHz	2x	Open	Closed	Open	1 & 2, 3 & 4
133MHz	66MHz	2x	Closed	Open	Closed	1 & 2, 3 & 4
150MHz	60MHz	2.5x	Open	Closed	Open	1 & 2, 4 & 5
150MHz	50MHz	3x	Closed	Open	Open	4 & 5
166MHz	66MHz	2.5x	Closed	Open	Closed	1 & 2, 4 & 5
180MHz	60MHz	3x	Open	Closed	Open	4 & 5
200MHz	66MHz	3x	Closed	Open	Closed	4 & 5

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION	
Voltage	JP21
3.3v (STD/VR)	Open
3.45v - 3.6v (VRE)	Closed

CPU VOLTAGE SELECTION	
Type	VRM
P54C/P54CT/P54CTB (3.135v - 3.6v)	Pins 11 & 13, 12 & 14 closed
P54C/P54CT/P54CTB (3.4v - 3.6v) *	Pins 11 & 13, 12 & 14 closed
P55C (2.5v)	VRM installed

Note: * = JP21 is closed.