# SIEMENS NIXDORF INFORMATIONSSYSTEME AG
# S 2 6 3 6 1 - D 7 5 6 - X

| | |
|---|---|
| **Processor** | 80486SX/80487SX/80486DX/80486DX2 |
| **Processor Speed** | 25/33/50(internal)/66(internal)MHz |
| **Chip Set** | Unidentified |
| **Video Chip Set** | Unidentified |
| **Maximum Onboard Memory** | 32MB |
| **Maximum Video Memory** | 1MB |
| **Cache** | 64/128/256KB |
| **BIOS** | Unidentified |
| **Dimensions** | 330mm x 218mm |
| **I/O Options** | Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), video feature connector, VGA port, riser slot |
| **NPU Options** | None |



| CONNECTIONS | | | |
|---|---|---|---|
| **Purpose** | **Location** | **Purpose** | **Location** |
| Front panel connector | CN1 | Serial port 2 | J20 |
| Speaker | J13 | Parallel port | J21 |
| PS/2 mouse port | J15 | Video feature connector | J23 |
| IDE interface | J17 | VGA port | J24 |
| Floppy drive interface | J18 | External battery | JP8 |
| Serial port 1 | J19 | Riser slot | SL1 |
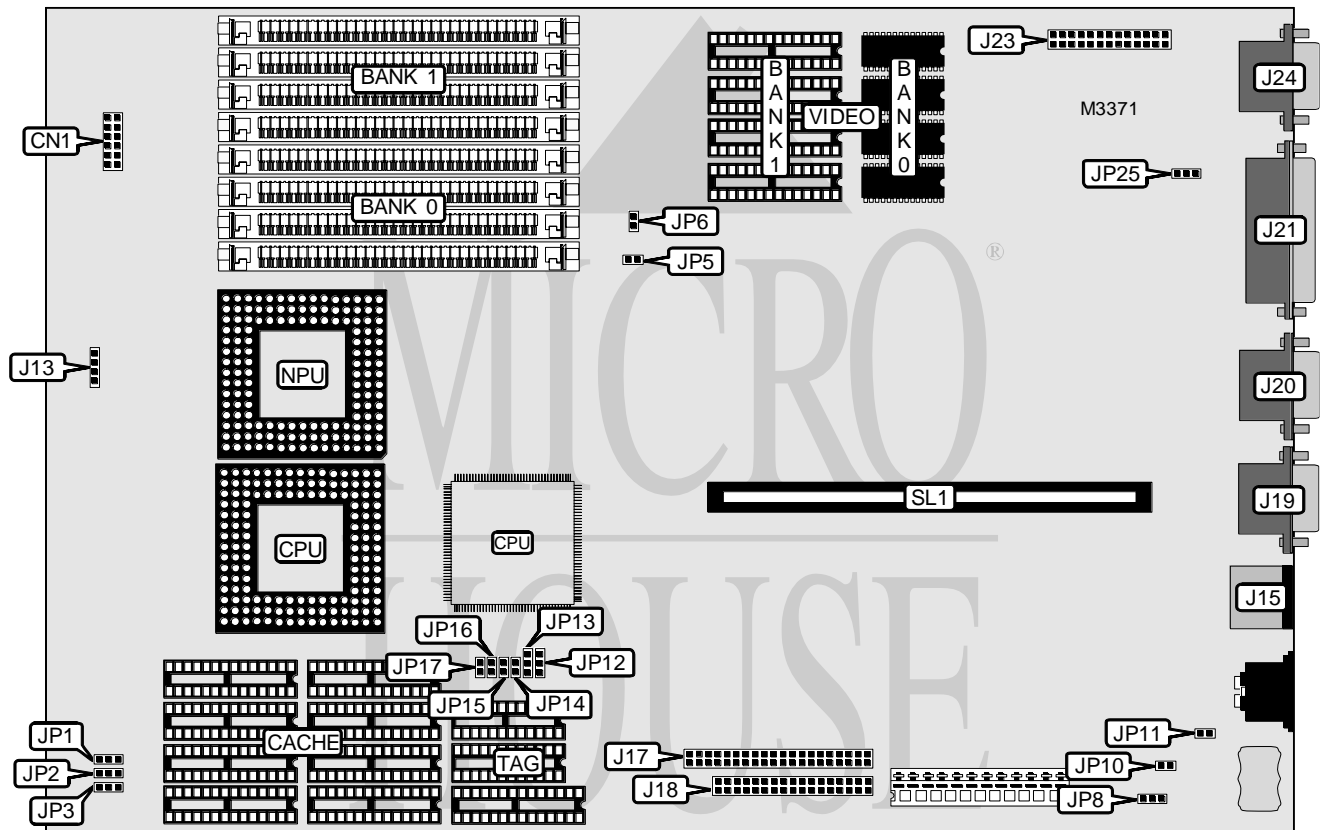
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| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| **Function** | **Label** | **Position** |
| í  Internal battery enabled | JP8 | Pins 1 & 2 closed |
| Internal battery disabled (CMOS memory clear) | JP8 | Pins 2 & 3 closed |
| í  Monitor type select color | JP10 | Open |
| Monitor type select monochrome | JP10 | Closed |
| í  PS/2 mouse IRQ12 enabled | JP11 | Closed |
| PS/2 mouse IRQ12 disabled | JP11 | Open |
| í  On board video enabled | JP25 | Pins 1 & 2 closed |
| On board video disabled | JP25 | Pins 2 & 3 closed |

| DRAM CONFIGURATION | | |
|---|---|---|
| Size | Bank 0 | Bank 1 |
| 4MB | (4) 1M x 9 | None |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | None |
| 20MB | (4) 1M x 9 | (4) 4M x 9 |
| 32MB | (4) 4M x 9 | (4) 4M x 9 |

| CACHE CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 64KB | (4) 8K x 8 | (4) 8K x 8 |
| 128KB | (4) 32K x 8 | None |
| 256KB | (4) 32K x 8 | (4) 32K x 8 |
| Note: The locations of banks 0 & 1 are unidentified. | | |

| CACHE TAG CONFIGURATION | | | |
|---|---|---|---|
| **Size** | **U48** | **U49** | **U50** |
| 64KB | (1) 8K x 8 | None | (1) 16K x 8 |
| 128KB | (1) 8K x 8 | None | (1) 16K x 8 |
| 256KB | (1) 8K x 8 | (1) 8K x 8 | (1) 16K x 8 |
| Note:  The locations of U48, U49 & U50 are unidentified. | | | |

| CACHE JUMPER CONFIGURATION | | | | | | |
|---|---|---|---|---|---|---|
| **Size** | **JP12** | **JP13** | **JP14** | **JP15** | **JP16** | **JP17** |
| None | 2 & 3 | 2 & 3 | Open | Open | Open | Open |
| 64KB | 2 & 3 | 2 & 3 | Open | Open | Open | Open |
| 128KB | 1 & 2 | 1 & 2 | Closed | Open | Open | Closed |
| 256KB | 2 & 3 | 2 & 3 | Closed | Closed | Closed | Closed |
| Note:  Pins designated should be in the closed position. | | | | | | |

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| VIDEO MEMORY CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 512KB | 512KB | None |
| 1MB | 512KB | 512KB |
| Note:  The chips sizes are unidentified. | | |

| CPU SPEED SELECTION | | |
|---|---|---|
| **Speed** | **JP5** | **JP6** |
| 25MHz | Closed | Open |
| 33MHz | Open | Closed |
| 50iMHz | Closed | Open |
| 66iMHz | Open | Closed |

| CPU TYPE SELECTION | | | |
|---|---|---|---|
| **Type** | **JP1** | **JP2** | **JP3** |
| 80486SX | Open | Pins 1 & 2 closed | Open |
| 80487SX | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 1 & 2 closed |
| 80486DX | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 1 & 2 closed |
| 80486DX2 | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 1 & 2 closed |
| Note:  If upgrade socket is filled, JP1 must be open. | | | |

| MISCELLANEOUS TECHNICAL NOTE |
|---|
| Note:  The location of pin 1 on the jumpers and connectors are unidentified. |