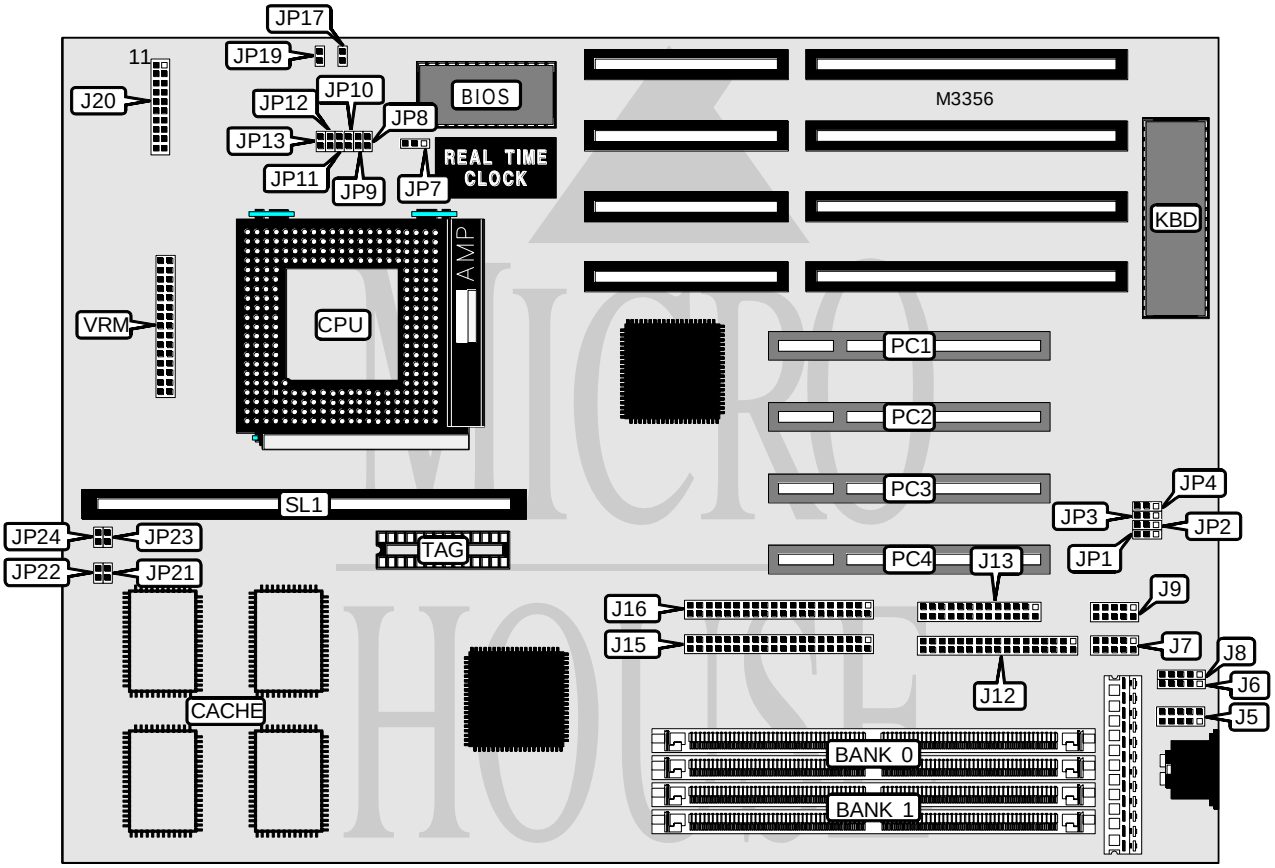
TMC RESERCH CORPORATION
A I 5 T H (V E R . 1 . 0)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB DRAM (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR connectors (2), VRM connector
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
PS/2 mouse interface	J5	Speaker	J20 pins 1 - 4
IR connector	J6	Green PC connector	J20 pins 6 & 16
Serial port 1	J7	Turbo LED	J20 pins 8 & 18
IR connector	J8	Reset switch	J20 pins 9 & 19
Serial port 2	J9	IDE interface LED	J20 pins 10 & 20
Floppy drive interface	J12	Power LED & keylock	J20 pins 11 - 15
Parallel port	J13	32-bit PCI slots	PC1 - PC4
IDE interface 1	J15	Cache slot	SL1
IDE interface 2	J16	VRM connector	VRM

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Flash BIOS voltage select 5v	JP7	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP7	Pins 1 & 2 closed
í AT bus clock select PCICLK/4	JP13	Closed
AT bus clock select PCICLK/3	JP13	Open
í CMOS memory normal operation	JP17	Open
CMOS memory clear	JP17	Closed

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	None	(2) 1M x 36
8MB	(2) 1M x 36	None
16MB	None	(2) 2M x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	None	(2) 4M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	None	(2) 8M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36

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DRAM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.		

CACHE CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**TAG**	**SL1**
256KB (A)	(2) 32K x 32	None	Unidentified	Not installed
256KB (B)	None	None	None	Installed
512KB (A)	(2) 32K x 32	(2) 32K x 32	Unidentified	Not installed
512KB (B)	(2) 64K x 32	None	Unidentified	Not installed
512KB (C)	None	None	None	Installed
Note: The location of Banks 0 & 1 are unidentified. The TAG chip sizes are unidentified.				

CACHE JUMPER CONFIGURATION		
Size	**JP21**	**JP22**
None	Open	Open
256KB (A, B)	Closed	Open
512KB (A, B, C)	Open	Closed

CACHE BANK CONFIGURATION		
Setting	**JP23**	**JP24**
1 bank	Open	Open
2 banks	Closed	Closed

CPU SPEED SELECTION (CYRIX)					
Speed	**JP8**	**JP9**	**JP10**	**JP11**	**JP12**
120MHz	Closed	Closed	Closed	Closed	Open
150MHz	Closed	Open	Closed	Closed	Open
166MHz	Open	Closed	Open	Closed	Open

CPU SPEED SELECTION (AMD)					
Speed	**JP8**	**JP9**	**JP10**	**JP11**	**JP12**
75MHz	Closed	Closed	Closed	Open	Open
90MHz	Closed	Open	Closed	Open	Open
100MHz	Open	Closed	Open	Open	Open

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CPU SPEED SELECTION (INTEL)					
Speed	**JP8**	**JP9**	**JP10**	**JP11**	**JP12**
75MHz	Closed	Closed	Closed	Open	Open
90MHz	Closed	Open	Closed	Open	Open
100MHz	Open	Closed	Open	Open	Open
120MHz	Closed	Open	Closed	Closed	Open
133MHz	Open	Closed	Open	Closed	Open
150MHz	Closed	Open	Closed	Closed	Closed
166MHz	Open	Closed	Open	Closed	Closed
180MHz	Closed	Open	Closed	Open	Closed
200MHz	Open	Closed	Open	Open	Closed

CPU TYPE SELECTION	
Type	**JP19**
Standard CPU	Closed
VRE CPU	Open

DMA CHANNEL SELECTION		
Channel	**JP3**	**JP4**
1	Pins 1 & 2 closed	Pins 1 & 2 closed
í 3	Pins 2 & 3 closed	Pins 2 & 3 closed

SERIAL PORT SELECTION		
Setting	**JP1**	**JP2**
í Serial port 2 set as serial port	Pins 1 & 2 closed	Pins 1 & 2 closed
IR connector	Pins 2 & 3 closed	Pins 2 & 3 closed