AMERICAN MEGATRENDS, INC. A P O L L O

Processor Pentium

Processor Speed 75/90/100/120/133MHz

Chip SetIntelVideo Chip SetNone

Maximum Onboard Memory 128MB (EDO supported)

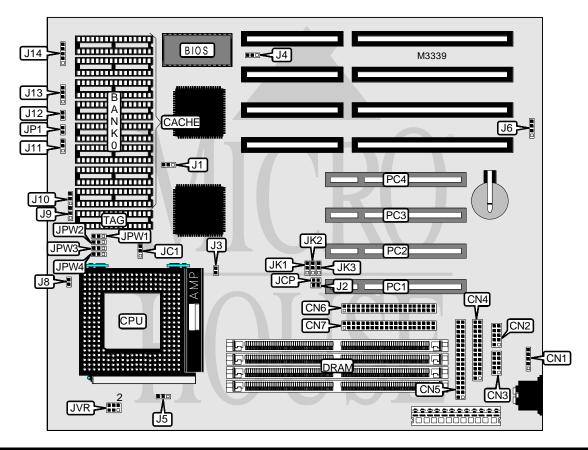
Maximum Video MemoryNoneCache256/512KBBIOSAMI

Dimensions 259mm x 216mm

I/O Options 32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), parallel port, PS/2

mouse interface, serial ports (2)

NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	CN1	External battery	J6
Serial port 2	CN2	Chassis fan power	J11
Serial port 1	CN3	Reset switch	J12
Parallel port	CN4	Speaker	J13
Floppy drive interface	CN5	Power LED & keylock	J14
IDE interface 1	CN6	IDE interface LED	JP1
IDE interface 2	CN7	32-bit PCI slots	PC1 - PC4

Continued on next page. . .

AMERICAN MEGATRENDS, INC. APOLLO

. . . continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
í Cache type select write back	J1	Open		
Cache type select write through	J1	Pins 1 & 2 closed		
í Monitor type select monochrome/EGA/VGA	J2	Open		
Monitor type select CGA	J2	Closed		
í Pipeline mode enabled	J3	Closed		
Pipeline mode disabled	J3	Open		
í Flash BIOS voltage select 5v	J4	Pins 2 & 3 closed		
Flash BIOS voltage select 12v	J4	Pins 1 & 2 closed		
CPU bus speed select PCLK/3	J5	Pins 1 & 2 closed		
CPU bus speed select PCLK/4	J5	Pins 2 & 3 closed		
PCI/CPU bus speed ratio select 1/2	J8	Closed		
PCI/CPU bus speed ratio select 3/2	J8	Open		
í CMOS memory normal operation	JCP	Open		
CMOS memory clear	JCP	Closed		

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
10MB	(2) 1M x 36	(2) 256K x 36
16MB	(2) 2M x 36	None
18MB	(2) 2M x 36	(2) 256K x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
66MB	(2) 8M x 36	(2) 256K x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION				
Size	Bank 0	TAG		
256KB (A) mixed voltage	(8) 32K x 8	(1) 8K x 8		
256KB (B) 3.3v	(8) 32K x 8	(1) 8K x 8		
512KB mixed voltage	(8) 64K x 8	(1) 32K x 8		

Continued on next page. . .

AMERICAN MEGATRENDS, INC. APOLLO

. . . continued from previous page

CACHE JUMPER CONFIGURATION							
Size	19	J10	JC1	JPW1	JPW2	JPW3	JPW4
256KB (A)	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB (B)	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
512KB	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION				
Speed	JK1	JK2	JK3	
75MHz	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed	
90MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	
100MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	
120MHz	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed	
133MHz	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed	

CPU VOLTAGE SELECTION			
Voltage JVR			
3.3v	Pins 1 & 2 closed		
3.45v	Pins 3 & 4 closed		
3.6v	Pins 5 & 6 closed		