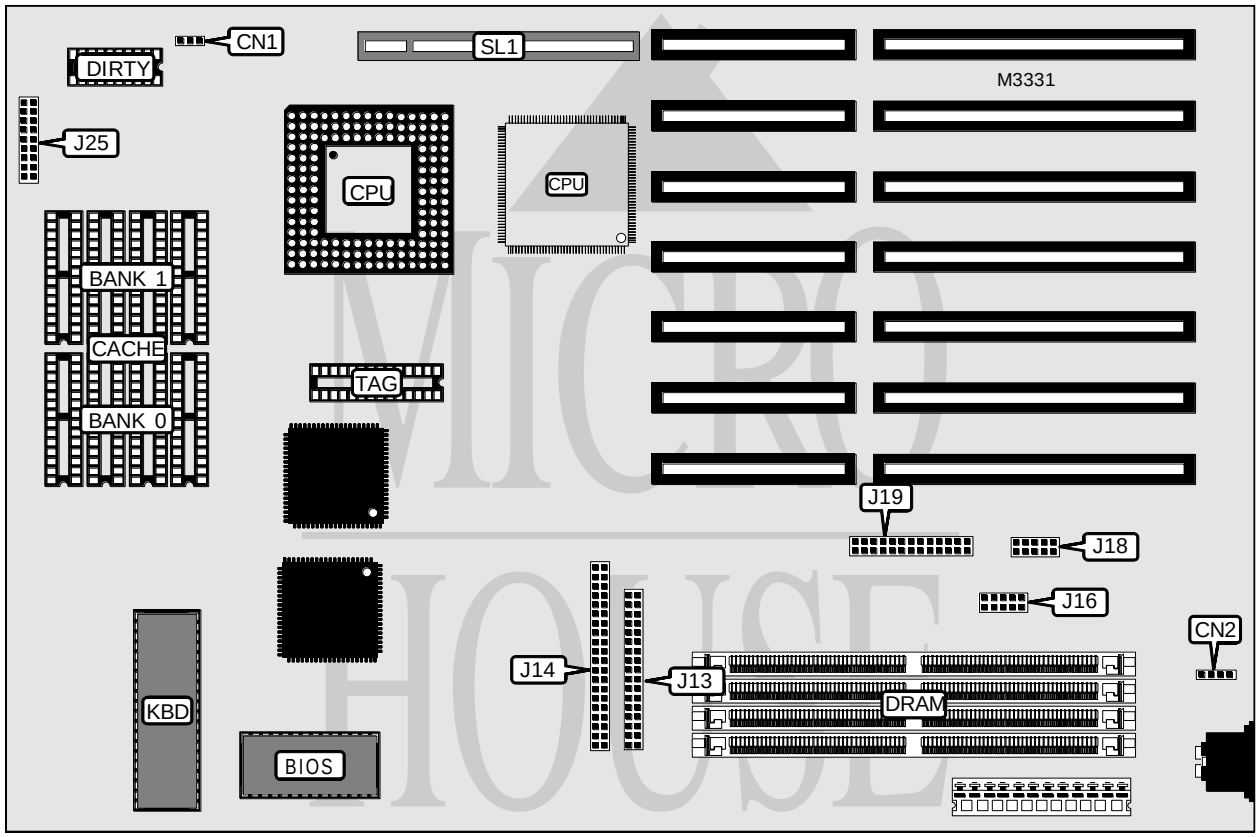
INTEL CORPORATION
C L A S S I C E E X P A N D A B L E D E S K T O P

Processor	80486SX/ODP486/80487SX/80486DX/80486DX2
Processor Speed	16/20/25/33/50(internal)/66(internal)MHz
Chip Set	OPTI
Video Chip Set	None
Maximum Onboard Memory	64MB
Maximum Video Memory	None
Cache	64/128/256KB
BIOS	Phoenix
Dimensions	426mm x 416mm
I/O Options	32-bit VESA local bus slot, floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	None



NOTE: THE LOCATION OF ALL JUMPERS ARE UNIDENTIFIED.

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CONNECTIONS			
Function	**Label**	**Function**	**Label**
Chassis fan power	CN1	Serial port 2	J18
External battery	CN2	Parallel port	J19
Floppy drive interface	J13	Front panel connector	J25
IDE interface	J14	32-bit VESA local bus slot	SL1
Serial port 1	J16		

USER CONFIGURABLE SETTINGS		
Setting	**Label**	**Position**
í Flash BIOS normal boot	J3	Pins 2 & 3 closed
Flash BIOS recovery mode boot	J3	Pins 1 & 2 closed
í On board I/O enabled	J4	Pins 1 & 2 closed
On board I/O disabled	J4	Pins 2 & 3 closed
í Password normal operation	J7	Pins 2 & 3 closed
Password clear	J7	Pins 1 & 2 closed
í Parallel port direction select output	J9	Pins 1 & 2 closed
Parallel port direction select input	J9	Pins 2 & 3 closed
í Parallel port direction select hardware for redirection	J11	Pins 2 & 3 closed
Parallel port direction select software hardware for redirection	J11	Pins 1 & 2 closed
í Battery type select 3.6v/4.5v	J17	Pins 2 & 3 closed
Battery type select 6v	J17	Pins 1 & 2 closed
í CMOS memory normal operation	J25 pins 17 & 18	Closed
CMOS memory clear	J25 pins 17 & 18	Open
í Fan enabled	J29	Pins 2 & 3 closed
Fan disabled	J29	Pins 1 & 2 closed
í 12v flash BIOS enabled	J30	Pins 1 & 2 closed
12v flash BIOS disabled	J30	Pins 2 & 3 closed
í Local bus ready select delayed	J31	Pins 1 & 2 closed
Local bus ready select normal operation	J31	Pins 2 & 3 closed

DRAM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
1MB	(1) 256K x 36	None	None	None
2MB	(1) 256K x 36	(1) 256K x 36	None	None
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	None
4MB	(1) 1M x 36	None	None	None
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
5MB	(1) 1M x 36	(1) 256K x 36	None	None
5MB	(1) 256K x 36	(1) 1M x 36	None	None
6MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	None
7MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
8MB	(1) 1M x 36	(1) 1M x 36	None	None
9MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	None

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CLASSIC E EXPANDABLE DESKTOP

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DRAM CONFIGURATION (CON'T)				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
13MB	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None	None	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
17MB	(1) 4M x 36	(1) 256K x 36	None	None
17MB	(1) 256K x 36	(1) 4M x 36	None	None
18MB	(1) 4M x 36	(1) 256K x 36	(1) 256K x 36	None
19MB	(1) 4M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
20MB	(1) 4M x 36	(1) 1M x 36	None	None
20MB	(1) 1M x 36	(1) 4M x 36	None	None
24MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	None
28MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 4M x 36	(1) 4M x 36	None	None
33MB	(1) 256K x 36	(1) 4M x 36	(1) 4M x 36	None
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
49MB	(1) 256K x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
Note: The orientation of the banks is unidentified.				

CACHE CONFIGURATION		
Size	**Bank 0**	**Bank 1**
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	None
256KB	(4) 32K x 8	(4) 32K x 8
Note: The chip sizes of the TAG and DIRTY bit are unidentified.		

CACHE JUMPER CONFIGURATION	
Size	**J34**
None	Open
64KB	Open
128KB	Pins 1 & 2, 5 & 6 closed
256KB	Pins 1 & 2, 3 & 4, 7 & 8 closed

CPU SPEED SELECTION		
Speed	**J5**	**J6**
16MHz	Pins 1 & 2 closed	Pins 1 & 2 closed
20MHz	Pins 2 & 3 closed	Pins 1 & 2 closed
25MHz	Pins 1 & 2 closed	Pins 2 & 3 closed
33MHz	Pins 2 & 3 closed	Pins 2 & 3 closed
50iMHz	Pins 1 & 2 closed	Pins 2 & 3 closed
66iMHz	Pins 2 & 3 closed	Pins 2 & 3 closed

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CPU TYPE SELECTION		
Type	**J32**	**J33**
80486SX	Pins 3 & 4 closed	Pins 4 & 6 closed
80487SX	Pins 1 & 2, 5 & 6 closed	Pins 1 & 3, 2 & 4 closed
ODP486	Pins 1 & 2, 5 & 6 closed	Pins 1 & 3, 2 & 4 closed
80486DX	Pins 1 & 2, 5 & 6 closed	Pins 3 & 5, 4 & 6 closed
80486DX2	Pins 1 & 2, 5 & 6 closed	Pins 3 & 5, 4 & 6 closed

MISCELLANEOUS TECHNICAL NOTE
The location of pin 1 on all jumpers is unidentified.