

FIRST INTERNATIONAL COMPUTER, INC.
PT - 2003

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Password disabled	JCP	Open
Password enabled	JCP	Closed
í Monitor type select monochrome/EGA/VGA	J2	Open
Monitor type select CGA	J2	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory.

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB (A)	(8) 32K x 8	(1) 8K x 8	Not installed
256KB (B)	None	None	Installed
512KB (A)	(8) 64K x 8	(1) 32K x 8	Not installed
512KB (B)	None	None	Installed

CACHE JUMPER CONFIGURATION	
Size	JS1
256KB	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

CACHE VOLTAGE CONFIGURATION		
Voltage	JPW1	JPW2
Mixed voltage	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed

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CPU SPEED CONFIGURATION					
Speed	J6	JC1	JC2	JCK1	JCK2
75MHz	1 & 2	Open	Open	Closed	Closed
90MHz	2 & 3	Open	Open	Closed	Open
100MHz	2 & 3	Open	Open	Open	Closed
120MHz	2 & 3	Closed	Open	Closed	Open
133MHz	2 & 3	Closed	Open	Open	Closed
150MHz	2 & 3	Closed	Closed	Closed	Open
166MHz	2 & 3	Closed	Closed	Open	Closed

Note: Pins designated should be in the closed position.

CPU VOLTAGE CONFIGURATION	
Voltage	JVR
3.38v	Pins 2 & 3 closed
3.5v	Pins 1 & 2 closed

BIOS CONFIGURATION		
Type	J3	J4
Intel SST 29EE010	Pins 2 & 3 closed	Pins 2 & 3 closed
Intel 28F01BX-T	Pins 1 & 2 closed	Pins 1 & 2 closed
Intel 28F010	Pins 1 & 2 closed	Pins 2 & 3 closed