## ICL

## DRS M35/286

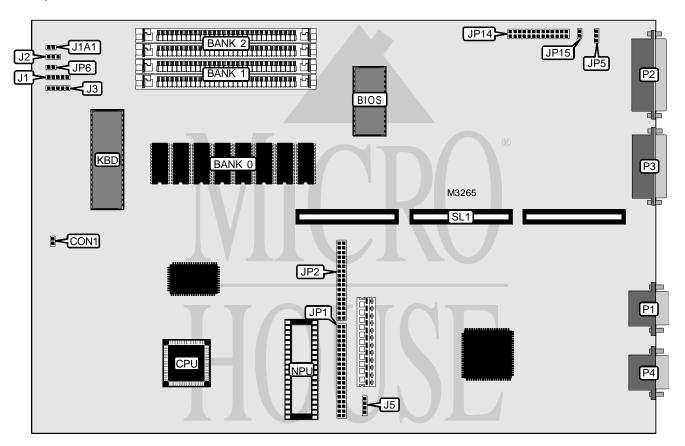
80286 **Processor Processor Speed** Unidentified Unidentified **Chip Set** Max. Onboard DRAM 5MB

Cache None **BIOS** Unidentified **Dimensions** 330mm x 218mm

Floppy drive interface, IDE interface, parallel port, serial ports (2), PS/2 mouse interface, I/O Options

VGA feature connector, VGA port, riser slot

**NPU Options** 80287



CONNECTIONS					
Purpose	Location	Purpose	Location		
Keyboard connector	J1	IDE interface LED	JP6		
Reset switch	J2	VGA feature connector	JP14		
PS/2 mouse interface	J3	Serial port	P1		
External battery	J5	Serial port	P2		
Keylock	J1A1	Parallel port	P3		
IDE interface	JP1	VGA port	P4		
Floppy drive interface	JP2	Riser slot	SL1		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
Password enabled	CON1	Closed		
Password disabled	CON1	Open		
Parallel port IRQ select IRQ7	JP5	Pins 2 & 3 closed		
Parallel port IRQ select IRQ5	JP5	Pins 1 & 2 closed		
On board video enabled	JP15	Closed		
On board video disabled	JP15	Open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	
1MB	1MB	None	None	
1.5MB	1MB	(2) 256K x 9	None	
2MB	1MB	(2) 256K x 9	(2) 256K x 9	
3MB	1MB	(2) 1M x 9	None	
5MB	1MB	(2) 1M x 9	(2) 1M x 9	
Note: Bank 0 is factory installed and is not configurable.				

MISCELLANEOUS TECHNICAL NOTE				
The location of pin 1 is unidentified.				