## ATRONICS INTERNATIONAL, INC. ATI-1000

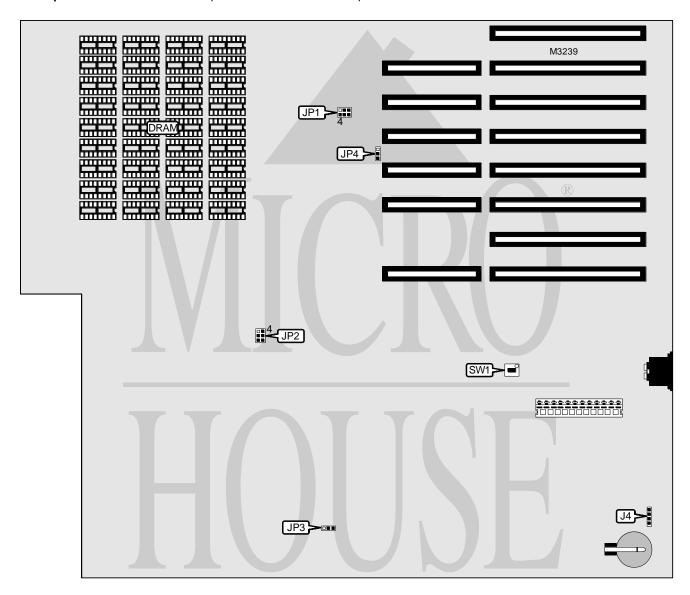
80286 (exact location unidentified) **Processor** 

**Processor Speed** 8MHz **Chip Set** Unidentified

Max. Onboard DRAM 1MB Cache None **BIOS** Unidentified **Dimensions** 325mm x 300mm

I/O Options None

**NPU Options** 80287 (exact location unidentified)



CONNECTIONS				
Purpose	Location	Purpose	Location	
Speaker	J1	External battery	J4	
Note: The location of J1 is unidentified	l.			

Continued on next page. . .

## ATRONICS INTERNATIONAL, INC. ATI-1000

. . . continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
í Factory configured - do not alter	JP3	Pins 2 & 3 closed		
EPROM selected	JP4	Pins 2 & 3 closed		
PROM selected	JP4	Pins 1 & 2 closed		
Monitor type select color	SW1	Off		
Monitor type select monochrome	SW1	On		

DRAM CONFIGURATION		
Note:	This board accepts 640KB or 1MB of memory. The configurations are unidentified.	

DRAM JUMPER CONFIGURATION		
Size	JP2	
640KB	Pins 1 & 2, 5 & 6 closed	
1MB	Pins 2 & 3, 5 & 6 closed	

BIOS CONFIGURATION			
BIOS chips installed	JP1		
2	Pins 2 & 3, 5 & 6 closed		
4	Pins 1 & 2, 4 & 5 closed		