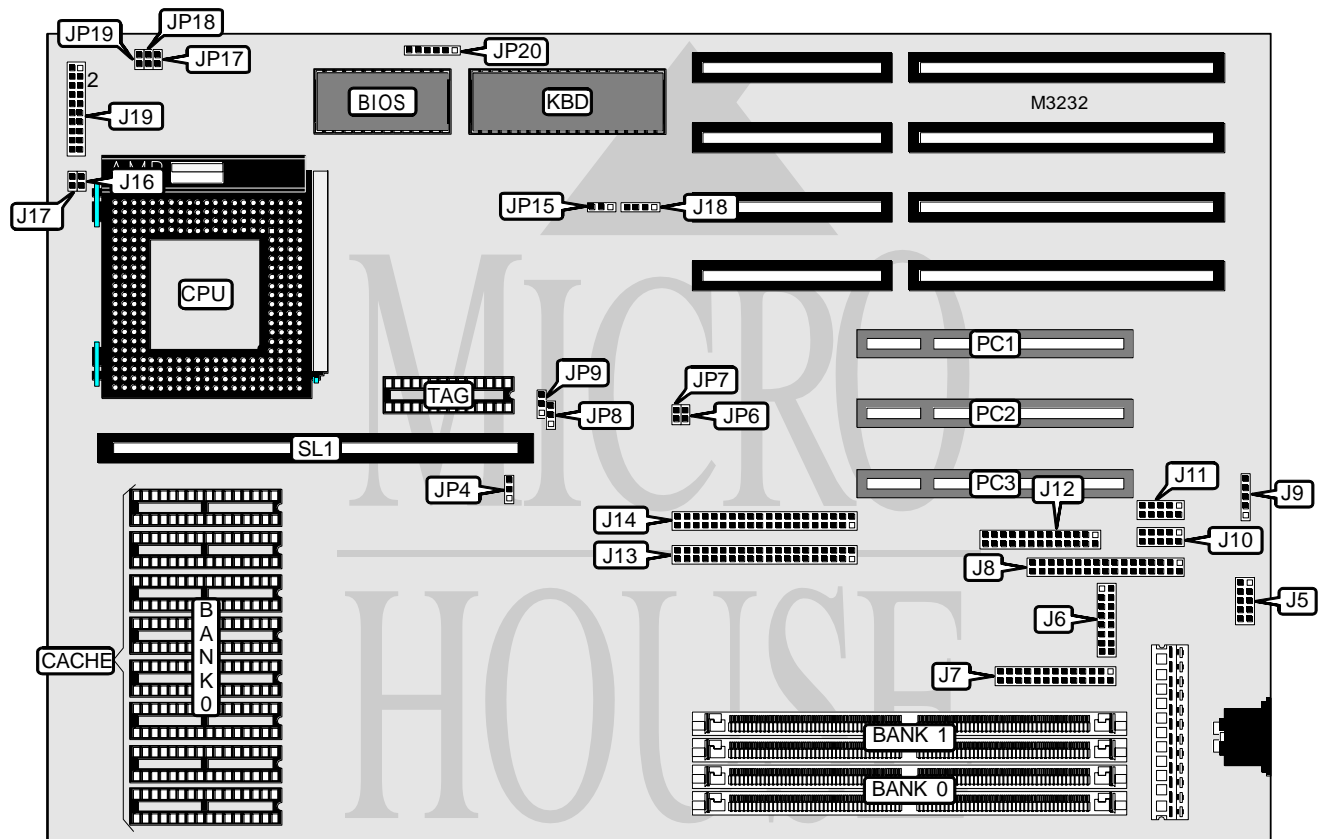


# TMC RESEARCH CORPORATION PCI54SV (VER. 0.0)

<b>Processor</b>	Pentium
<b>Processor Speed</b>	75/90/100/120/133/150/166/180/200MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	256/512/1024KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VGA feature connector, VGA interface, cache slot, IR connector
<b>NPU Options</b>	None



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# TMC RESEARCH CORPORATION

## PCI54SV (VER. 0.0)

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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	J5	Green PC connector	J17
VGA interface	J6	External battery	J18
VGA feature connector	J7	Speaker	J19 pins 1 - 4
Floppy drive interface	J8	Turbo switch	J19 pins 7 & 17
IR connector	J9	Turbo LED	J19 pins 8 & 18
Serial port 2	J10	Reset switch	J19 pins 9 & 19
Serial port 1	J11	IDE interface LED	J19 pins 10 & 20
Parallel port	J12	Power LED & keylock	J19 pins 11 - 15
IDE interface 1	J13	32-bit PCI slots	PC1 - PC3
IDE interface 2	J14	Cache slot	SL1
Green PC connector	J16		

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP15	Pins 1 & 2 closed
CMOS memory clear	JP15	Pins 2 & 3 closed
PS/2 mouse enabled	JP17	Closed
PS/2 mouse disabled	JP17	Open
í Flash BIOS voltage select 5v	JP20	Pins 2 & 3, 5 & 6 closed
Flash BIOS voltage select 12v	JP20	Pins 1 & 2, 5 & 6 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 1M x 36	(2) 256K x 36
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 2M x 36	(2) 256K x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	None

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# TMC RESEARCH CORPORATION

## PCI54SV (VER. 0.0)

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 4M x 36	(2) 256K x 36
34MB	(2) 256K x 36	(2) 4M x 36
36MB	(2) 4M x 36	(2) 512K x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 8M x 36	(2) 256K x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB (A)	(8) 32K x 8	(1) 8K x 8	Not installed
256KB (B)	None	None	Installed
512KB (A)	(8) 64K x 8	(1) 16K x 8	Not installed
512KB (B)	None	None	Installed
1MB	(8) 128K x 8	(1) 32K x 8	Not installed

CACHE JUMPER CONFIGURATION		
Size	JP8	JP9
256KB (A)	Pins 1 & 2 closed	JP9 pin 2 & JP8 pin 3 closed
512KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed
1MB	JP8 pin 2 & JP9 pin 1 closed	Pins 2 & 3 closed

CACHE TYPE CONFIGURATION	
Type	JP4
Asynchronous	Pins 1 & 2 closed
Pipeline burst	Pins 2 & 3 closed

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**TMC RESEARCH CORPORATION**  
**PCI54SV (VER. 0.0)***... continued from previous page*

CPU SPEED CONFIGURATION				
Speed	JP6	JP7	JP18	JP19
75MHz	Closed	Open	Open	Open
90MHz	Open	Closed	Open	Open
100MHz	Open	Open	Open	Open
120MHz	Open	Closed	Closed	Open
133MHz	Open	Open	Closed	Open
150MHz	Open	Closed	Closed	Closed
166MHz	Open	Open	Closed	Closed
180MHz	Open	Closed	Open	Closed
200MHz	Open	Open	Open	Closed