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Processor	80486SX/SL80486SX/SL80486SX2/ODP486SX/UMCU5/80486DX/SL80486DX/CX486M7/AM 486DX2/80486DX2/SL80486DX2/P24D/80486DX4/ Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/ 100(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	64MB
Cache	64/128/256/512KB
BIOS	Award
Dimensions	250mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None
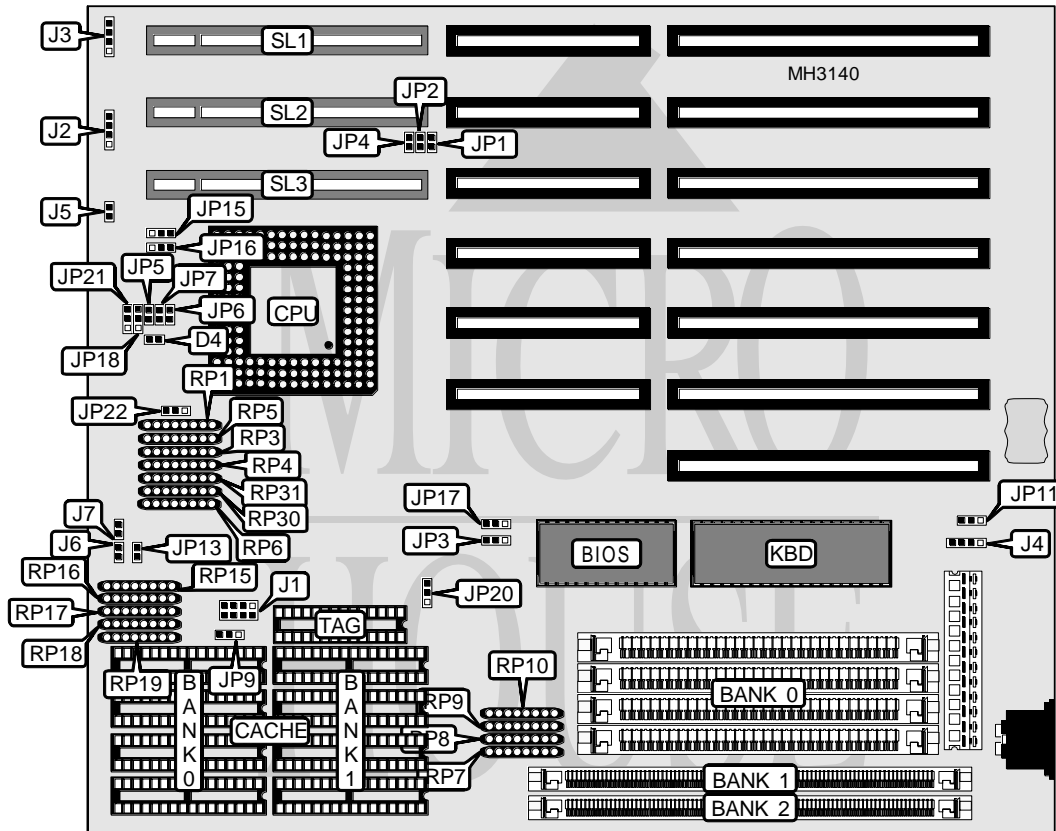
CONNECTIONS			
Purpose	Location	Purpose	Location
Green PC LED	D4	Reset switch	J5
Green PC connector	J1	Turbo switch	J6
Power LED & keylock	J2	Turbo LED	J7
Speaker	J3	Green PC connector	JP13
External battery	J4	32-bit VESA local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP11	pins 1 & 2 closed
CMOS memory clear	JP11	pins 2 & 3 closed
í Factory configured - do not alter	RP1	N/A

DRAM CONFIGURATION 1		
Size	Bank 0	Bank 1	Bank 2
1MB	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE
16MB	(4) 4M x 9	NONE	NONE
64MB	(4) 16M x 9	NONE	NONE

DRAM CONFIGURATION 2		
Size	Bank 0	Bank 1	Bank 2
1MB	NONE	(1) 256K x 36	NONE
2MB	NONE	(1) 256K x 36	(1) 256K x 36
4MB	NONE	(1) 1M x 36	NONE
5MB	NONE	(1) 256K x 36	(1) 1M x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
16MB	NONE	(1) 4M x 36	NONE
20MB	NONE	(1) 1M x 36	(1) 4M x 36
32MB	NONE	(1) 4M x 36	(1) 4M x 36

DRAM CONFIGURATION 3		
Size	Bank 0	Bank 1	Bank 2
2MB	NONE	(1) 512K x 36	NONE
8MB	NONE	(1) 2M x 36	NONE
10MB	NONE	(1) 512K x 36	(1) 2M x 36
16MB	NONE	(1) 2M x 36	(1) 2M x 36
32MB	NONE	(1) 8M x 36	NONE
40MB	NONE	(1) 2M x 36	(1) 8M x 36
64MB	NONE	(1) 8M x 36	(1) 8M x 36

DRAM CONFIGURATION 4		
Size	Bank 0	Bank 1	Bank 2
2MB	(4) 256K x 9	(1) 256K x 36	NONE
5MB	(4) 256K x 9	(1) 1M x 36	NONE
6MB	(4) 256K x 9	(1) 256K x 36	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
18MB	(4) 256K x 9	(1) 256K x 36	(1) 4M x 36
20MB	(4) 1M x 9	(1) 4M x 36	NONE
24MB	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	NONE
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36

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DRAM CONFIGURATION 5			
Size	Bank 0	Bank 1	Bank 2
12MB	(4) 1M x 9	(1) 2M x 36	NONE
16MB	(4) 1M x 9	(1) 2M x 36	(1) 1M x 36
36MB	(4) 1M x 9	(1) 8M x 36	NONE
48MB	(4) 4M x 9	(1) 8M x 36	NONE
64MB	(4) 4M x 9	(1) 8M x 36	(1) 4M x 36

DRAM RESISTOR CONFIGURATION				
DRAM configuration	RP7	RP8	RP9	RP10
1	Not installed	Not installed	Not installed	Installed
2	Not installed	Installed	Not installed	Not installed
3	Installed	Not installed	Not installed	Not installed
4	Not installed	Not installed	Not installed	Installed
5	Not installed	Not installed	Installed	Not installed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 16K or (1) 32K x 8
256KB (B)	(4) 64K x 8	NONE	(1) 16K or (1) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP9	JP20
64KB	1 & 2	2 & 3
128KB	2 & 3	1 & 2
256KB (A)	2 & 3	1 & 2
256KB (B)	2 & 3	1 & 2
512KB	2 & 3	1 & 2

CACHE RESISTOR CONFIGURATION					
Size	RP15	RP16	RP17	RP18	RP19
64KB	Installed	Not installed	Not installed	Not installed	Not installed
128KB	Not installed	Installed	Not installed	Not installed	Not installed
256KB (A)	Not installed	Not installed	Installed	Not installed	Not installed
256KB (B)	Not installed	Not installed	Not installed	Installed	Not installed
512KB	Not installed	Not installed	Not installed	Not installed	Installed

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CPU TYPE CONFIGURATION					
Type	JP3	JP17	JP18	JP21	JP23
80486SX	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
SL80486SX	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
SL80486SX2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
ODP486SX	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3
UMCU5	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
80486DX	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
SL80486DX	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
CX486M7	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
AM486DX2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
80486DX2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
SL80486DX2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
P24D	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
80486DX4	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
P24T	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION (CON'T)					
Type	RP3	RP4	RP5	RP6	RP30	RP31
80486SX	Not installed	Not installed	Installed	Not installed	Not installed	Installed
SL80486SX	Not installed	Not installed	Installed	Not installed	Not installed	Installed
SL80486SX2	Not installed	Not installed	Installed	Not installed	Not installed	Installed
ODP486SX	Installed	Not installed	Not installed	Not installed	Not installed	Installed
UMCU5	Not installed	Not installed	Installed	Not installed	Not installed	Installed
80486DX	Installed	Not installed	Not installed	Not installed	Not installed	Installed
SL80486DX	Installed	Not installed	Not installed	Not installed	Not installed	Installed
CX486M7	Installed	Not installed	Not installed	Not installed	Installed	Not installed
AM486DX2	Installed	Not installed	Not installed	Not installed	Not installed	Installed
80486DX2	Installed	Not installed	Not installed	Not installed	Not installed	Installed
SL80486DX2	Installed	Not installed	Not installed	Not installed	Not installed	Installed
P24D	Not installed	Installed	Not installed	Installed	Not installed	Not installed
80486DX4	Installed	Not installed	Not installed	Not installed	Not installed	Installed
P24T	Installed	Not installed	Not installed	Installed	Not installed	Not installed

CPU SPEED CONFIGURATION (MX8315)			
Speed	JP5	JP6	JP7
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
75iMHz	Open	Open	Closed
80iMHz	Open	Closed	Closed
100iMHz	Closed	Closed	Closed

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CPU SPEED CONFIGURATION (IMISC466)			
Speed	JP5	JP6	JP7
25MHz	Open	Open	Open
33MHz	Closed	Closed	Open
40MHz	Open	Closed	Open
50iMHz	Open	Open	Open
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Open
75iMHz	Open	Open	Open
80iMHz	Open	Closed	Open
100iMHz	Closed	Closed	Open

CPU TYPE/SPEED CONFIGURATION (80486DX4)	
Speed	JP22
Cyrix/AMD CPU	pins 1 & 2 closed
2x	pins 2 & 3 closed
3x	Open

CPU VOLTAGE CONFIGURATION			
Voltage	JP1	JP2	JP4
3.3v	Open	Open	Open
5v	Closed	Closed	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP15
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP16
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed