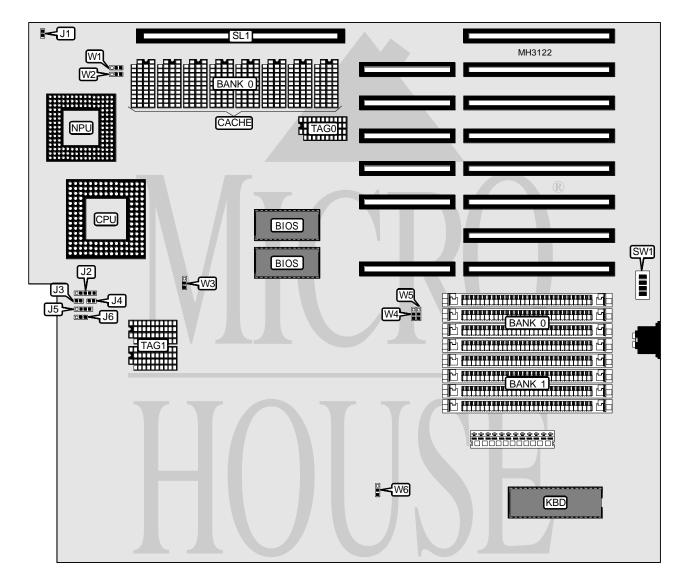
Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

80486SX 25MHz VLSI 16MB (8MB on external memory card) 64/256KB DTK/Phoenix 355mm x 305mm 32-bit external memory card (PEI-306) 4167



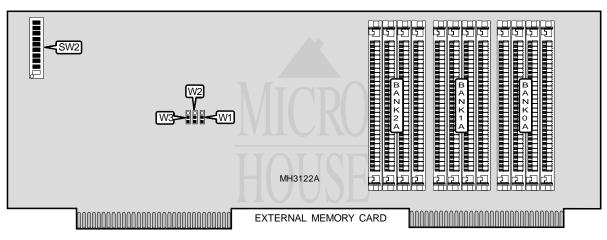
... continued from previous page

CONNECTIONS					
Purpose Location Purpose Location					
Chassis fan power	J1	Speaker	J5		
Power LED & keylock	J2	Turbo switch	J6		
Turbo LED	J3	32-bit external memory card	SL1		
Reset switch	J4				

USER CONFIGURABLE SETTINGS					
Function Jumper/Switch Position					
BIOS type select 27256	W3	pins 1 & 2 closed			
BIOS type select 27512	W3	pins 2 & 3 closed			
í Factory configured - do not alter	W6	N/A			
Monitor type select color	SW1/4	On			
Monitor type select monochrome	SW1/4	Off			

CACHE CONFIGURATION					
Size Bank 0 TAG 0 TAG 1					
64KB	(8) 16K x 4	(1) 16K x 1	(2) 16K x 4		
256KB	(8) 64K x 4	(1) 64K x 1	(2) 64K x 4		

	CACHE JUMPER CONFIGURATION			
Size W1 W2				
64KB	pins 1 & 2 closed	pins 1 & 2 closed		
256KB	pins 2 & 3 closed	pins 2 & 3 closed		



... continued from previous page

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 0A	Bank 1A	Bank 2A	
1MB	(4) 256K x 9	NONE	NONE	NONE	NONE	
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE	NONE	
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE	NONE	
4MB (A)	(4) 1M x 9	NONE	NONE	NONE	NONE	
4MB (B)	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE	
5MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE	NONE	
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE	NONE	
9MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	NONE	NONE	
10MB (A)	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	NONE	
10MB (B)	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE	
11MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE	NONE	
14MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE	
Note: Banks 0A,	Note: Banks 0A, 1A, and 2A are located on the external memory card					

DRAM JUMPER CONFIGURATION						
Size	W1	W2	W3	W4	W5	
1MB	N/A	N/A	N/A	2&3	2&3	
2MB	N/A	N/A	N/A	2&3	2&3	
3MB	2 & 3	2 & 3	2 & 3	2&3	2&3	
4MB (A)	N/A	N/A	N/A	1&2	1 & 2	
4MB (B)	2 & 3	2&3	2 & 3	2&3	2&3	
5MB	2 & 3	2&3	2&3	2&3	2&3	
6MB	1 & 2	1&2	1 & 2	2&3	2&3	
8MB	N/A	N/A	N/A	1&2	1&2	
9MB	2&3	2&3	2&3	1&2	1&2	
10MB (A)	2 & 3	2&3	2&3	1&2	1&2	
10MB (B)	1 & 2	1&2	1 & 2	2&3	2&3	
11MB	2 & 3	2&3	2 & 3	1&2	1&2	
12MB	1 & 2	1&2	1 & 2	1&2	1&2	
14MB	1 & 2	1&2	1 & 2	2&3	2&3	
16MB	1 & 2	1 & 2	1 & 2	1&2	1&2	
Note: W4, W5 a	Note: W4, W5 are located on the mainboard.					

... continued from previous page

DRAM SWITCH CONFIGURATION (CON'T)					
Size	SW1/1	SW1/2	SW1/3		
1MB	On	On	On		
2MB	On	On	On		
3MB	On	On	On		
4MB (A)	On	On	Off		
4MB (B)	On	On	On		
5MB	On	Off	On		
6MB	On	Off	On		
8MB	On	Off	Off		
9MB	Off	On	Off		
10MB (A)	Off	On	Off		
10MB (B)	Off	On	On		
11MB	Off	On	Off		
12MB	Off	On	Off		
14MB	Off	Off	On		
16MB	Off	Off	Off		
Note: SW1 is located on the mainboard.					

DRAM SWITCH CONFIGURATION (CON'T)						
Size	SW2/1	SW2/2	SW2/3	SW2/4	SW2/5	
1MB	N/A	N/A	N/A	N/A	N/A	
2MB	N/A	N/A	N/A	N/A	N/A	
3MB	On	On	Off	Off	On	
4MB (A)	N/A	N/A	N/A	N/A	N/A	
4MB (B)	On	On	Off	Off	On	
5MB	On	On	Off	Off	On	
6MB	On	Off	Off	Off	On	
8MB	N/A	N/A	N/A	N/A	N/A	
9MB	On	On	On	Off	Off	
10MB (A)	On	On	On	Off	Off	
10MB (B)	On	Off	Off	Off	On	
11MB	On	On	On	Off	Off	
12MB	On	Off	On	Off	Off	
14MB	On	Off	Off	Off	On	
16MB	On	Off	On	Off	Off	
Note: SW2 is loc	Note: SW2 is located on the memory card					

... continued from previous page

DRAM SWITCH CONFIGURATION (CON'T)						
Size	SW2/6	SW2/7	SW2/8	SW2/9	SW2/10	
1MB	N/A	N/A	N/A	N/A	N/A	
2MB	N/A	N/A	N/A	N/A	N/A	
3MB	Off	Off	On	On	N/A	
4MB (A)	N/A	N/A	N/A	N/A	N/A	
4MB (B)	Off	Off	On	On	N/A	
5MB	Off	Off	On	On	N/A	
6MB	Off	Off	On	On	N/A	
8MB	N/A	N/A	N/A	N/A	N/A	
9MB	Off	Off	On	On	N/A	
10MB (A)	Off	Off	On	On	N/A	
10MB (B)	Off	Off	On	On	N/A	
11MB	Off	Off	On	On	N/A	
12MB	Off	Off	On	On	N/A	
14MB	Off	Off	On	On	N/A	
16MB	Off	Off	On	On	N/A	
Note: SW2 is loc	Note: SW2 is located on the memory card.					