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I S A - 4 8 6 S V 2

Processor	80486SX/80487SX/80486DX/80486DX2/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	None
NPU Options	None
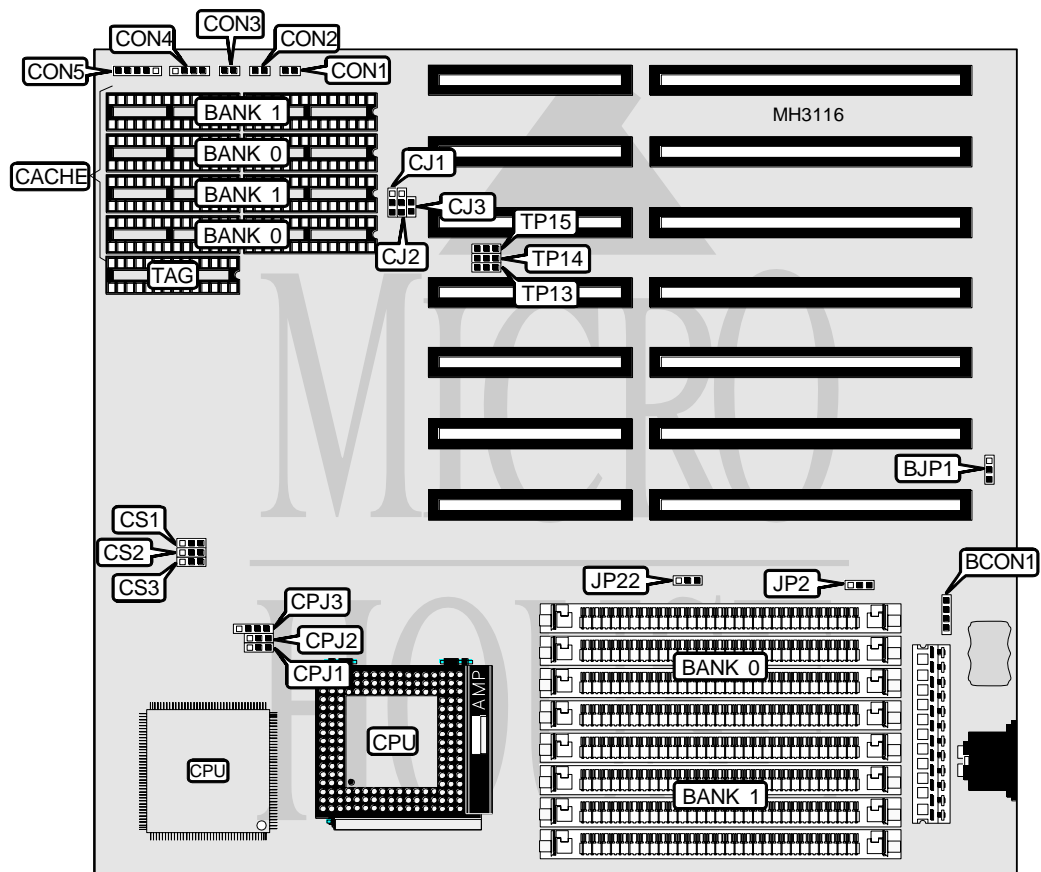
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BCON1	Reset switch	CON3
Turbo switch	CON1	Speaker	CON4
Turbo LED	CON2	Power LED & keylock	CON5

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Battery type select internal	BJP1	pins 1 & 2 closed
Battery type select external	BJP1	pins 2 & 3 closed
Monitor type select CGA	JP2	pins 2 & 3 closed
Monitor type select monochrome/EGA/VGA	JP2	pins 1 & 2 closed
CPU type select AMD/Intel	JP22	pins 2 & 3 closed
CPU type select Cyrix	JP22	pins 1 & 2 closed
í Factory configured - do not alter	TP13	pins 1 & 2 closed
í Factory configured - do not alter	TP14	pins 1 & 2 closed
í Factory configured - do not alter	TP15	pins 1 & 2 closed
í Factory configured - do not alter	TP17	pins 2 & 3 closed
í Factory configured - do not alter	TP25	1D
í Factory configured - do not alter	TP26	Open
í Factory configured - do not alter	TP27	1D
Note: The location of jumpers TP17, TP25, TP26 and TP27 are unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	CJ1	CJ2	CJ3
64KB	pins 2 & 3 closed	pins 2 & 3 closed	Open
128KB	pins 1 & 2 closed	Open	Closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	Open

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CPU TYPE CONFIGURATION			
Type	CPJ1	CPJ2	CPJ3
80486SX (PQFP)	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed
80486SX (PGA)	pins 2 & 3 closed	pins 2 & 3 closed	Open
80487SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 2 & 3 closed
Pentium Overdrive	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed
Note: See also JP22 in User Configurable Settings table.			

CPU SPEED CONFIGURATION			
Speed	CS1	CS2	CS3
20MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
25MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
40MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
50iMHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
66iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed