UNIDENTIFIED GREEN G486VLI

CX486M6/80486SX/AM486DX/CX486M7/80486DX/80486DX2/Pentium Overdrive **Processor**

Processor Speed 25/33/40/50(internal)/50/66(internal)MHz

Chip Set Contag Max. Onboard DRAM 128MB

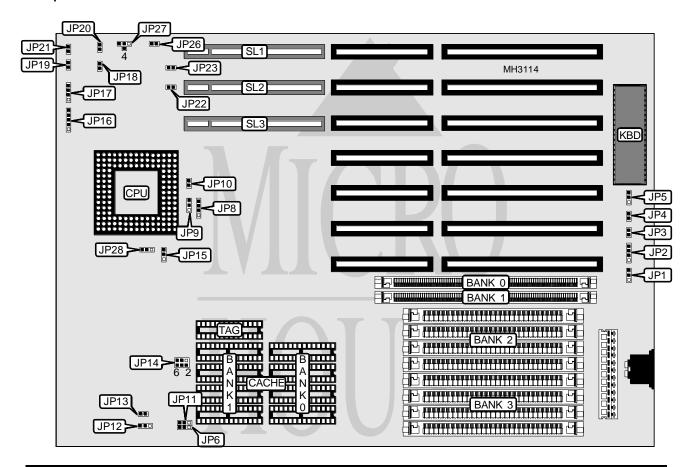
Cache 256/512/1024KB

BIOS AMI

Dimensions 285mm x 220mm

I/O Options 32-bit VESA local bus slots (3), green PC connector

NPU Options None



CONNECTIONS					
Purpose	ose Location Purpose				
External battery	JP2	Turbo switch	JP19		
Green PC connector	JP3	JP3 Green PC connector			
Power LED & keylock	JP16	Turbo LED	JP21		
Speaker	JP17	Reset switch	JP26		
Green PC LED	JP18	32-bit VESA local bus slots	SL1 - SL3		

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í CMOS memory normal operation	JP1	pins 1 & 2 closed		
CMOS memory clear	JP1	pins 2 & 3 closed		
Monitor type select color/EGA/PGA/VGA	JP4	Open		
Monitor type select CGA	JP4	Closed		
BIOS voltage select 5v	JP5	pins 1 & 2 closed		
BIOS voltage select 12v	JP5	pins 2 & 3 closed		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
1MB	NONE	NONE	(4) 256K x 9	NONE	
1MB	(1) 256K x 36	NONE	NONE	NONE	
2MB	NONE	NONE	(4) 256K x 9	(4) 256K x 9	
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE	
2MB	NONE	(1) 256K x 36	(4) 256K x 9	NONE	
2MB	(1) 256K x 36	NONE	NONE	(4) 256K x 9	
4MB	NONE	NONE	(4) 1M x 9	NONE	
4MB	(1) 1M x 36	NONE	NONE	NONE	
8MB	NONE	NONE	(4) 1M x 9	(4) 1M x 9	
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE	
8MB	NONE	(1) 1M x 36	(4) 1M x 9	NONE	
8MB	(1) 1M x 36	NONE	NONE	(4) 1M x 9	
16MB	NONE	NONE	(4) 4M x 9	NONE	
16MB	(1) 4M x 36	NONE	NONE	NONE	
20MB	NONE	NONE	(4) 1M x 9	(4) 4M x 9	
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE	
20MB	NONE	(1) 4M x 36	(4) 1M x 9	NONE	
20MB	(1) 4M x 36	NONE	NONE	(4) 1M x 9	
32MB	NONE	NONE	(4) 4M x 9	(4) 4M x 9	
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE	
32MB	NONE	(1) 4M x 36	(4) 4M x 9	NONE	
32MB	(1) 4M x 36	NONE	NONE	(4) 4M x 9	
64MB	NONE	NONE	(4) 16M x 9	NONE	
80MB	NONE	NONE	(4) 4M x 9	(4) 16M x 9	
80MB	NONE	(1) 4M x 36	(4) 16M x 9	NONE	
128MB	NONE	NONE	(4) 16M x 9	(4) 16M x 9	

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG		
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8		
256KB (B)	(4) 64K x 8	NONE	(1) 32K x 8		
512KB (A)	(4) 64K x 8	(4) 64K x 8	(1) 64K x 8		
512KB (B)	(4) 128K x 8	NONE	(1) 32K x 8		
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8		

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CACHE JUMPER CONFIGURATION					
Size JP6 JP11 JP12					
256KB	Open	pins 1 & 2 closed	pins 1 & 2 closed		
512KB	Open	pins 2 & 3 closed	pins 2 & 3 closed		
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed		

CPU TYPE CONFIGURATION						
Туре	JP8	JP9	JP10	JP15		
CX486M6	pins 2 & 3 closed	Open	Open	pins 2 & 3 closed		
80486SX (PQFP)	pins 2 & 3 closed	Open	Open	pins 1 & 2 closed		
80486SX (PGA)	pins 2 & 3 closed	Open	Open	pins 1 & 2 closed		
CX486M7	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	pins 2 & 3 closed		
AM486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	pins 1 & 2 closed		
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	pins 1 & 2 closed		
80486DX2	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	pins 1 & 2 closed		
P24T	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Closed	pins 1 & 2 closed		
Note: The location	Note: The location of the PQFP is unidentified.					

CPU SPEED CONFIGURATION						
Speed	JP13	JP14	JP22	JP23	JP27	JP28
25MHz	Closed	3 & 4, 5 & 6	Open	Open	2 & 4	2 & 3
33MHz	Open	5 & 6	Open	Closed	2 & 4	2 & 3
40MHz	Open	1 & 2, 3 & 4	Closed	Closed	1 & 2	1 & 2
50iMHz	Closed	3 & 4, 5 & 6	Open	Open	2 & 4	2 & 3
50MHz	Open	1 & 2	Closed	Closed	1 & 2	1 & 2
66iMHz	Open	5 & 6	Open	Closed	2 & 4	2 & 3
Note: Pins designated should be in the closed position.						