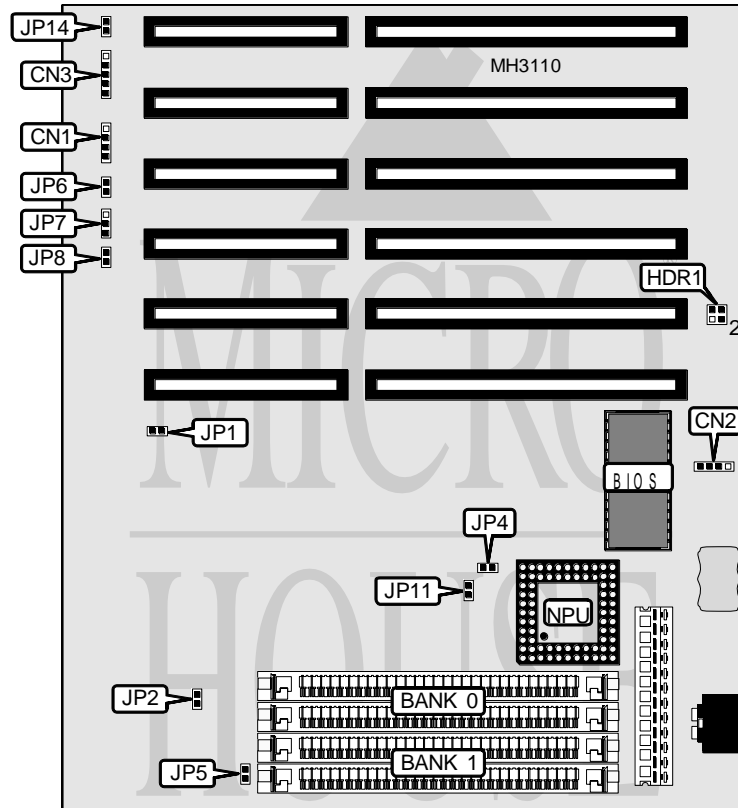


# UNIDENTIFIED SCAMP 386SX

<b>Processor</b>	80386SX/CX486SLC (exact location unidentified)
<b>Processor Speed</b>	16/20/25/33MHz
<b>Chip Set</b>	VLSI
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	218mm x 168mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	CN1	Turbo switch	JP6
External battery	CN2	Turbo LED	JP8
Power LED & keylock	CN3	Reset switch	JP14

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# UNIDENTIFIED SCAMP 386SX

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	HDR1	pins 1 & 2 closed
CMOS memory clear	HDR1	pins 3 & 4 closed
Pipeline mode disabled	JP1	Open
Pipeline mode enabled	JP1	Closed
Monitor type select monochrome	JP4	Open
Monitor type select color	JP4	Closed
í Keyboard matching select normal operation	JP5	Open
Keyboard matching select keyboard error during boot-up	JP5	Closed
CPU clock speed select software turbo	JP7	pins 1 & 2 closed
CPU clock speed select hardware turbo	JP7	pins 2 & 3 closed
NPU clock select CPU clock speed	JP11	Open
NPU clock select NPU clock speed	JP11	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
2.5MB	(2) 1M x 9	(2) 256K x 9
2.5MB	(2) 256K x 9	(2) 1M x 9
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION	
Speed	JP2
16MHz	Open
20MHz	Open
25MHz	Open
33MHz	Closed

Note: If CPU speed is 16MHz or 20MHz, set Fast Clock Divider in Advanced CMOS Setup to 4.