UNIDENTIFIED
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Processor	80386DX/CX486DLC
Processor Speed	25/33/40MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387DX
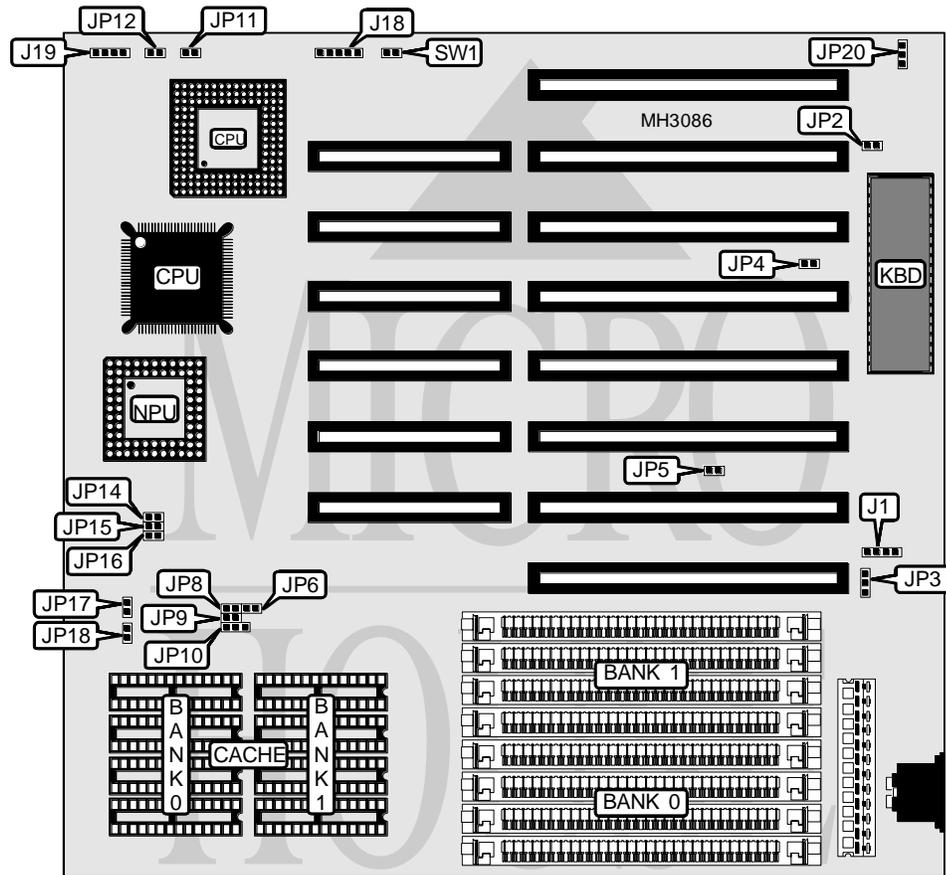
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	JP11
Power LED & keylock	J18	Turbo LED	JP12
Speaker	J19	Reset switch	SW1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP3	pins 2 & 3 closed
CMOS memory clear	JP3	pins 1 & 2 closed
Monitor type select color	JP4	Closed
Monitor type select monochrome	JP4	Open
í Factory configured - do not alter	JP8	N/A
í Factory configured - do not alter	JP9	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
Note: The location of TAG is unidentified.			

CACHE JUMPER CONFIGURATION				
Size	JP6	JP10	JP17	JP18
64KB	Open	pins 2 & 3 closed	Open	Open
128KB	Closed	pins 1 & 2 closed	Open	Closed
256KB	Closed	pins 2 & 3 closed	Closed	Closed

CPU TYPE CONFIGURATION		
Type	JP5	JP20
80386DX	Open	Open
CX486DLC	Closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION				
Speed	JP2	JP14	JP15	JP16
25MHz	N/A	Closed	Open	Closed
33MHz	Closed	Open	Closed	Closed
40MHz	Open	Closed	Open	Open