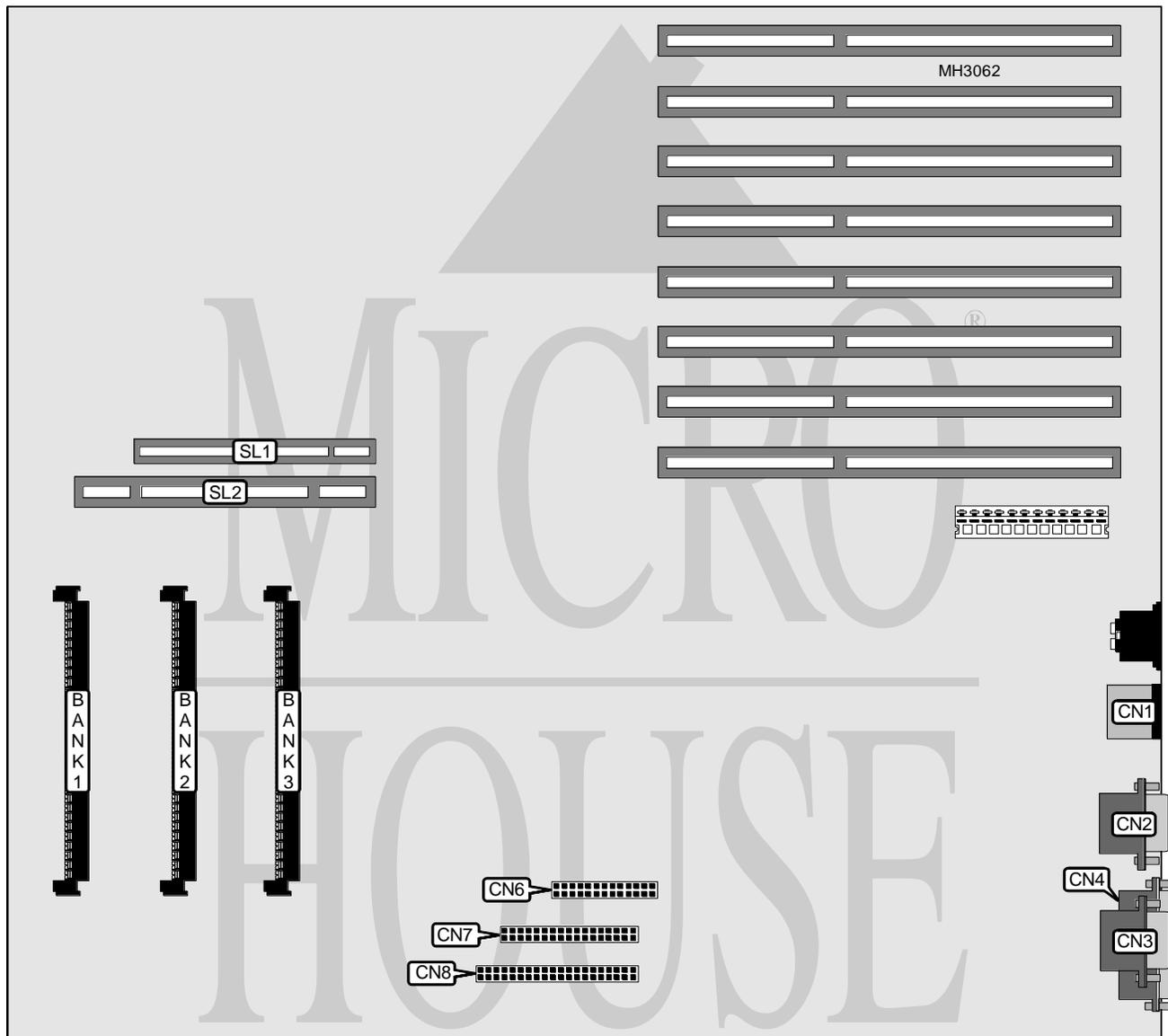


ADVANCED LOGIC RESEARCH, INC. BUSINESS VEISA II

Processor	80486SX/80486DX/80486DX2 (depends on CPU card installed)
Processor Speed	33/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	52MB
Cache	256KB (on external cache card)
BIOS	Unidentified
Dimensions	355mm x 304mm
I/O Options	Floppy drive interface, IDE interface, parallel ports (2), PS/2 mouse port, serial port, VGA port, cache slot, CPU slot
NPU Options	None



Continued on next page. . .

**ADVANCED LOGIC RESEARCH, INC.
BUSINESS VEISA II**

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Parallel port	CN6
Serial port	CN2	IDE interface	CN7
VGA port	CN3	Cache slot	SL1
Parallel port	CN4	CPU slot	SL2
Floppy drive interface	CN5		

Note: The location of pin 1 is unidentified.

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	4MB	NONE	NONE	NONE
8MB	4MB	(1) 1M x 36	NONE	NONE
12MB	4MB	(1) 1M x 36	(1) 1M x 36	NONE
16MB	4MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
20MB	4MB	(1) 4M x 36	NONE	NONE
24MB	4MB	(1) 1M x 36	(1) 4M x 36	NONE
28MB	4MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36
36MB	4MB	(1) 4M x 36	(1) 4M x 36	NONE
40MB	4MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
52MB	4MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36

Note: Bank 0 is factory installed and is not configurable. The location of Bank 0 is unidentified.

CACHE CONFIGURATION	
Size	SL1
256KB	Card installed