## ADVANCED LOGIC RESEARCH, INC. FLYER 32DT/SD32

80486SX/80486DX/80486DX2 (depends on CPU card installed) **Processor** 

**Processor Speed** 25/33/50(internal)/50/66(internal)MHz

Unidentified **Chip Set** Max. Onboard DRAM 36MB

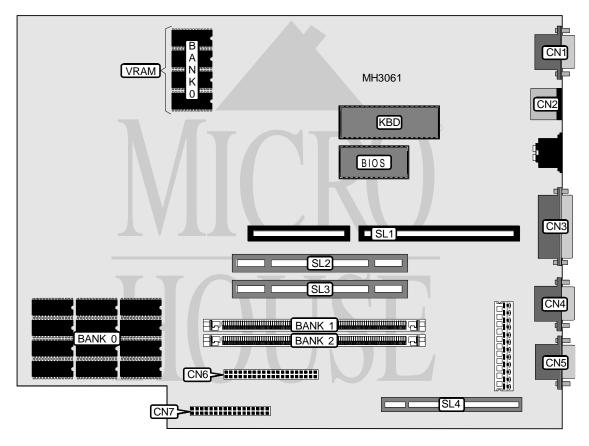
Cache 256KB (on external cache card)

**BIOS** Unidentified **Dimensions** 330mm x 218mm

Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), VGA I/O Options

port, riser slot, cache slot, CPU slot

**NPU Options** None



CONNECTIONS				
Purpose	Location	Purpose	Location	
VGA port	CN1	Floppy drive interface	CN7	
PS/2 mouse port	CN2	Riser slot	SL1	
Parallel port	CN3	Cache slot	SL2	
Serial port 1	CN4	CPU slot	SL3	
Serial port 2	CN5	ALP proprietary slot	SL4	
IDE interface	CN6			
Note: The location of pin 1 is unidenti	fied.			

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	DRAM CO	NFIGURATION	
Size	Bank 0	Bank 1	Bank 2
4MB	4MB	NONE	NONE
8MB	4MB	(1) 1M x 36	NONE
12MB	4MB	(1) 1M x 36	(1) 1M x 36
20MB	4MB	(1) 4M x 36	NONE
24MB	4MB	(1) 1M x 36	(1) 4M x 36
36MB	4MB	(1) 4M x 36	(1) 4M x 36
Note: Bank 0 is factory inst	alled and is not configurabl	e.	

CACHE CONFIGURATION		
Size SL1		
256КВ	Card installed	

	VRAM CONFIGURATION
	VIVAIVICONTIGURATION
Note:	512KB cache is factory installed and is not configurable.
	512ND cache is factory instance and is not configurable.