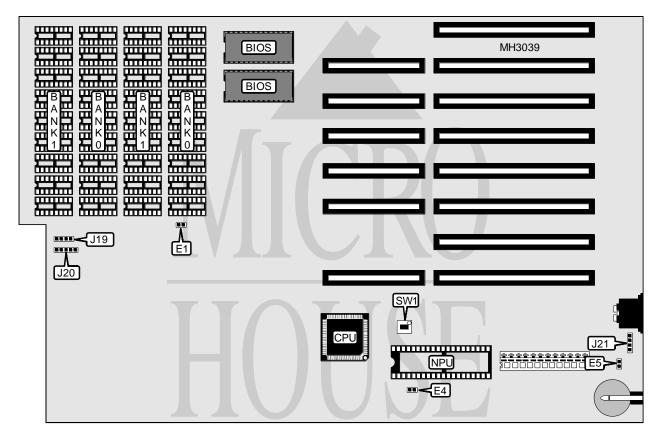
UNIDENTIFIED S M A R T E K

Processor	80286
Processor Speed	6/8MHz
Chip Set	None
Max. Onboard DRAM	1MB
Cache	None
BIOS	SMT
Dimensions	350mm x 304mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J19	External battery	J21
Power LED & keylock	J20	Reset switch	E5

Continued on next page. . .

UNIDENTIFIED S M A R T E K

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
Speed select high speed	E1	Closed	
Speed select low speed	E1	Open	
Memory wait state select 0 wait states	E2	Closed	
Memory wait state select 1 wait state	E2	Open	
Turbo select high	J3	Open	
Turbo select low	J3	Closed	
Turbo select software	J3	Ctrl, Alt, -	
í Monitor type select color	SW1	Off	
Monitor type select monochrome	SW1	On	
Note: The location of E2 is unidentified.			

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	
512KB	(18) 41256	NONE	
640KB	(18) 41256	(18) 4164	
1MB	(18) 41256	(18) 41256	

DRAM JUMPER CONFIGURATION			
Size	E3	E4	
512KB	Open	Closed	
640KB	Closed	Open	
1MB	Open	Open	
Note: The location of E3 is unidentified.			