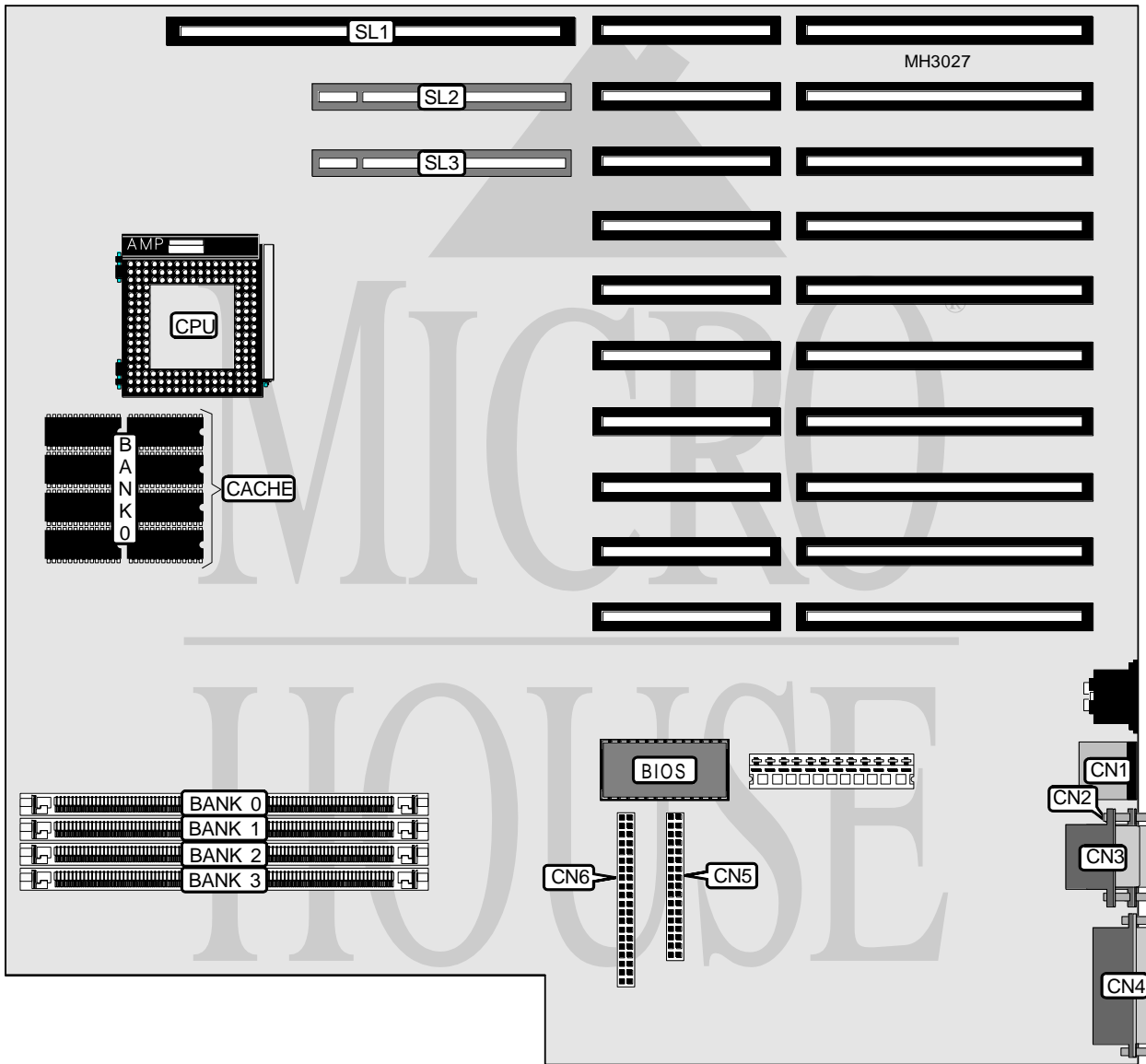


ADVANCED LOGIC RESEARCH, INC. PROVIESA LT

Processor	80486DX/80486DX2
Processor Speed	33/50(internal)/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	256KB
BIOS	Unidentified
Dimensions	355mm x 305mm
I/O Options	32-bit slot, 32-bit VESA local bus slots (2), floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2)
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Floppy drive interface	CN5
Serial port 1	CN2	IDE interface	CN6
Serial port 2	CN3	32-bit slot	SL1
Parallel port	CN4	32-bit VESA local bus slots	SL2 & SL3
Note: The location of pin 1 is unidentified.			

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
28MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36

CACHE CONFIGURATION	
Size	Bank 0
256KB	(8) 32K x 8
Note: Cache is factory installed and is not configurable.	