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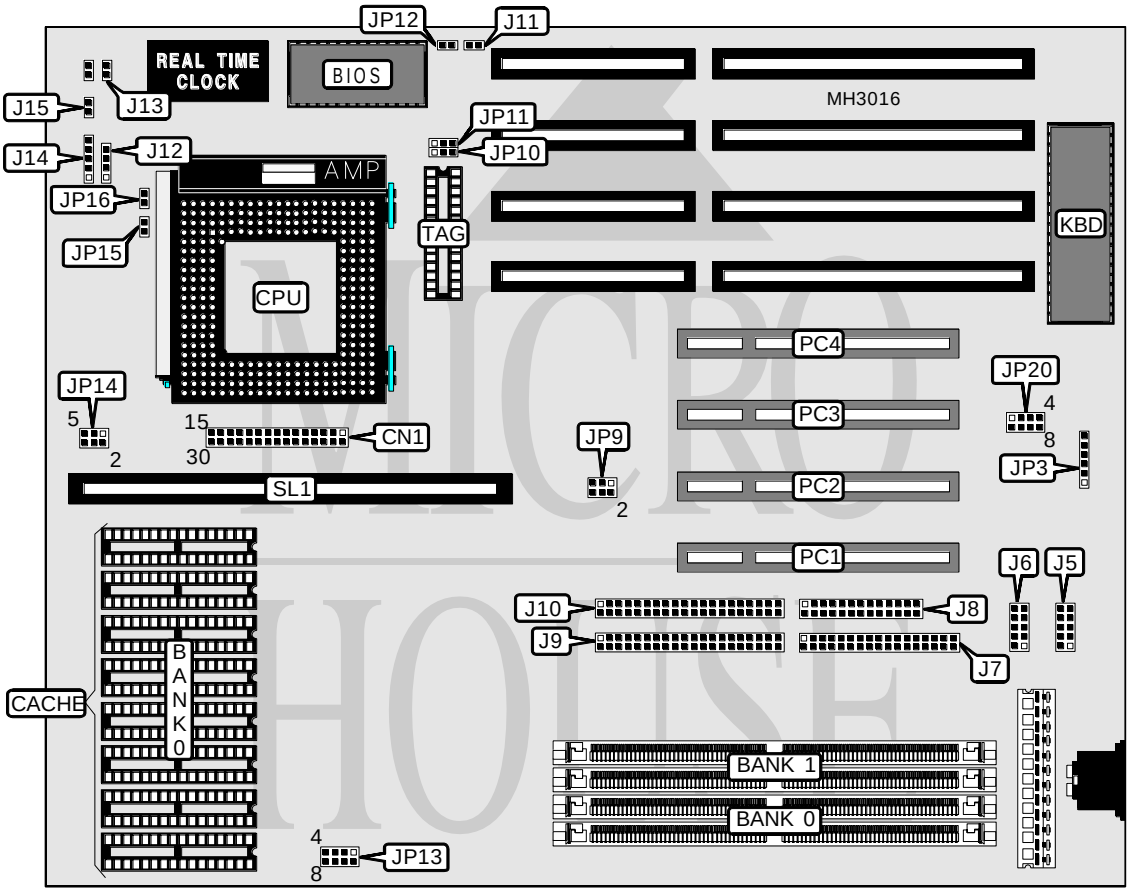
5 8 6 I E M 0 . 1

Processor	Pentium
Processor Speed	75/90/100/120/133/150MHz
Chip Set	Intel
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Award
Dimensions	275mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2), VRM module connector, cache slot
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
VRM module connector	CN1	Speaker	J12
Serial port 1	J5	Turbo LED	J13
Serial port 2	J6	Power LED & keylock	J14
Parallel port	J7	Reset switch	J15
Floppy drive interface	J8	IR connector	JP3
IDE interface 1	J9	Green PC connector	JP12
IDE interface 2	J10	32-bit PCI slots	PC1 - PC4
IDE interface LED	J11	Cache slot	SL1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í VRM on board regulator disabled	CN1	Open
VRM on board regulator enabled	CN1	pins 6 & 7, 21 & 22 closed
í IR connector type select Intel	JP3	pins 1 - 5 closed
IR connector type select HP	JP3	pins 3 - 6 closed
í COM port enabled	JP20	pins 1 & 5, 2 & 6 closed
IR function enabled	JP20	pins 3 & 7, 4 & 8 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
2MB	NONE	(2) 256K x 36
4MB	(2) 512K x 36	NONE
4MB	NONE	(2) 512K x 36
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 1M x 36	NONE
8MB	NONE	(2) 1M x 36
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 256K x 36	(2) 1M x 36
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 512K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	NONE
16MB	NONE	(2) 2M x 36
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 256K x 36	(2) 2M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 512K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
24MB	(2) 1M x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	NONE
32MB	NONE	(2) 4M x 36
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 256K x 36	(2) 4M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 512K x 36	(2) 4M x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 1M x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	NONE

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
64MB	NONE	(2) 8M x 36
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 256K x 36	(2) 8M x 36
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 512K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 1M x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 4M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	TAG	SL1
256KB	(8) 32K x 8	(1) 8K x 8	Not installed
256KB	NONE	NONE	Installed
512KB	(8) 64K x 8	(1) 16K or (1) 32K x 8	Not installed
512KB	NONE	NONE	Installed
Note: If SL1 is used, remove cache from Bank 0 and TAG.			

CACHE JUMPER CONFIGURATION	
Size	JP10
256KB	pins 1 & 2 closed
512KB	pins 2 & 3 closed

CACHE TYPE CONFIGURATION	
Type	JP13
Mixed cache	pins 1 & 5, 2 & 6 closed
3.3v cache	pins 3 & 7, 4 & 8 closed

CPU SPEED CONFIGURATION						
Speed	Voltage	JP9	JP11	JP14	JP15	JP16
75MHz	3.3v	3 & 5, 4 & 6	2 & 3	1 & 2	Open	Open
90MHz	3.3v	1 & 3, 4 & 6	1 & 2	1 & 2	Open	Open
90MHz	3.4v	1 & 3, 4 & 6	1 & 2	3 & 4	Open	Open
90MHz	3.5v	1 & 3, 4 & 6	1 & 2	5 & 6	Open	Open
100MHz	3.5v	3 & 5, 2 & 4	1 & 2	5 & 6	Open	Open
120MHz	3.5v	1 & 3, 4 & 6	1 & 2	5 & 6	Open	Closed
133MHz	3.5v	3 & 5, 2 & 4	1 & 2	5 & 6	Open	Closed
150MHz	3.5v	1 & 3, 4 & 6	1 & 2	5 & 6	Closed	Closed
Note: Pins designated should be in the closed position.						