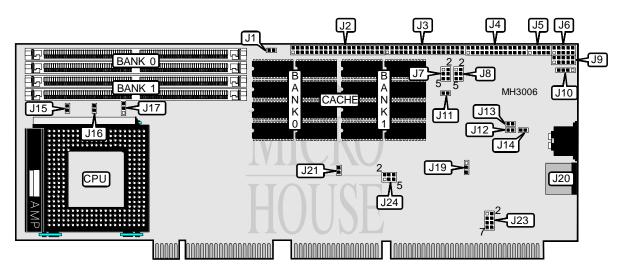
## I-BUS S H A R K

Processor	Pentium
Processor Speed	90/100MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Unidentified
Dimensions	300mm x 89mm
I/O Options	Floppy drive interface
NPU Options	None

ntium 100MHz TI BMB 5/512KB dentified Omm x 89mm ppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2)



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface LED	J1	Serial port 2	J6
IDE interface	J2	Auxiliary keyboard connector	19
Floppy drive interface	J3	Speaker	J10
Parallel port	J4	32-bit system memory card	J13
Serial port 1	J5	PS/2 mouse port	J20

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## I-BUS S H A R K

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Floppy drive type select 1.44MB	8L	pins 1 & 2 closed
Floppy drive type select 2.88MB	81	pins 3 & 4, 5 & 6 closed
í IDE IRQ14 enabled	J11	Closed
IDE IRQ14 disabled	J11	Open
í Password enabled	J12	Closed
Password disabled	J12	Open
í Mouse IRQ12 enabled	J14	Closed
Mouse IRQ12 disabled	J14	Open
í CPU normal operation	J16	Open
CPU stop clock enabled	J16	Closed
í Cache invalidate select always invalidated	J17	pins 2 & 3 closed
Cache invalidate select invalidate on write only	J17	pins 1 & 2 closed
í CMOS memory normal operation	J19	pins 2 & 3 closed
CMOS memory clear	J19	pins 1 & 2 closed
On board battery disabled	J19	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(4) 32K x 8	(4) 32K x 8
512KB (4) 64K x 8 (4) 64K x 8		
Note: Board will be factory installed with either 256KB or 512KB cache		

CACHE JUMPER CONFIGURATION		
Size J7 J21		
256KB	pins 3 & 5, 4 & 6 closed	Open
512KB	pins 1 & 3, 2 & 4 closed	pins 1 & 2 closed

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## I-BUS S H A R K

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CPU SPEED CONFIGURATION		
Speed	Clock speed	J24
90MHz	1.5x	pins 1 & 2, 3 & 4 closed
100MHz	1.5x	pins 3 & 4 closed
100MHz	2x	Open

CPU SPEED CONFIGURATION		
Speed J15		
1.5x	Open	
2x	Closed	

DMA CONFIGURATION		
DMA J23		
DMA 1	pins 3 & 4, 7 & 8 closed	
DMA 3	pins 1 & 2, 5 & 6 closed	