

CHAINTECH COMPUTER COMPANY, LTD.

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| USER CONFIGURABLE SETTINGS | | |
|--------------------------------|--------|-------------------|
| Function | Jumper | Position |
| í CMOS memory normal operation | JP22 | pins 1 & 2 closed |
| CMOS memory clear | JP22 | pins 2 & 3 closed |
| í CPU type select Cyrix | JP25 | Open |
| CPU type select AM486DX2 | JP25 | Closed |
| CPU type select AM486DX4 | JP25 | Open |

| DRAM CONFIGURATION | | | | |
|--------------------|---------------|---------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (1) 256K x 36 | NONE | NONE | NONE |
| 2MB | (1) 256K x 36 | (1) 256K x 36 | NONE | NONE |
| 2MB | (1) 512K x 36 | NONE | NONE | NONE |
| 4MB | (1) 1M x 36 | NONE | NONE | NONE |
| 5MB | (1) 256K x 36 | (1) 1M x 36 | NONE | NONE |
| 6MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | NONE |
| 6MB | (1) 512K x 36 | NONE | (1) 1M x 36 | NONE |
| 8MB | (1) 1M x 36 | (1) 1M x 36 | NONE | NONE |
| 8MB | (1) 2M x 36 | NONE | NONE | NONE |
| 10MB | (1) 256K x 36 | (1) 256K x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 10MB | (1) 256K x 36 | (1) 256K x 36 | (1) 2M x 36 | NONE |
| 10MB | (1) 512K x 36 | NONE | (1) 2M x 36 | NONE |
| 12MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | NONE |
| 12MB | (1) 2M x 36 | NONE | (1) 1M x 36 | NONE |
| 16MB | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (1) 2M x 36 | NONE | (1) 2M x 36 | NONE |
| 16MB | (1) 2M x 36 | NONE | (1) 1M x 36 | (1) 1M x 36 |
| 20MB | (1) 1M x 36 | (1) 4M x 36 | NONE | NONE |
| 24MB | (1) 2M x 36 | NONE | (1) 4M x 36 | NONE |
| 32MB | (1) 4M x 36 | (1) 4M x 36 | NONE | NONE |
| 32MB | (1) 8M x 36 | NONE | NONE | NONE |
| 36MB | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 36MB | (1) 1M x 36 | (1) 8M x 36 | NONE | NONE |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 40MB | (1) 1M x 36 | (1) 1M x 36 | (1) 8M x 36 | NONE |
| 40MB | (1) 2M x 36 | NONE | (1) 8M x 36 | NONE |
| 40MB | (1) 2M x 36 | NONE | (1) 4M x 36 | (1) 4M x 36 |
| 48MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 48MB | (1) 4M x 36 | (1) 8M x 36 | NONE | NONE |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 8M x 36 | NONE |
| 64MB | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | (1) 8M x 36 | NONE | (1) 8M x 36 | NONE |

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| CACHE CONFIGURATION | | | |
|---------------------|-------------|-------------|------------------------|
| Size | Bank 0 | Bank 1 | TAG |
| 128KB | (4) 32K x 8 | NONE | (1) 8K x 8 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 16K or (1) 32K x 8 |

| CACHE RESISTOR CONFIGURATION | | |
|------------------------------|---------------|---------------|
| Size | RP12 | RP13 |
| 128KB | Installed | Not installed |
| 256KB | Not installed | Installed |

| CPU TYPE CONFIGURATION | | | | | |
|---|-------|-------|-------|-------|-------|
| Type | JP9 | JP10 | JP16 | JP20 | JP21 |
| 80486SX | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| SL80486SX | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| 80486SX2 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| SL80486SX2 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| ODP486SX | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| UMC U5 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| CX486DX | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| 80486DX | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| SL80486DX | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| CX486DX2 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| P24D | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| 80486DX2 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| SL80486DX2 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| 80486DX4 | 1 & 2 | 1 & 2 | 1 & 2 | 2 & 3 | 2 & 3 |
| Note: Pins designated should be in the closed position. | | | | | |

| CPU TYPE CONFIGURATION (CON'T) | | | | | | |
|--------------------------------|---------------|---------------|---------------|---------------|---------------|---------------|
| Type | RP5 | RP6 | RP7 | RP8 | RP9 | RP10 |
| 80486SX | Installed | Not installed | Not installed | Installed | Not installed | Not installed |
| SL80486SX | Installed | Not installed | Not installed | Installed | Not installed | Not installed |
| 80486SX2 | Installed | Not installed | Not installed | Installed | Not installed | Not installed |
| SL80486SX2 | Installed | Not installed | Not installed | Installed | Not installed | Not installed |
| ODP486SX | Not installed | Installed | Not installed | Installed | Not installed | Not installed |
| UMC U5 | Installed | Not installed | Not installed | Installed | Not installed | Not installed |
| CX486DX | Not installed | Installed | Not installed | Not installed | Installed | Not installed |
| 80486DX | Not installed | Installed | Not installed | Installed | Not installed | Not installed |
| SL80486DX | Not installed | Installed | Not installed | Installed | Not installed | Not installed |
| CX486DX2 | Not installed | Installed | Not installed | Not installed | Installed | Not installed |
| P24D | Not installed | Not installed | Installed | Not installed | Not installed | Installed |
| 80486DX2 | Not installed | Installed | Not installed | Installed | Not installed | Not installed |
| SL80486DX2 | Not installed | Installed | Not installed | Installed | Not installed | Not installed |
| 80486DX4 | Not installed | Installed | Not installed | Installed | Not installed | Not installed |

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| CPU SPEED CONFIGURATION (MX8315) | | | |
|----------------------------------|--------|--------|--------|
| Speed | JP11 | JP12 | JP13 |
| 25MHz | Open | Closed | Open |
| 33MHz | Closed | Closed | Closed |
| 40MHz | Open | Closed | Closed |
| 50iMHz | Open | Closed | Open |
| 50MHz | Closed | Open | Open |
| 66iMHz | Closed | Closed | Closed |
| 75iMHz | Open | Closed | Open |
| 80iMHz | Open | Closed | Closed |
| 100iMHz | Closed | Closed | Closed |

| CPU SPEED CONFIGURATION (MXIMISC466) | | | |
|--------------------------------------|--------|------|--------|
| Speed | JP11 | JP12 | JP13 |
| 25MHz | Open | Open | Open |
| 33MHz | Closed | Open | Closed |
| 40MHz | Open | Open | Closed |
| 50iMHz | Open | Open | Open |
| 50MHz | Closed | Open | Open |
| 66iMHz | Closed | Open | Closed |
| 75iMHz | Open | Open | Open |
| 100iMHz | Closed | Open | Closed |

| CPU SPEED CONFIGURATION (80486DX4 ONLY) | |
|---|-------------------|
| Speed | JP15 |
| Cyrix | pins 1 & 2 closed |
| 2x | pins 2 & 3 closed |
| 3x | Open |

| CPU VOLTAGE CONFIGURATION | | | | |
|---------------------------|--------|--------|--------|-------------------|
| Voltage | JP1 | JP2 | JP3 | JP6 |
| 3v | Open | Open | Open | pins 1 & 2 closed |
| 4v | Closed | Open | Open | pins 2 & 3 closed |
| 5v | Closed | Closed | Closed | Open |

| VESA WAIT STATE CONFIGURATION | |
|-------------------------------|-------------------|
| Wait states | JP4 |
| 0 wait states | pins 1 & 2 closed |
| 1 wait state | pins 2 & 3 closed |

| BUS SPEED CONFIGURATION | |
|-------------------------|-------------------|
| CPU speed | JP5 |
| <= 33MHz | pins 1 & 2 closed |
| > 33MHz | pins 2 & 3 closed |