TMC RESEARCH CORPORATION PCI48AF (VER. 3.0)

Processor SL80486SX/80486SX/SL80486SX2/SL80486DX/80486DX/CX486M7/

TI486DX2/UMCU5/(SL)AM486DX2/AM486DX2/SL80486DX2/80486DX2/CXM1/(SL)AM486

DX4/AM486DX4/80486DX4/P24D/Pentium Overdrive

Processor Speed 25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/100(internal)/

120(internal)MHz

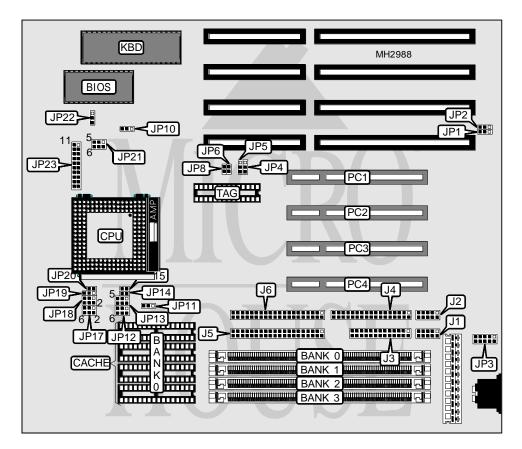
Chip Set ALI Max. Onboard DRAM 128MB

Cache128/256/512KBBIOSUnidentifiedDimensions254mm x 218mm

I/O Options PS/2 mouse interface, parallel port, serial ports (2), 32-bit PCI slots (4), floppy drive

interface, IDE interface (2)

NPU Options None



CONNECTIONS					
Purpose	Location	Purpose	Location		
Serial port 1	J1	Speaker	JP23 pins 1 - 4		
Serial port 2	J2	Turbo switch	JP23 pins 7 & 17		
Parallel port	J3	Turbo LED	JP23 pins 8 & 18		
Floppy drive interface	J4	Reset switch	JP23 pins 9 & 19		
IDE interface 1	J5	Power LED & keylock	JP23 pins 11 - 15		
IDE interface 2	J6	IDE interface LED	JP23 pins 10 & 20		
PS/2 mouse interface	JP3	32-bit PCI slots	PC1 - PC4		

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USER CONFIGURABLE SETTINGS				
Function Jumper Position				
í CMOS memory normal operation	JP22	pins 1 & 2 closed		
CMOS memory clear	JP22	pins 2 & 3 closed		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE	
2MB	(1) 512K x 36	NONE	NONE	NONE	
4MB	(1) 1M x 36	NONE	NONE	NONE	
4MB	(1) 256K x 36				
4MB	(1) 512K x 36	NONE	(1) 256K x 36	(1) 256K x 36	
4MB	(1) 512K x 36	NONE	(1) 512K x 36	NONE	
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE	
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	NONE	
6MB	(1) 512K x 36	NONE	(1) 1M x 36	NONE	
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE	
8MB	(1) 2M x 36	NONE	NONE	NONE	
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36	
10MB	(1) 512K x 36	NONE	(1) 1M x 36	(1) 1M x 36	
10MB	(1) 512K x 36	NONE	(1) 2M x 36	NONE	
12MB	(1) 1M x 36	NONE	(1) 2M x 36	NONE	
16MB	(1) 4M x 36	NONE	NONE	NONE	
16MB	(1) 1M x 36				
16MB	(1) 2M x 36	NONE	(1) 1M x 36	(1) 1M x 36	
16MB	(1) 2M x 36	NONE	(1) 2M x 36	NONE	
17MB	(1) 256K x 36	(1) 4M x 36	NONE	NONE	
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE	
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE	
32MB	(1) 8M x 36	NONE	NONE	NONE	
64MB	(1) 16M x 36	NONE	NONE	NONE	
64MB	(1) 4M x 36				
64MB	(1) 8M x 36	NONE	(1) 4M x 36	(1) 4M x 36	
64MB	(1) 8M x 36	NONE	(1) 8M x 36	NONE	
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE	
128MB	(1) 32M x 36	NONE	NONE	NONE	

CACHE CONFIGURATION				
Size	Bank 0	TAG		
128KB	(4) 32K x 8	(1) 8K x 8		
256KB	(4) 64K x 8	(1) 16K x 8		
512KB	(4) 128K x 8	(1) 32K x 8		

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CACHE JUMPER CONFIGURATION				
Size	JP4	JP5		
128KB	pins 2 & 3 closed	pins 2 & 3 closed		
256KB	pins 2 & 3 closed	pins 1 & 2 closed		
512KB	pins 1 & 2 closed	pins 1 & 2 closed		

CPU TYPE CONFIGURATION					
Туре	JP10	JP11	JP12	JP13	JP14
SL80486SX	1 & 2	1 & 2	3 & 4	3 & 4	Closed
80486SX	1 & 2	1 & 2	Open	1 & 2	Open
SL80486SX2	1 & 2	1 & 2	3 & 4	3 & 4	Closed
SL80486DX	2 & 3	1 & 2	3 & 4	3 & 4	Closed
80486DX	2 & 3	1 & 2	Open	1 & 2	Open
CX486M7	2 & 3	1 & 2	5 & 6	5 & 6	Closed
TI486DX2	2 & 3	1 & 2	5 & 6	5 & 6	Closed
UMC U5	1 & 2	1 & 2	1 & 2	1 & 2	Open
SL AM486DX2	2 & 3	2 & 3	3 & 4	3 & 4	Closed
AM486DX2	2 & 3	1 & 2	Open	1 & 2	Open
SL80486DX2	2 & 3	1 & 2	3 & 4	3 & 4	Closed
80486DX2	2 & 3	1 & 2	Open	1 & 2	Open
CXM1	2 & 3	1 & 2	3 & 4	3 & 4	Closed
SL AM486DX4	2 & 3	2 & 3	3 & 4	3 & 4	Closed
AM486DX4	2 & 3	1 & 2	Open	1 & 2	Closed
80486DX4	2 & 3	1 & 2	3 & 4	3 & 4	Closed
P24D	2 & 3	2 & 3	3 & 4	3 & 4	Closed
P24T	2 & 3	2 & 3	3 & 4	3 & 4	Closed
Note: Pins desig	Note: Pins designated should be in the closed position.				

CPU TYPE CONFIGURATION (CON'T)					
Туре	JP15	JP17	JP18	JP19	JP20
SL80486SX	Open	Open	3 & 4	Open	Closed
80486SX	Open	Open	Open	Open	Closed
SL80486SX2	Open	Open	3 & 4	Open	Closed
SL80486DX	Open	Open	3 & 4	2 & 3	Closed
80486DX	Open	Open	Open	2 & 3	Closed
CX486M7	Open	1 & 2	5 & 6	2 & 3	Open
TI486DX2	Open	1 & 2	5 & 6	2 & 3	Open
UMC U5	Open	Open	1 & 2	Open	Closed
SL AM486DX2	Open	1 & 3, 5 & 6	3 & 4	2 & 3	Closed
AM486DX2	Closed	Open	Open	2 & 3	Closed
SL80486DX2	Open	Open	3 & 4	2 & 3	Closed
80486DX2	Open	Open	Open	2 & 3	Closed
CXM1	Open	5 & 6	3 & 4	2 & 3	Closed
SL AM486DX4	Open	5 & 6	3 & 4	2 & 3	Closed
AM486DX4	Open	Open	Open	2 & 3	Closed

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CPU TYPE CONFIGURATION (CON'T)					
Туре	JP15	JP17	JP18	JP19	JP20
80486DX4	Open	Open	3 & 4	2 & 3	Closed
P24D	Open	5 & 6	3 & 4	2 & 3	Closed
P24T Open Open 3 & 4 1 & 2 Closed					
Note: Pins designated should be in the closed position.					

	CPU SPEED CONFIGURATION				
Speed	JP6	JP8			
25MHz	Open	Open			
33MHz	Closed	Closed			
40MHz	Closed	Open			
50iMHz	Open	Open			
50MHz	Open	Closed			
66iMHz	Closed	Closed			
75iMHz	Open	Open			
80iMHz	Closed	Open			
100iMHz	Closed	Closed			
120iMHz	Closed	Closed			

CPU VOLTAGE CONFIGURATION		
Voltage	JP21	
3.45v	pins 1 & 2 closed	
4v	pins 3 & 4 closed	
5v	pins 5 & 6 closed	

DMA CONFIGURATION				
DMA JP1 JP2				
DMA 1	pins 2 & 3 closed	pins 2 & 3 closed		
DMA 3	pins 1 & 2 closed	pins 1 & 2 closed		