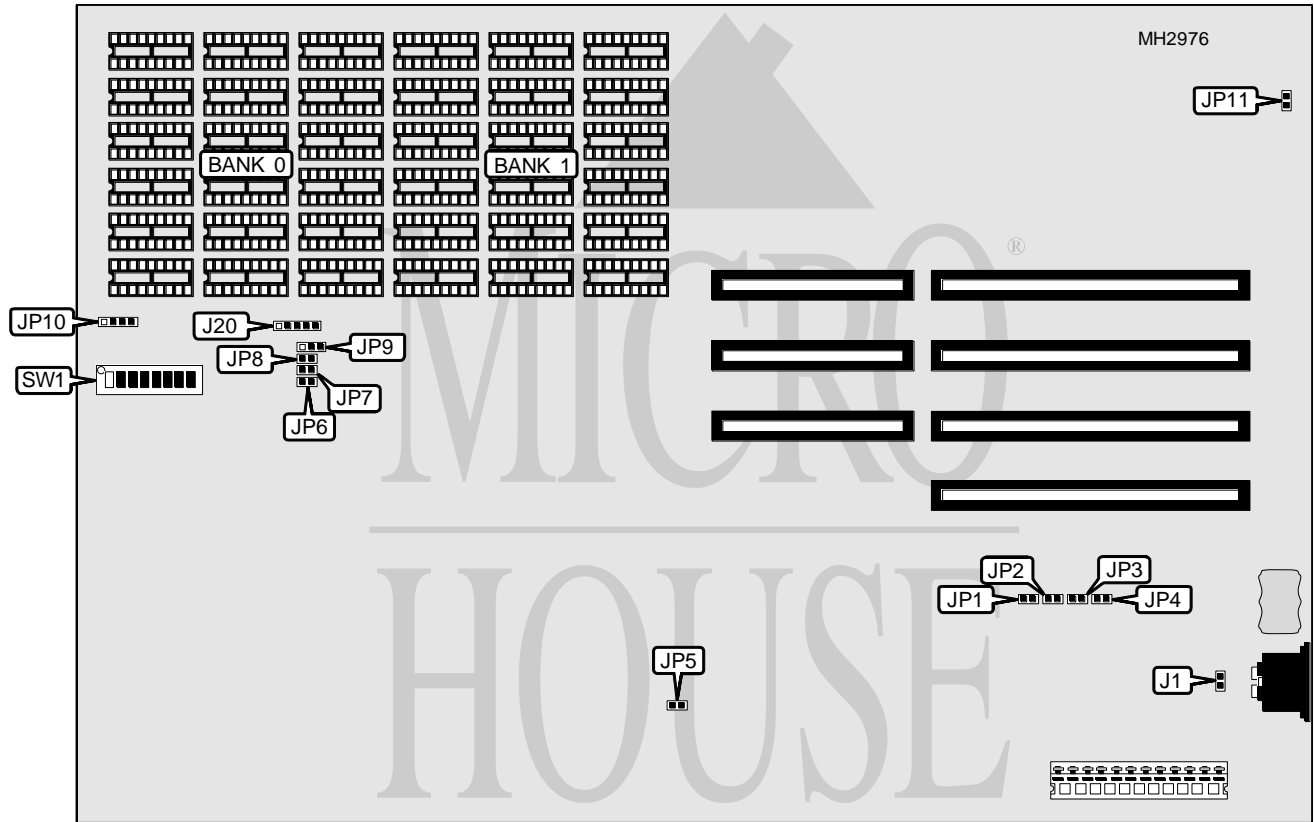


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Processor	80286 (exact location unidentified)
Processor Speed	6/12MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI/Award/Erso/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	JP8
Power LED & keylock	J20	Speaker	JP10
Reset switch	JP6	IDE interface LED	JP11
Turbo switch	JP7		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Turbo type select keyboard control	JP9	pins 1 & 2 closed
Turbo type select dipswitch control	JP9	pins 2 & 3 closed
í Clock speed select slow	SW1/1	On
Clock speed select fast	SW1/1	Off
í Monitor type select color	SW1/2	On
Monitor type select monochrome	SW1/2	Off
í Parity select enabled	SW1/3	On
Parity select disabled	SW1/3	Off
í EMS port address select 0E8H	SW1/4	On
EMS port address select 098H	SW1/4	Off
í PROM type select 27128	SW1/5	On
PROM type select 27258	SW1/5	Off

DRAM CONFIGURATION			
Size	Mode	Bank 0	Bank 1
512KB	0	(18) 41256KB	NONE
640KB	1	(18) 41256KB	(18) 4164KB
1MB (640 + 384KB)	2	(18) 41256KB	(18) 41256KB
1MB (640 + 384KB EMS)	3	(18) 41256KB	(18) 41256KB
2MB (640 + 1408KB)	4	(18) 411000KB	NONE
2MB (640 + 1408KB EMS)	5	(18) 411000KB	NONE
4MB (640 + 1408KB)	6	(18) 411000KB	(18) 411000KB
4MB (640 + 1408KB EMS)	7	(18) 411000KB	(18) 411000KB

DRAM SWITCH CONFIGURATION			
Mode	SW1/6	SW1/7	SW1/8
0	On	On	On
1	On	On	Off
2	On	Off	On
3	On	Off	Off
4	Off	On	On
5	Off	On	Off
6	Off	Off	On
7	Off	Off	Off

KEYBOARD BIOS CONFIGURATION				
Select	JP1	JP2	JP3	JP4
8742 Pin32	Closed	Open	Open	Open
8742 Pin27	Open	Closed	Open	Open
8742 Pin23	Open	Open	Closed	Open
8742 Pin30	Open	Open	Open	Closed

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VESA WAIT STATE CONFIGURATION	
Wait states	JP5
0 wait states	On
1 wait state	Off