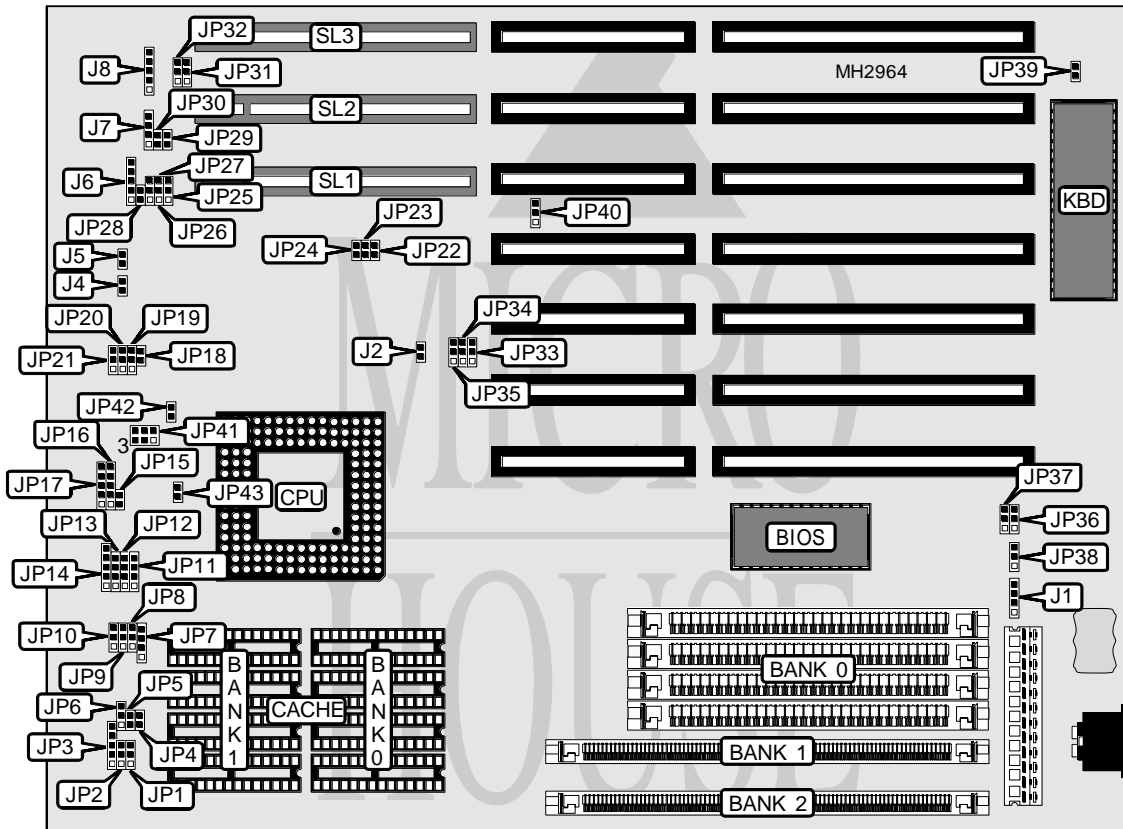


VISION TECHNOLOGIES SIS 486-471 VL BUS, VLT MB

Processor	CX486S/80486SX/SL80486SX/CX486DX/AM486DX/80486DX/AM486DXL/ SL80486DX/CX486DX2/AM486DX2/AM486DXL2/80486DX2/SL80486DX2/ 80486DX4
Processor Speed	20/25/33/40/50(internal)/66(internal)/75(internal)/100(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	64/128/256/512/1024KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Speaker	J7
Green PC connector (VGA)	J2	Power LED & keylock	J8
Reset switch	J4	Green PC connector	JP29
Turbo LED	J5	32-bit VESA local bus slots	SL1 - SL3
Turbo switch	J6 pins 2 & 3		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP20	N/A
Factory configured - do not alter	JP21	N/A
í Factory configured - do not alter	JP30	Open
í Factory configured - do not alter	JP35	N/A
í CMOS memory normal operation	JP38	pins 1 & 2 closed
CMOS memory clear	JP38	pins 2 & 3 closed
í Monitor type select monochrome/EGA/VGA	JP39	Open
Monitor type select CGA	JP39	Closed
í RC pin for break switch enabled	JP40	pins 1 & 2 closed
RC pin used for RC	JP40	pins 2 & 3 closed

DRAM CONFIGURATION 1		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(1) 256K x 36
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(1) 1M x 36
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36
64MB	(4) 8M x 9	(1) 8M x 36
128MB	(4) 16M x 9	(1) 16M x 36

DRAM CONFIGURATION 2			
Size	Bank 0	Bank 1	Bank 2
1MB	(4) 256K x 9	NONE	NONE
2MB	(4) 256K x 9	(1) 256K x 36	NONE
4MB	(4) 1M x 9	NONE	NONE
5MB	(4) 256K x 9	(1) 1M x 36	NONE
6MB	(4) 256K x 9	(1) 256K x 36	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE
17MB	(4) 256K x 9	(1) 4M x 36	NONE
18MB	(4) 256K x 9	(1) 256K x 36	(1) 4M x 36
20MB	(4) 1M x 9	(1) 4M x 36	NONE
24MB	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	NONE
34MB	(4) 256K x 9	(1) 256K x 36	(1) 8M x 36
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
40MB	(4) 1M x 9	(1) 1M x 36	(1) 8M x 36
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	(1) 8M x 36	NONE
64MB	(4) 4M x 9	(1) 4M x 36	(1) 8M x 36

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DRAM CONFIGURATION 2 (CON'T)			
Size	Bank 0	Bank 1	Bank 2
64MB	(4) 16M x 9	NONE	NONE
68MB	(4) 1M x 9	(1) 8M x 36	(1) 8M x 36
80MB	(4) 4M x 9	(1) 8M x 36	(1) 8M x 36
96MB	(4) 16M x 9	(1) 8M x 36	NONE
128MB	(4) 16M x 9	(1) 8M x 36	(1) 8M x 36

DRAM CONFIGURATION 3			
Size	Bank 0	Bank 1	Bank 2
1MB	NONE	(1) 256K x 36	NONE
2MB	NONE	(1) 256K x 36	(1) 256K x 36
4MB	NONE	(1) 1M x 36	NONE
5MB	NONE	(1) 256K x 36	(1) 1M x 36
6MB	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
8MB	NONE	(1) 2M x 36	NONE
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE
16MB	NONE	(1) 4M x 36	NONE
17MB	NONE	(1) 256K x 36	(1) 4M x 36
18MB	(4) 4M x 9	(1) 256K x 36	(1) 256K x 36
20MB	NONE	(1) 1M x 36	(1) 4M x 36
24MB	(4) 4M x 9	(1) 1M x 36	(1) 1M x 36
32MB	NONE	(1) 4M x 36	(1) 4M x 36
32MB	NONE	(1) 8M x 36	NONE
36MB	(4) 4M x 9	(1) 1M x 36	(1) 4M x 36
36MB	NONE	(1) 1M x 36	(1) 8M x 36
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
48MB	NONE	(1) 4M x 36	(1) 8M x 36
64MB	(4) 16M x 9	NONE	NONE
72MB	(4) 16M x 9	(1) 1M x 36	(1) 1M x 36
96MB	(4) 16M x 9	(1) 4M x 36	(1) 4M x 36
128MB	(4) 16M x 9	(1) 8M x 36	(1) 8M x 36

DRAM JUMPER CONFIGURATION		
Configuration	JP36	JP37
1	pins 1 & 2 closed	Open
2	pins 1 & 2 closed	pins 2 & 3 closed
3	pins 2 & 3 closed	pins 1 & 2 closed

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K or (1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(8) 64K x 8	NONE	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

Note: The location of the TAG is unidentified.

CACHE JUMPER CONFIGURATION							
Size	JP1	JP2	JP3	JP25	JP26	JP27	JP28
64KB	1 & 2	2 & 3	2 & 3, 4 & 5	1 & 2	1 & 2	1 & 2	Open
128KB	2 & 3	1 & 2	1 & 2, 3 & 4	2 & 3	1 & 2	1 & 2	Open
256KB	2 & 3	2 & 3	2 & 3, 4 & 5	2 & 3	Open	Open	Closed
256KB	2 & 3	2 & 3	2 & 3, 4 & 5	2 & 3	2 & 3	1 & 2	Open
512KB	2 & 3	1 & 2	1 & 2, 3 & 4	2 & 3	2 & 3	2 & 3	Open
1MB	2 & 3	2 & 3	2 & 3, 4 & 5	2 & 3	2 & 3	2 & 3	Open

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION					
Type	JP4	JP5	JP6	JP7	JP8
CX486SX	Open	Open	Open	2 & 3	2 & 3
80486SX	Open	Open	Open	3 & 4	Open
SL80486SX	Open	Open	Open	1 & 2, 3 & 4	Open
CX486DX	Open	Open	Open	1 & 2, 3 & 4	2 & 3
80486DX	Open	Open	Open	1 & 2, 3 & 4	Open
SL80486DX	Open	Open	Open	1 & 2, 3 & 4	Open
CX486DX2-66	Open	Open	Open	1 & 2, 3 & 4	2 & 3
CX486DX2-80	Open	Open	Open	1 & 2, 3 & 4	2 & 3
AM486DX2	Open	Open	Open	1 & 2, 3 & 4	Open
AM486DXL2	Open	Open	Open	1 & 2, 3 & 4	Open
80486DX2	Open	Open	Open	1 & 2, 3 & 4	Open
SL80486DX2	Open	Open	Open	1 & 2, 3 & 4	Open
80486DX4	Open	Open	Open	1 & 2, 3 & 4	Open

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)					
Type	JP9	JP10	JP11	JP12	JP13
CX486SX	2 & 3	1 & 2	2 & 3	Open	1 & 2, 3 & 4
80486SX	2 & 3	1 & 2	Open	Open	Open
SL80486SX	2 & 3	1 & 2	Open	Open	2 & 3
CX486DX	2 & 3	1 & 2	2 & 3	3 & 4	1 & 2, 3 & 4
80486DX	2 & 3	1 & 2	Open	3 & 4	Open
SL80486DX	2 & 3	1 & 2	Open	3 & 4	2 & 3
CX486DX2-66	2 & 3	1 & 2	2 & 3	3 & 4	1 & 2, 3 & 4
CX486DX2-80	2 & 3	1 & 2	2 & 3	3 & 4	1 & 2, 3 & 4
AM486DX2	2 & 3	1 & 2	Open	3 & 4	Open
AM486DXL2	2 & 3	2 & 3	3 & 4	1 & 2, 3 & 4	Open
80486DX2	2 & 3	1 & 2	Open	3 & 4	Open
SL80486DX2	2 & 3	1 & 2	Open	3 & 4	2 & 3
80486DX4	2 & 3	1 & 2	Open	3 & 4	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)						
Type	JP14	JP15	JP16	JP17	JP18	JP19
CX486SX	2 & 3, 4 & 5	Open	Open	2 & 3	Closed	2 & 3
80486SX	Open	Open	Open	Open	Closed	2 & 3
SL80486SX	3 & 4	Open	1 & 2	4 & 5	Closed	2 & 3
CX486DX	2 & 3, 4 & 5	Open	Open	2 & 3	Closed	2 & 3
80486DX	Open	Open	Open	Open	Closed	2 & 3
SL80486DX	3 & 4	Open	1 & 2	4 & 5	Closed	2 & 3
CX486DX2-66	2 & 3, 4 & 5	Open	Open	2 & 3	Closed	2 & 3
CX486DX2-80	2 & 3	Open	Open	2 & 3	Closed	2 & 3
AM486DX2	Open	Open	Open	Open	Closed	2 & 3
AM486DXL2	Open	Open	Open	Open	Closed	2 & 3
80486DX2	Open	Open	Open	Open	Closed	2 & 3
SL80486DX2	3 & 4	Open	1 & 2	4 & 5	Closed	2 & 3
80486DX4	3 & 4	Open	1 & 2	4 & 5	Open	2 & 3

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)					
Type	JP33	JP34	JP41	JP42	JP43
CX486SX	1 & 2	2 & 3	3 & 4, 5 & 6	Open	Open
80486SX	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
SL80486SX	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
CX486DX	1 & 2	2 & 3	3 & 4, 5 & 6	Open	Open
80486DX	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
SL80486DX	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
CX486DX2-66	1 & 2	2 & 3	1 & 2, 3 & 4	Closed	Open
CX486DX2-80	1 & 2	2 & 3	1 & 2, 3 & 4	Closed	Open
AM486DX2	2 & 3	1 & 2	1 & 2, 3 & 4	Open	Closed
AM486DXL2	1 & 2	2 & 3	3 & 4, 5 & 6	Open	Open
80486DX2	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
SL80486DX2	2 & 3	1 & 2	3 & 4, 5 & 6	Open	Open
80486DX4	2 & 3	1 & 2	1 & 2, 3 & 4	Closed	Open

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION (AV9107)			
Speed	JP22	JP23	JP24
20MHz	Closed	Open	Closed
25MHz	Closed	Closed	Open
33MHz	Open	Closed	Closed
40MHz	Closed	Open	Open
50iMHz	Closed	Closed	Open
50MHz	Open	Open	Closed
66iMHz	Open	Closed	Closed
75iMHz	Closed	Closed	Open
100iMHz	Open	Closed	Closed

CPU SPEED CONFIGURATION (MX8315)			
Speed	JP22	JP23	JP24
20MHz	Open	Open	Open
25MHz	Open	Closed	Open
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Closed	Open
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
75iMHz	Open	Closed	Open
100iMHz	Closed	Closed	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP31
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

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BUS SPEED CONFIGURATION	
CPU speed	JP32
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed