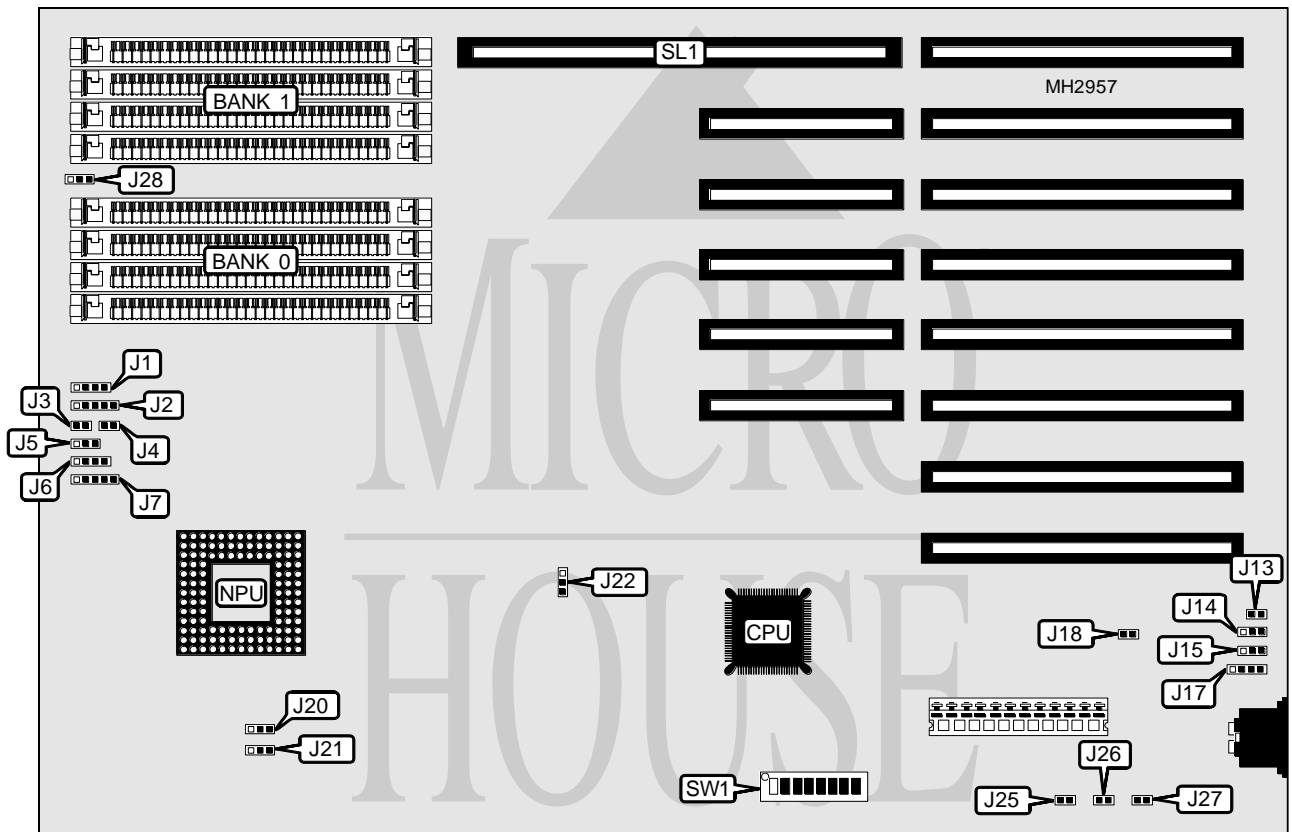


DIAMOND FLOWER, INC.

DFI 386-33UC, DFI-386-25UC

Processor	80386DX
Processor Speed	25/33MHz
Chip Set	UMC
Max. Onboard DRAM	12MB (4MB on external memory card)
Cache	32KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	External memory card
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
CPU clock indicator connector	J1	Power LED & keylock	J7
Auxiliary keyboard connector	J2	Reset switch	J13
Reset switch	J3	External battery	J17
Turbo LED	J4	Riser Card	SL1
Speaker	J6		

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DFI 386-33UC, DFI-386-25UC

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Battery type select internal	J14	pins 2 & 3 closed
Battery type select external	J14	pins 1 & 2 closed
í Monitor type select monochrome	J18	Open
Monitor type select color	J18	Closed
í Factory configured - do not alter	J20	pins 2 & 3 closed
í Factory configured - do not alter (386-33UC only)	J21	pins 1 & 2 closed
í Cache enabled	J22	pins 1 & 2 closed
Cache disabled	J22	pins 2 & 3 closed
í Factory configured - do not alter	J25	Closed
í Factory configured - do not alter	J26	Closed
í NPU type select Weitek 3167 or none installed	J27	Closed
NPU type select 80387 or Weitek 3167 installed	J27	Open
í Factory configured - do not alter	J28	pins 2 & 3 closed
í DRAM type select 1M x 9	SW1/1	Off
DRAM type select 256K x 9	SW1/1	On
í Factory configured - do not alter	SW1/4	On
í Factory configured - do not alter	SW1/5	On
í Factory configured - do not alter	SW1/6	On
í Factory configured - do not alter	SW1/7	On
í Factory configured - do not alter	SW1/8	On

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
1MB	(4) 256K x 9	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

Note: Bank 3 is located on the external memory card.

DRAM BANK CONFIGURATION		
Bank	SW1/2	SW1/3
0, 1, 2	Off	Off
0, 1	Off	On
0	On	On

CACHE CONFIGURATION
Note: The location of the 32KB cache is unidentified.

CPU SPEED CONFIGURATION		
Speed	J5	J15
High (cache installed)	pins 1 & 2 closed	pins 1 & 2 closed
Low (cache not installed)	pins 2 & 3 closed	pins 2 & 3 closed