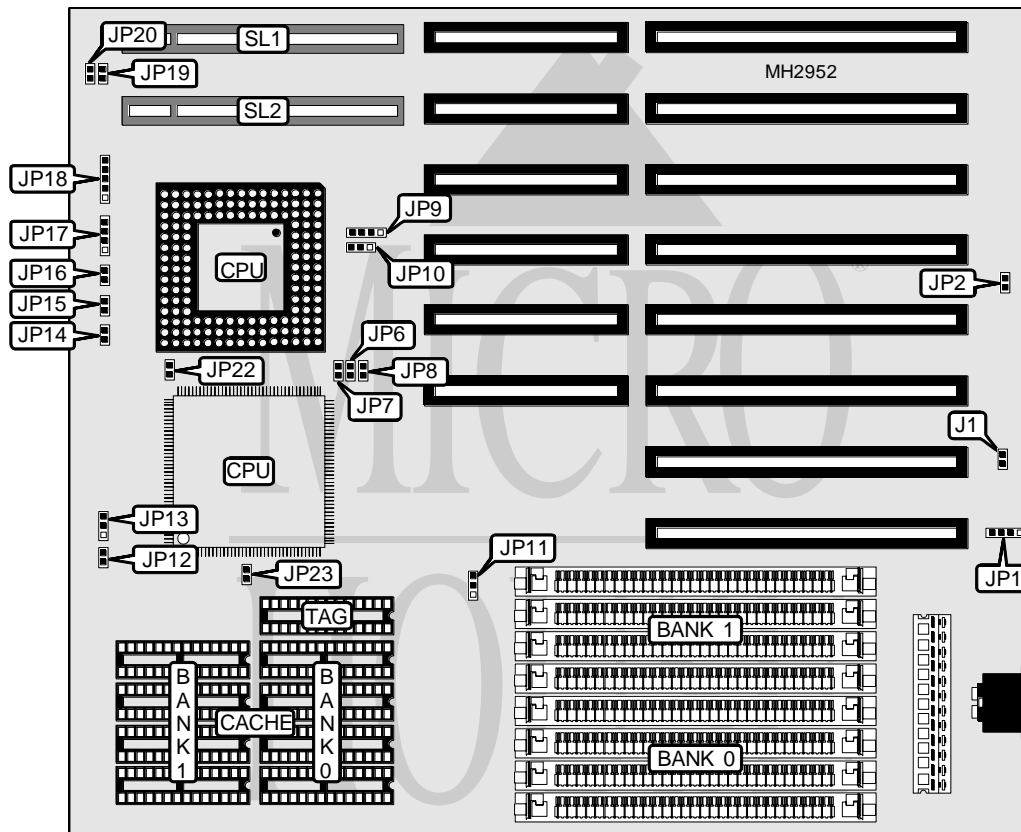


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486 V L I

Processor	80486SX/80487SX/80487SX2/80486DX/80486DX2/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP1	Speaker	JP17
Reset switch	JP14	Power LED & keylock	JP18
Turbo switch	JP15	32-bit VESA local bus slots	SL1 & SL2
Turbo LED	JP16		

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í Factory configured - do not alter	J1	N/A
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP11	N/A

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP12	JP13
64KB	Open	Open
128KB	Closed	pins 1 & 2 closed
256KB	Closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION

Type	JP9	JP10	JP22	JP23
80486SX (PQFP)	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Closed	Open
80486SX	pins 2 & 3 closed	Open	Open	Closed
80487SX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	Closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Open	Closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Open	Closed
Pentium Overdrive	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open	Closed

CPU SPEED CONFIGURATION

Speed	JP6	JP7	JP8
20MHz	Open	Closed	Closed
25MHz	Closed	Open	Closed
33MHz	Closed	Closed	Open
40MHz	Open	Open	Closed
50iMHz	Closed	Open	Closed
50MHz	Open	Closed	Open
66iMHz	Closed	Closed	Open

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VESA WAIT STATE CONFIGURATION	
Wait states	JP19
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP20
<= 33MHz	Open
> 33MHz	Closed