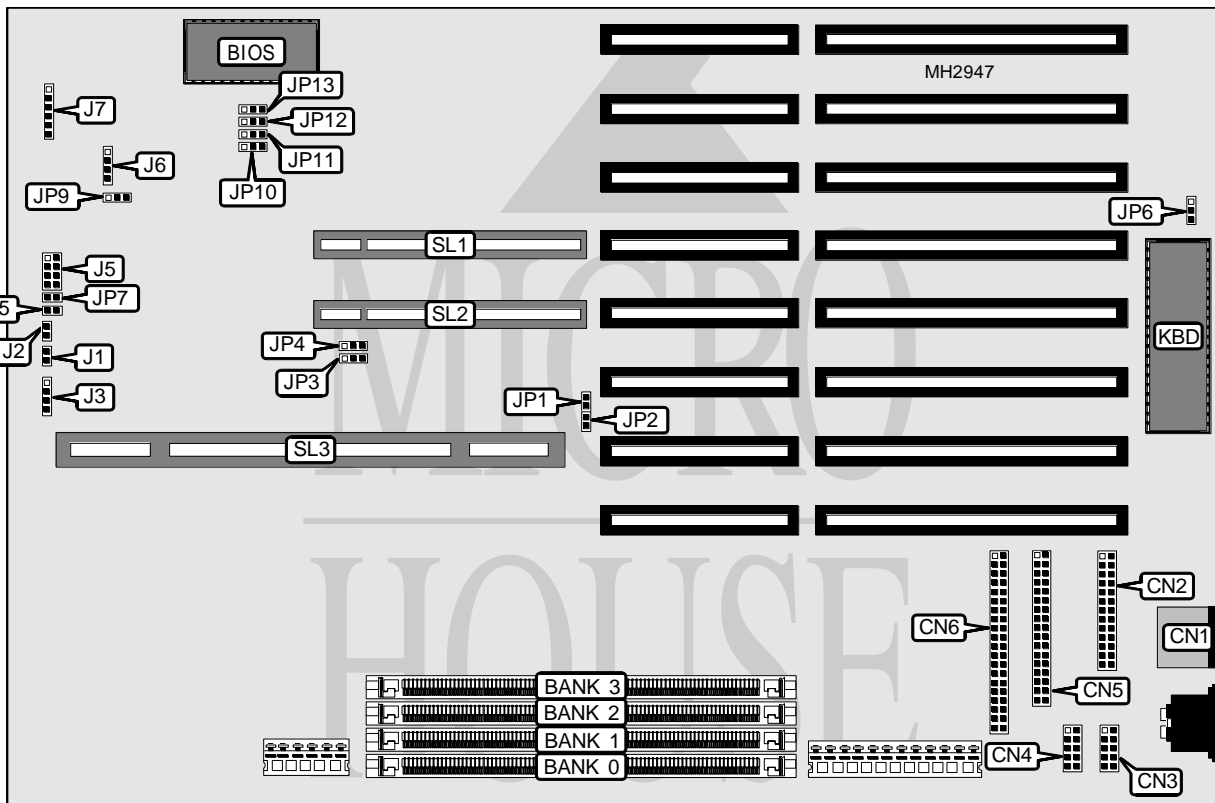


ICL TEAMSERVER EI SERIES

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2/Pentium Overdrive/Pentium (depends on CPU card installed)	
Processor Speed	25/33/40/50(internal)/50/60/66(internal)/66/80(internal)MHz	(depends on CPU card installed)
Chip Set	OPTI	
Max. Onboard DRAM	256MB (depends on CPU card installed)	
Cache	64/128/256/1024KB (depends on CPU card installed)	
BIOS	Unidentified	
Dimensions	330mm x 218mm	
I/O Options	PS/2 mouse port, parallel port, serial ports (2), floppy drive interface, IDE interface, CPU slot, 32-bit VESA local bus slots (2)	
NPU Options	None	



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	IDE interface LED	J3
Parallel port	CN2	Power on LED	J5
Serial port 1	CN3	Speaker (not used)	J6
Serial port 2	CN4	Keyboard/mouse connector (not used)	J7
Floppy drive interface	CN5	Reset switch	JP7
IDE interface	CN6	32-bit VESA local bus slots	SL1 & SL2
Chassis fan power	J1	CPU slot	SL3
CPU fan power	J2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP5	Closed
í Security disabled	JP6	pins 1 & 2 closed
Security enabled	JP6	pins 2 & 3 closed
í Speaker type select internal	JP9	pins 2 & 3 closed
Speaker type select external	JP9	pins 1 & 2 closed

Note: To clear CMOS memory, remove jumper on JP6. Connect middle pin on JP6 to pin 2 on CN2.

DRAM CONFIGURATION (E180i CPU BOARD INSTALLED ONLY)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
28MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36

DRAM CONFIGURATION (E380i/E430i/E480i CPU BOARD INSTALLED ONLY)		
Size	Bank 0	Bank 1
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: If one of the above CPU boards are installed, Bank 0 = Banks 1 & 2 and Bank 1 = 2 & 3 on the mainboard.

CMOS CONFIGURATION				
Type	JP10	JP11	JP12	JP13
Dallas	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Benchmark	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

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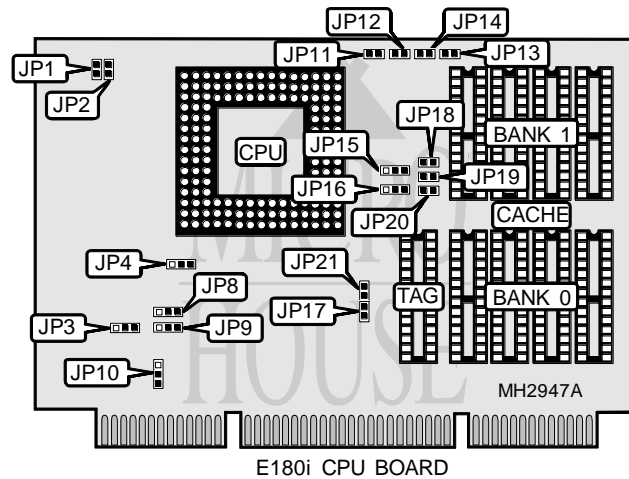
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VESA WAIT STATE CONFIGURATION	
Wait states	JP3
0 wait states	pins 2 & 3 closed
1 wait state	pins 1 & 2 closed

BUS SPEED CONFIGURATION	
CPU speed	JP4
<= 33MHz	pins 2 & 3 closed
> 33MHz	pins 1 & 2 closed

ON BOARD SCSI CONFIGURATION		
Setting	JP1	JP2
Disabled	Closed	Closed
Enabled	Open	Open

ON BOARD SCSI CONFIGURATION
 Note: Mainboard has a mix of EIAS and ISA slots. The location of the EISA slots are unidentified.



USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í LADS setting for VESA local bus 33MHz	JP3	pins 1 & 2 closed
LADS setting for VESA local bus 50MHz	JP3	pins 2 & 3 closed
í Local bus enabled	JP10	pins 2 & 3 closed
Local bus disabled	JP10	pins 1 & 2 closed
í Factory configured - do not alter	JP15	N/A
í Factory configured - do not alter	JP16	N/A
í Factory configured - do not alter	JP17	N/A

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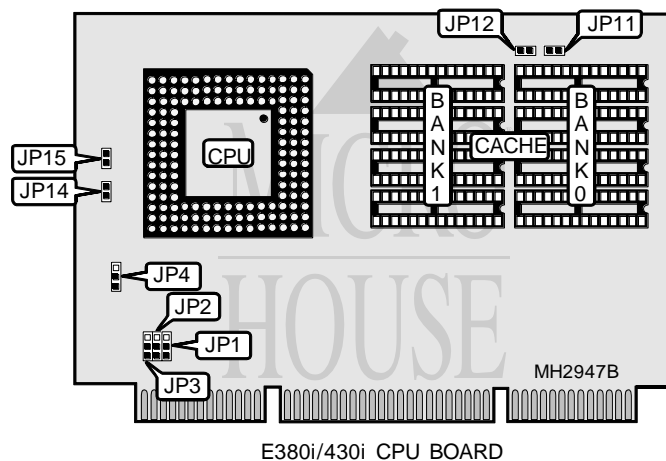
CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP11	JP12	JP13	JP14	JP18	JP19	JP20
None	Open	Open	Open	Open	Open	Open	Open
64KB	Closed	Open	Open	Open	Open	Closed	Open
128KB	Closed	Closed	Open	Open	Open	Closed	Closed
256KB	Closed	Closed	Closed	Closed	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION	
Type	JP4
80486SX	pins 1 & 2 closed
ODP486SX	pins 2 & 3 closed
80487SX	pins 2 & 3 closed
80486DX	Open
80486DX2	pins 2 & 3 closed
Pentium Overdrive	Open

CPU SPEED CONFIGURATION				
Speed	JP1	JP2	JP8	JP9
25MHz	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
33MHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	Closed	Open	pins 2 & 3 closed	pins 1 & 2 closed
50iMHz	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
50MHz	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed
66iMHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed



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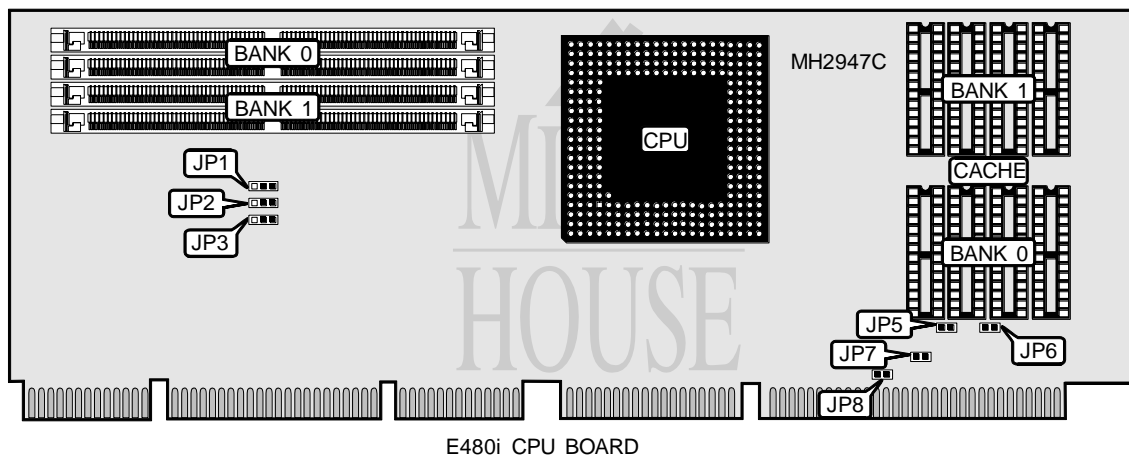
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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Local bus enabled	JP4	pins 2 & 3 closed
Local bus disabled	JP4	pins 1 & 2 closed

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(4) 32K x 8	(4) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8

CACHE JUMPER CONFIGURATION				
Size	JP11	JP12	JP14	JP15
256KB	Open	Open	Open	Open
1MB	Closed	Closed	Closed	Closed

CPU SPEED CONFIGURATION			
Speed	JP1	JP2	JP3
33MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
60MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
66MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
80MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed



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DRAM CONFIGURATION (E480i CPU BOARD ONLY)		
Size	Bank 0	Bank 1
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(4) 32K x 8	(4) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8

CACHE JUMPER CONFIGURATION				
Size	JP5	JP6	JP7	JP8
256KB	Open	Open	Open	Open
1MB	Closed	Closed	Closed	Closed

CPU SPEED CONFIGURATION			
Speed	JP1	JP2	JP3
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
66MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed