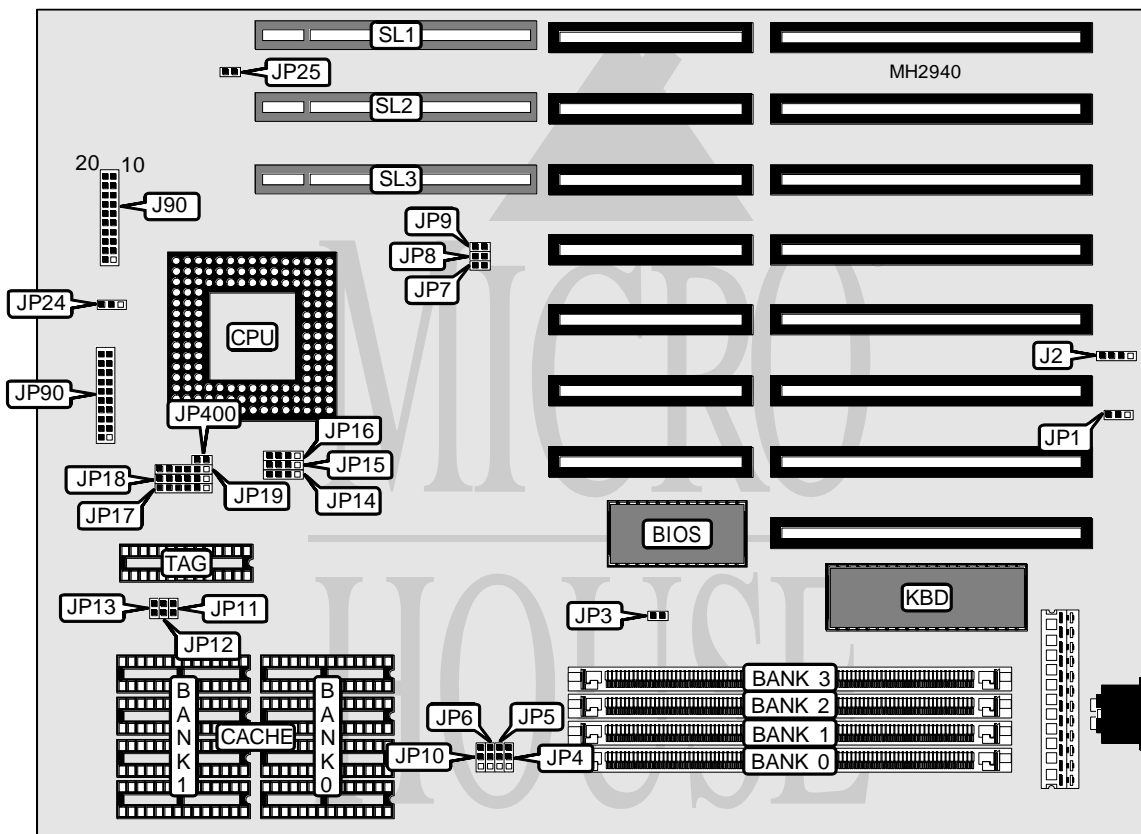


# RELIALOGIC CORPORATION PRIVATE, LTD.

## R 4 V L U G 2

<b>Processor</b>	CX486M6/80486SX/SL80486SX/80486SX2/SL80486SX2/80487SX/CX486DX/AM486DX/UMCU5/80486DX/SL80486DX/CX486DX2/AM486DX2/80486DX2/SL80486DX2/AM486DX4/80486DX4
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
<b>Chip Set</b>	UMC
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	128/256/512/1024KB
<b>BIOS</b>	Phoenix
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (3), green PC connector
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Speaker	J90 pins 17 - 20
Turbo LED	J90 pins 2 & 3	Power LED & keylock	J90 pins 11 - 15
Green PC connector	J90 pins 4 & 5	CPU voltage regulator	JP90
Turbo switch	J90 pins 6 & 7	32-bit VESA local bus slots	SL1 - SL3
Reset switch	J90 pins 9 & 10		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Flash BIOS voltage select 5v	JP1	pins 1 & 2 closed
Flash BIOS voltage select 12v	JP1	pins 2 & 3 closed
í Keyboard type select internal	JP3	Closed
Keyboard type select external	JP3	Open
í 80486DX4 clock multiplier select 2x	JP24	pins 2 & 3 closed
80486DX4 clock multiplier select 3x	JP24	Open
í AM486DX4 clock multiplier select 2x	JP400	Closed
AM486DX4 clock multiplier select 3x	JP400	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	NONE	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
2MB	NONE	NONE	NONE	(1) 512K x 36
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	NONE	NONE	(1) 512K x 36	(1) 512K x 36
4MB	(1) 1M x 36	NONE	NONE	NONE
5MB	(1) 1M x 36	(1) 256K x 36	NONE	NONE
6MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	NONE
7MB	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	NONE	NONE	NONE	(1) 2M x 36
9MB	(1) 1M x 36	(1) 1M x 36	(1) 256K x 36	NONE
10MB	(1) 1M x 36	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
13MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 256K x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	NONE	NONE	(1) 2M x 36	(1) 2M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
17MB	(1) 4M x 36	(1) 256K x 36	NONE	NONE
18MB	(1) 4M x 36	(1) 256K x 36	(1) 256K x 36	NONE
19MB	(1) 4M x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
20MB	(1) 4M x 36	(1) 1M x 36	NONE	NONE
21MB	(1) 4M x 36	(1) 1M x 36	(1) 256K x 36	NONE
22MB	(1) 4M x 36	(1) 1M x 36	(1) 256K x 36	(1) 256K x 36
24MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	NONE
25MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 256K x 36
28MB	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB	(4) 64K x 8	NONE	(1) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 64K x 8

CACHE JUMPER CONFIGURATION							
Size	JP4	JP5	JP6	JP10	JP11	JP12	JP13
128KB	1 & 2	1 & 2	Open	Open	Open	Open	Open
256KB	1 & 2	1 & 2	1 & 2	Open	Open	Open	Closed
256KB	2 & 3	2 & 3	Open	Open	Open	Open	Closed
512KB	2 & 3	2 & 3	2 & 3	Open	Closed	Open	Closed
512KB	1 & 2	2 & 3	2 & 3	1 & 2	Closed	Open	Closed
1MB	2 & 3	2 & 3	2 & 3	2 & 3	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP14	JP15	JP16	JP17
CX486M6	2 & 3, 4 & 5	2 & 3	Open	2 & 3, 4 & 5
80486SX	Open	2 & 3	Open	Open
SL80486SX	1 & 2, 3 & 4	2 & 3	Open	5 & 6
80486SX2	Open	2 & 3	Open	Open
SL80486SX2	1 & 2, 3 & 4	2 & 3	Open	5 & 6
CX486DX	2 & 3	1 & 2, 3 & 4	1 & 2	2 & 3, 4 & 5
AM486DX	Open	1 & 2, 3 & 4	1 & 2	Open
UMC U5	Open	2 & 3	3 & 4	1 & 2
80486DX	Open	1 & 2, 3 & 4	1 & 2	Open
SL8046DX	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	5 & 6
CX486DX2	2 & 3	1 & 2, 3 & 4	1 & 2	2 & 3, 4 & 5
AM486DX2	Open	1 & 2, 3 & 4	1 & 2	Open
80486DX2	Open	1 & 2, 3 & 4	1 & 2	Open
SL80486DX2	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	5 & 6
AM486DX4	Open	1 & 2, 3 & 4	1 & 2	Open
80486DX4	1 & 2, 3 & 4	1 & 2, 3 & 4	1 & 2	5 & 6

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)		
Type	JP18	JP19
CX486M6	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4, 5 & 6 closed
80486SX	pins 2 & 3 closed	Open
SL80486SX	pins 1 & 2 closed	pins 1 & 2 closed
80486SX2	pins 2 & 3 closed	Open
SL80486SX2	pins 1 & 2 closed	pins 1 & 2 closed
CX486DX	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
AM486DX	pins 2 & 3 closed	Open
UMC U5	pins 2 & 3 closed	pins 2 & 3 closed
80486DX	pins 2 & 3 closed	Open
SL8046DX	pins 1 & 2 closed	pins 1 & 2 closed
CX486DX2	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 1 & 2, 3 & 4 closed
AM486DX2	2 & 3	Open
80486DX2	pins 2 & 3 closed	Open
SL80486DX2	pins 1 & 2 closed	pins 1 & 2 closed
AM486DX4	pins 2 & 3 closed	Open
80486DX4	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION			
Speed	JP7	JP8	JP9
25MHz	Closed	Open	Open
33MHz	Closed	Closed	Closed
40MHz	Closed	Closed	Open
50iMHz	Closed	Open	Open
50MHz	Open	Open	Closed
66iMHz	Closed	Closed	Closed
75iMHz	Closed	Open	Open
100iMHz	Closed	Closed	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP26
0 wait states	Open
1 wait state	Closed
Note: The location of JP26 is unidentified.	

BUS SPEED CONFIGURATION	
CPU speed	JP25
<= 33MHz	Open
> 33MHz	Closed