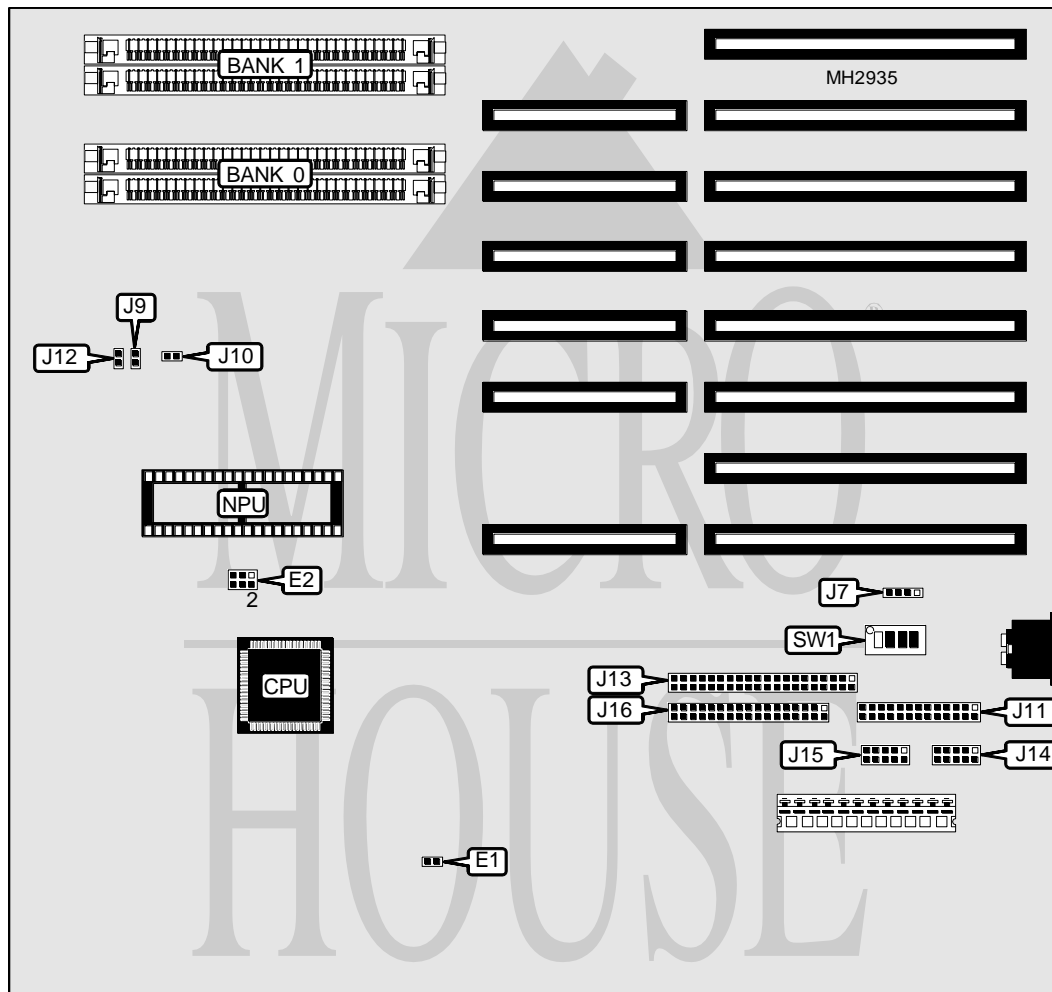


TANDON CORPORATION P C A / 1 2

Processor	80286
Processor Speed	12MHz
Chip Set	Unidentified
Max. Onboard DRAM	4MB
Cache	None
BIOS	Unidentified
Dimensions	254mm x 220mm
I/O Options	Parallel port, serial ports (2), floppy drive interface, IDE interface
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J7	IDE interface	J13
Speaker	J9	Serial port 2	J14
Reset switch	J10	Serial port 1	J15
Parallel port	J11	Floppy drive interface	J16
IDE interface LED	J12		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í High speed clock disabled	E1	Closed
High speed clock enabled	E1	Open
í NPU disabled	E2	pins 1 & 2 open
NPU enabled	E2	pins 1 & 2 closed
í Factory configured - do not alter	E3	pins 2 & 3 closed
í 256KB - 512KB memory enabled	SW1/1	Off
256KB - 512KB memory disabled	SW1/1	On
í 512KB - 640KB memory enabled	SW1/2	Off
512KB - 640KB memory disabled	SW1/2	On
í Security enabled	SW1/3	Off
Security disabled	SW1/3	On
í Factory configured - do not alter	SW1/4	Off

Note: The location of E3 is unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9