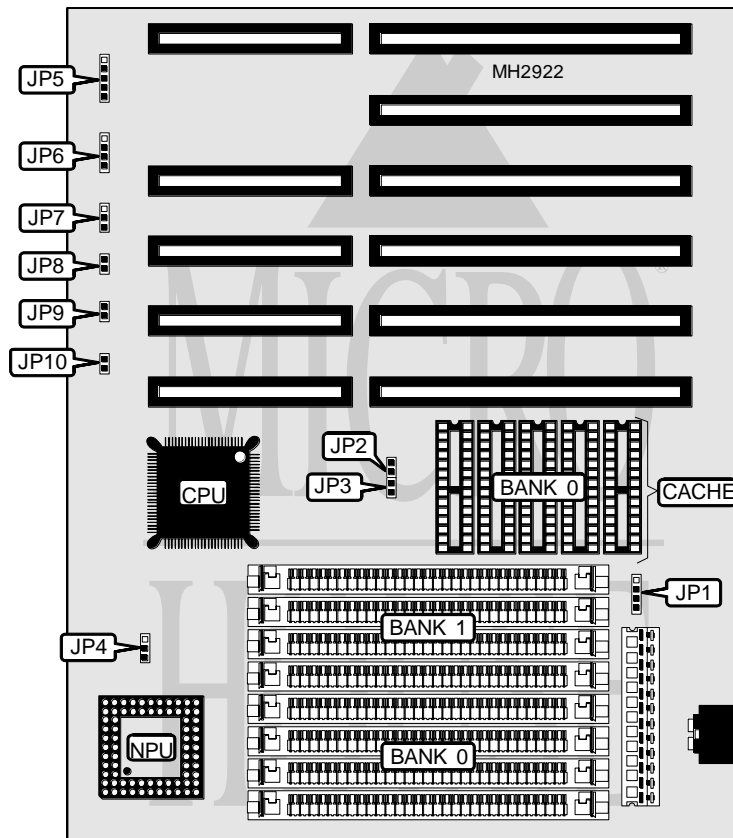


# UNIDENTIFIED DLC/DX-5800

**Processor** 80386DX/CX486DLC/TI486DLC  
**Processor Speed** 33/40MHz  
**Chip Set** Unidentified  
**Max. Onboard DRAM** 32MB  
**Cache** 128KB  
**BIOS** Award  
**Dimensions** 220mm x 220mm  
**I/O Options** None  
**NPU Options** 80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED	JP8
Power LED & keylock	JP5	Turbo switch	JP9
Speaker	JP6	Reset switch	JP10

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# UNIDENTIFIED

## DLC/DX-5800

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU disabled	JP7	pins 1 & 2 closed
NPU enabled	JP7	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION	
Size	Bank 0
128KB	(5) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP2	JP3
128KB	Closed	Closed

CPU SPEED CONFIGURATION	
Speed	JP4
33MHz	pins 1 & 2 closed
40MHz	pins 2 & 3 closed