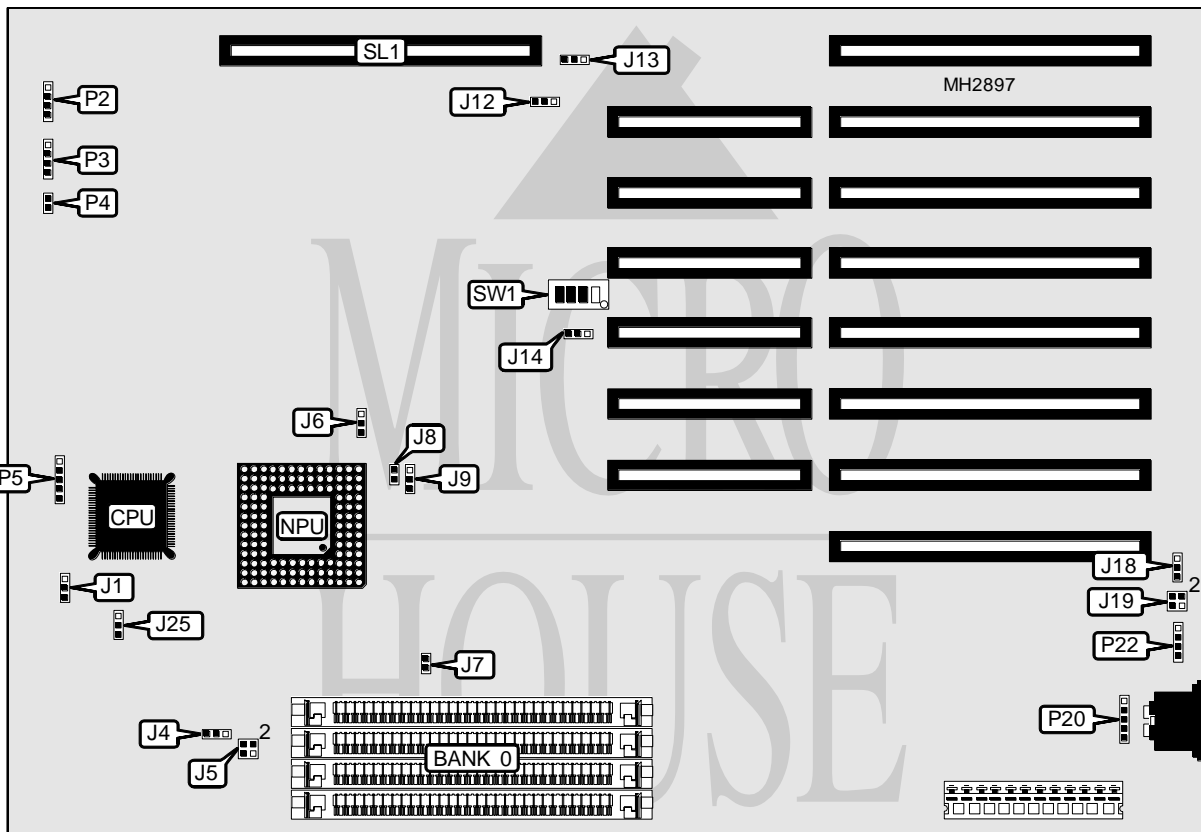


# INFORMTECH INTERNATIONAL, INC.

## IT 3 8 6

<b>Processor</b>	80386SX
<b>Processor Speed</b>	25/33MHz
<b>Chip Set</b>	TI
<b>Max. Onboard DRAM</b>	16MB (12MB on external memory card)
<b>Cache</b>	32KB
<b>BIOS</b>	Phoenix
<b>Dimensions</b>	318mm x 220mm
<b>I/O Options</b>	Auxiliary keyboard connector, external memory card (IT-FEM386)
<b>NPU Options</b>	80387SX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J8	Power LED & keylock	P5
Turbo LED	P2	Auxiliary keyboard connector	P20
Speaker	P3	External battery	P22
Reset switch	P4	External memory card	SL1

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**IT 386**

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Turbo enabled	J8	Open
Turbo disabled	J8	Closed
í AT bus clock speed select 8MHz	J14	pins 2 & 3 closed
AT bus clock speed select 12MHz	J14	pins 1 & 2 closed
í Power good signal detect from power supply	J18	pins 1 & 2 closed
Power good signal detect from board	J18	pins 2 & 3 closed
í External memory card not installed	J25	pins 1 & 2 closed
External memory card installed	J25	pins 2 & 3 closed
í 80387 disabled (Weitek 3167 is auto detected)	SW1/1	Off
80387 enabled (Weitek 3167 is auto detected)	SW1/1	On
í Monitor type select monochrome	SW1/2	Off
Monitor type select color/EGA/VGA	SW1/2	On

DRAM CONFIGURATION	
Size	Bank 0
1MB	(4) 256K x 9
4MB	(4) 1M x 9

DRAM JUMPER CONFIGURATION			
Size	J1	J12	J13
1MB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
4MB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed

**CACHE CONFIGURATION**  
Note: 32KB cache is factory installed and is not configurable. The location is unidentified.

DRAM SPEED CONFIGURATION		
CPU speed	DRAM speed	J7
25MHz	120ns	Closed
25MHz	100ns	Open
33MHz	100ns	Closed
33MHz	80ns	Open

CPU SPEED CONFIGURATION					
CPU speed	NPU speed	J4	J5	J6	J9
25MHz	20MHz	2 & 3	1 & 3, 2 & 4	2 & 3	2 & 3
25MHz	25MHz	2 & 3	2 & 4	1 & 2	2 & 3
33MHz	25MHz	1 & 2	1 & 2	2 & 3	1 & 2
33MHz	33MHz	1 & 2	Open	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

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## IT386

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I/O MODE CONFIGURATION				
I/O bus mode	I/O wait state 8-bit	I/O wait state 16-bit	SW1/3	SW1/4
1	4	1	On	On
2	6	2	Off	On
3	8	3	On	Off
4	10	4	Off	Off

BATTERY TYPE CONFIGURATION	
Type	J19
4.5v	pins 3 & 4 closed
3.6 - 4.5v	pins 1 & 3 closed
6v (without internal current limiting resistor)	pins 1 & 2 closed
6v (with internal current limiting resistor)	pins 2 & 4 closed