BIOSTAR MICROTECH INTERNATIONAL CORPORATION M B - 8 4 2 5 / 3 3 / 4 0 / 5 0 U U C

Processor CXM1/80486SX/UMCU5/CX486M6/AM486DX/80486DX/SL80486DX/CX487M7AM486DX2/

SL AM486DX2/80486DX2/SL80486DX2/AM486DX4/SL AM486DX4

SL80486DX4/P24D/Pentium Overdrive

Processor Speed 20/25/33/40/50(internal)/50/66(internal)/75(internal)/80(internal)/ 100(internal)MHz

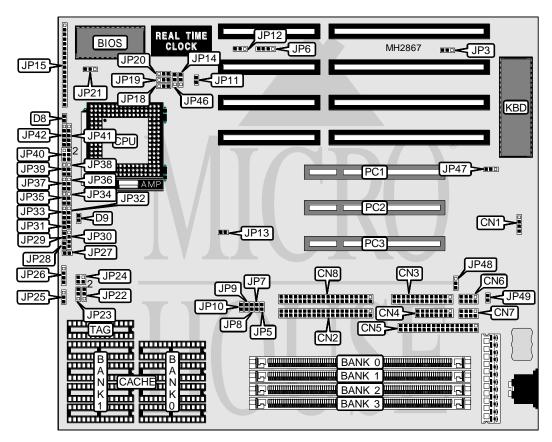
Chip SetUnidentifiedMax. Onboard DRAM128MBCache128/256KBBIOSAward

Dimensions 260mm x 220mm

I/O Options Parallel port, serial ports (2), game port, 32-bit PCI slots (3), green PC connector, floppy

drive interface, IDE interfaces (2)

NPU Options None



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CONNECTIONS					
Purpose	Location	Purpose	Location		
External battery	CN1	IDE interface LED	D9		
IDE interface 1	CN2	Green PC connector	JP13		
Parallel port	CN3	Speaker	JP15 pins 1 - 4		
Game port	CN4	Power LED & keylock	JP15 pins 5 - 9		
Floppy drive interface	CN5	Turbo LED	JP15 pins 10 - 11		
Serial port 1	CN6	Reset switch	JP15 pins 12 - 13		
Serial port 2	CN7	Turbo switch	JP15 pins 14 - 16		
IDE interface 2	CN8	+5v ground	JP15 pins 17 - 18		
+5v ground	D8	32-bit PCI slots	PC1 - PC3		

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í BIOS type select 12v flash	JP3	pins 1 & 2 closed		
BIOS type select 5v flash	JP3	pins 2 & 3 closed		
BIOS type select EPROM	JP3	Open		
í CMOS memory normal operation	JP6	pins 1 & 2 closed		
CMOS memory clear	JP6	pins 1 & 2, 3 & 4 closed		
í Factory configured - do not alter	JP9	N/A		
í Factory configured - do not alter	JP10	N/A		
í Factory configured - do not alter	JP11	N/A		
í Factory configured - do not alter	JP12	N/A		
í Factory configured - do not alter	JP20	N/A		
í Factory configured - do not alter	JP21	N/A		
í Factory configured - do not alter	JP46	N/A		
í Factory configured - do not alter	JP49	N/A		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE	
2MB	(1) 512K x 36	NONE	NONE	NONE	
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	NONE	
4MB	(1) 256K x 36				
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE	
4MB	(1) 1M x 36	NONE	NONE	NONE	
6MB	(1) 512K x 36	(1) 512K x 36	(1) 512K x 36	NONE	
8MB	(1) 512K x 36				
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE	
8MB	(1) 2M x 36	NONE	NONE	NONE	
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE	
16MB	(1) 1M x 36				
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE	
16MB	(1) 4M x 36	NONE	NONE	NONE	
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	NONE	

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	DRAM CONFIGURATION (CON'T)						
Size	Bank 0	Bank 1	Bank 2	Bank 3			
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36			
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE			
32MB	(1) 8M x 36	NONE	NONE	NONE			
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE			
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36			
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE			
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	NONE			
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36			

CACHE CONFIGURATION						
Size Bank 0 Bank 1 TAG						
128KB	(4) 32K x 8	NONE	(1) 8K x 8			
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8			
256KB (B)	(4) 64K x 8	NONE	(1) 32K x 8			

CACHE JUMPER CONFIGURATION					
Size JP24 JP25 JP26					
128KB	Open	pins 1 & 2 closed	Open		
256KB (A) pins 1 & 2 closed pins 1 & 2 closed pins 1 & 2 closed					
256KB (B)	pins 1 & 2 closed	pins 2 & 3 closed	Open		

CPU TYPE CONFIGURATION					
Туре	JP27	JP28	JP29	JP30	JP31
CX M1	Closed	Open	Closed	3 & 4	Closed
80486SX	Open	Closed	Closed	Open	Closed
UMC-U5	Open	Open	Open	1 & 2	Open
CX486M6	Open	Open	Open	Open	Open
AM486DX	Open	Closed	Closed	3 & 4	Closed
80486DX	Open	Closed	Closed	3 & 4	Closed
SL80486DX	Open	Closed	Closed	3 & 4	Closed
CX486M7	Open	Open	Open	3 & 4	Open
AM486DX2	Open	Closed	Closed	3 & 4	Closed
SL AM486DX2 WT	Open	Closed	Closed	3 & 4	Closed
SL AM486DX2 WB	Open	Closed	Closed	3 & 4	Closed
80486DX2	Open	Closed	Closed	3 & 4	Closed
SL80486DX2	Open	Closed	Closed	3 & 4	Closed
AM486DX4	Open	Closed	Closed	3 & 4	Closed
SL AM486DX4 WT	Open	Closed	Closed	3 & 4	Closed
SL AM486DX4 WB	Open	Closed	Closed	3 & 4	Closed
SL80486DX4	Open	Closed	Closed	3 & 4	Closed
P24D	Open	Closed	Closed	3 & 4	Closed
Pentium Overdrive	Open	Open	Open	2 & 3	Open
Note: Pins designate	ed should be in the	closed position.			

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CPU TYPE CONFIGURATION (CON'T)					
Туре	JP32	JP33	JP34	JP35	JP36
CX M1	2 & 3, 4 & 5	Open	Open	Open	1 & 2
80486SX	2 & 3, 4 & 5	Open	Open	Open	1 & 2
UMC-U5	Open	Open	Open	Open	Open
CX486M6	1 & 2, 3 & 4	Open	1 & 2	2 & 3	2 & 3
AM486DX	2 & 3, 4 & 5	Open	Open	Open	1 & 2
80486DX	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL80486DX	2 & 3, 4 & 5	Open	Open	Open	1 & 2
CX486M7	1 & 2, 3 & 4	Open	1 & 2	2 & 3	2 & 3
AM486DX2	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL AM486DX2 WT	2 & 3, 4 & 5	2 & 3	Open	Open	1 & 2
SL AM486DX2 WB	2 & 3, 4 & 5	2 & 3	Open	Open	1 & 2
80486DX2	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL80486DX2	2 & 3, 4 & 5	Open	Open	Open	1 & 2
AM486DX4	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL AM486DX4 WT	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL AM486DX4 WB	2 & 3, 4 & 5	Open	Open	Open	1 & 2
SL80486DX4	2 & 3, 4 & 5	Open	Open	Open	1 & 2
P24D	2 & 3, 4 & 5	Open	Open	Open	1 & 2
Pentium Overdrive	2 & 3, 4 & 5	Open	2 & 3	1 & 2	1 & 2
Note: Pins designated	should be in the c	losed position.			

	CPU TYPE CONFIGURATION (CON'T)					
Туре	JP37	JP38	JP39	JP40	JP41	JP42
CX M1	Open	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
80486SX	2 & 3	2 & 3	1 & 2	1 & 2	Open	2 & 3
UMC-U5	2 & 3	2 & 3	2 & 3	1 & 2	3 & 4	2 & 3
CX486M6	2 & 3	1 & 2	1 & 2	3 & 4	1 & 2	2 & 3
AM486DX	2 & 3	2 & 3	1 & 2	1 & 2	Open	1 & 2, 3 & 4
80486DX	2 & 3	2 & 3	1 & 2	1 & 2	Open	1 & 2, 3 & 4
SL80486DX	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
CX486M7	2 & 3	2 & 3	1 & 2	3 & 4	2 & 3	1 & 2, 3 & 4
AM486DX2	2 & 3	2 & 3	1 & 2	1 & 2	Open	1 & 2, 3 & 4
SL AM486DX2 WT	2 & 3	1 & 2	1 & 2	3 & 4	Open	1 & 2, 3 & 4
SL AM486DX2 WB	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
80486DX2	2 & 3	2 & 3	1 & 2	1 & 2	Open	1 & 2, 3 & 4
SL80486DX2	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
AM486DX4	2 & 3	2 & 3	1 & 2	1 & 2	Open	1 & 2, 3 & 4
SL AM486DX4 WT	2 & 3	1 & 2	1 & 2	3 & 4	Open	1 & 2, 3 & 4
SL AM486DX4 WB	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
SL80486DX4	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
P24D	2 & 3	2 & 3	1 & 2	3 & 4	Open	1 & 2, 3 & 4
Pentium Overdrive	2 & 3	2 & 3	1 & 2	3 & 4, 5 & 6	1 & 2	1 & 2, 3 & 4
Note: Pins designated	d should be in th	ne closed positio	n.			

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	CPU TYPE CONFIGURATION	
Туре	JP22	JP23
Cyrix	pins 2 & 3 closed	pins 1 & 2 closed
AMD	pins 1 & 2 closed	pins 1 & 2 closed
UMC	pins 1 & 2 closed	pins 2 & 3 closed
Intel	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION					
Speed	JP5	JP7	JP8		
20MHz	Open	Open	Open		
25MHz	Open	Open	Closed		
33MHz	Closed	Closed	Closed		
40MHz	Open	Closed	Closed		
50iMHz	Open	Open	Closed		
50MHz	Closed	Open	Open		
66iMHz	Closed	Closed	Closed		
75iMHz	Open	Open	Closed		
80iMHz	Open	Closed	Closed		
100iMHz	Closed	Closed	Closed		

CPU VOLTAGE CONFIGURATION					
Voltage JP14 JP18 JP19					
3.3v	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed		
4v	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed		
5v	Open	pins 2 & 3 closed	pins 2 & 3 closed		

	DMA CONFIGURATION	
DMA	JP47	JP48
DMA 1	pins 1 & 2 closed	pins 1 & 2 closed
DMA 3	pins 2 & 3 closed	pins 2 & 3 closed