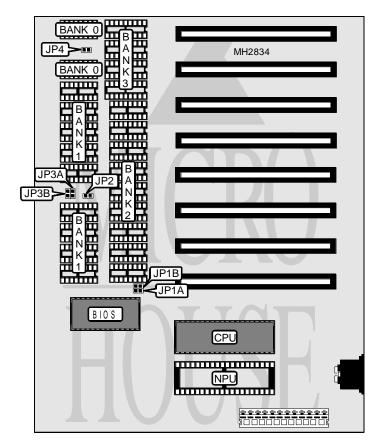
XENER SUPER XT

8088 **Processor Processor Speed** 10MHz **Chip Set** Unidentified Max. Onboard DRAM 640KB Cache None **BIOS** Unidentified **Dimensions** 220mm x 160mm

I/O Options None **NPU Options** 8087



CONNECTIONS				
Purpose	Location	Purpose	Location	
Reset switch	JP2	Speaker	JP4	

Continued on next page. . .

XENER SUPER XT

. . . continued from previous page

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
640KB	(2) 4164	(9) 41256	(9) 41256	(4) 4464

BIOS CONFIGURATION					
Size	JP1A	JP1B			
2764/27128	Open	Open			
27256	Closed	Open			
27512	Closed	Closed			

VIDEO CONFIGURATION					
Туре	JP3A	JP3B			
None	Closed	Closed			
80 x 25 color	Open	Closed			
40 x 25 color	Closed	Open			
Monochrome/color	Open	Open			