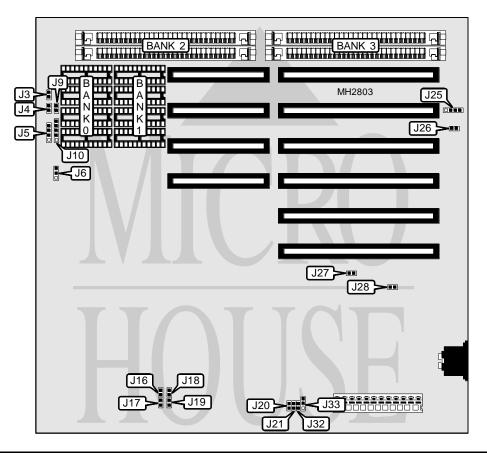
UNIDENTIFIED ACE 286V-16/20

Processor	80286 (exact location unknown)
Processor Speed	12/16MHz
Chip Set	VLSI
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI/Award/Phoenix
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80287 (exact location unknown)



CONNECTIONS				
Purpose	Location	Purpose	Location	
Turbo LED	J3	Turbo switch	19	
Reset switch	J4	Power LED & keylock	J10	
Speaker	J5	External battery	J25	

Continued on next page. . .

UNIDENTIFIED ACE 286V-16/20

... continued from previous page

USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Parity enabled	J6	pins 1 & 2 closed	
Parity disabled	J6	pins 2 & 3 closed	
í REMAP enabled	J16	Closed	
REMAP disabled	J16	Open	
í BIOS wait state select 2 wait states	J20	Closed	
BIOS wait state select 3 wait states	J20	Open	
í RAM read wait state select 0 wait states	J21	Closed	
RAM read wait state select 1 wait states	J21	Open	
í Internal battery enabled	J26	Closed	
Internal battery disabled	J26	Open	
í BIOS type select 27256	J27	Closed	
BIOS type select 27128	J27	Open	
í Monitor type select color	J28	Closed	
Monitor type select monochrome	J28	Open	
í RAM write wait state select 0 wait states	J32	Closed	
RAM write wait state select 1 wait states	J32	Open	
í IOCHRRDY signal select normal operation	J33	pins 1 & 2 closed	
IOCHRRDY signal select if I/O card does not work with default setting	J33	pins 2 & 3 closed	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(4) 44256	NONE	NONE	NONE
1.5MB	(4) 44256	(4) 44256	(2) 256K x 9	NONE
2MB	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION				
Size	J17	J18	J19	
512KB	Closed	Open	Closed	
1.5MB	Closed	Closed	Open	
2MB	Open	Open	Closed	
4MB	Open	Closed	Open	