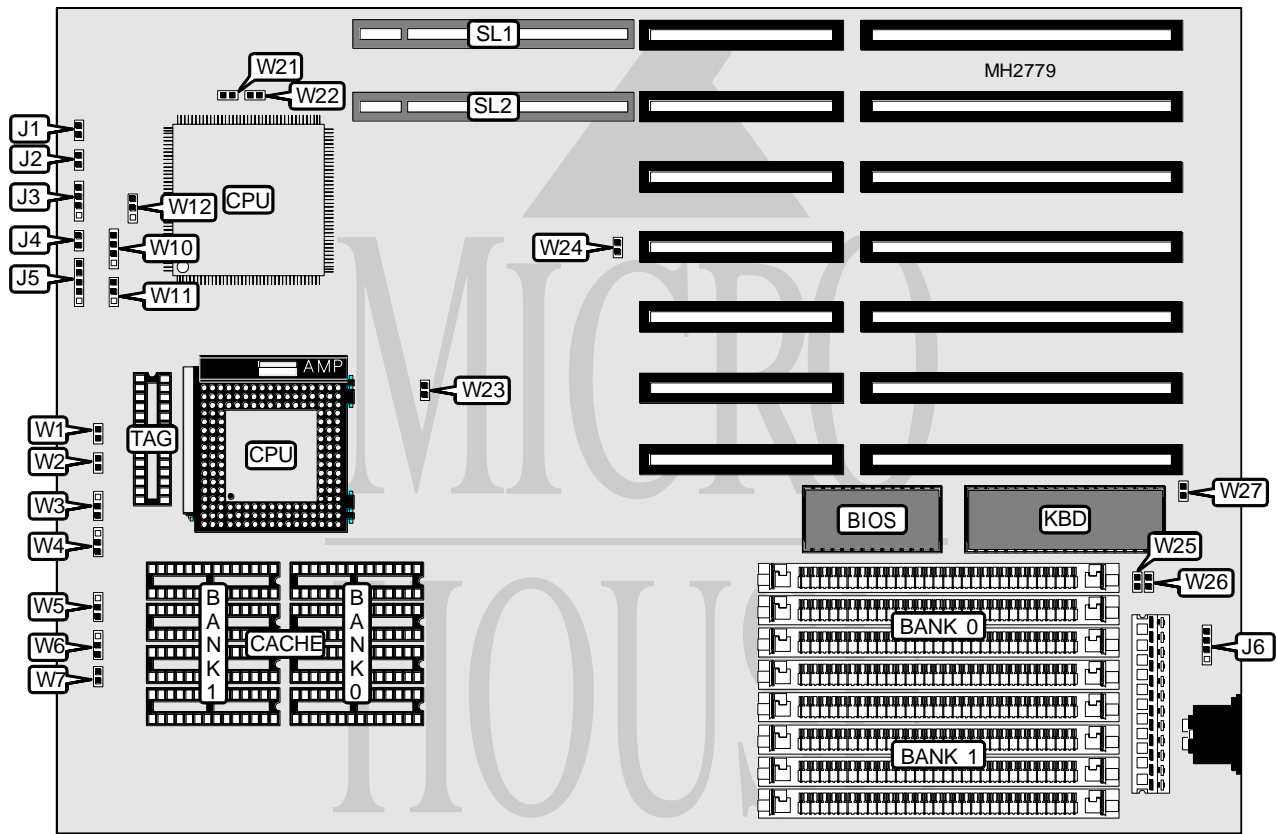


IPC CORPORATION, LTD.

IPC OFFICEMAGIC/ IPC GREEN PC (MB486GHE#10)

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2/Pentium Overdrive
Processor Speed	25/33/50(internal)/66(internal)MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J2	Green PC connector	JP4
Speaker	J3	Green PC connector	JP5
Reset switch	J4	Green PC connector (monitor)	JP6
Power LED & keylock	J5	32-bit VESA local bus slots	SL1 & SL2
External battery	J6	Turbo switch	W23

Note: The locations of JP4, JP5 & JP6 are unidentified.

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Turbo enabled	J1	Open
Turbo disabled	J1	Closed
í CPU clock select 1x	W24	Open
CPU clock select 2x	W24	Closed
í Factory configured - do not alter	W25	Open
í Monitor type select color	W26	Closed
Monitor type select monochrome	W26	Open
í Battery type select internal	W27	Closed
Battery type select external	W27	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	W1	W2	W3	W4	W5	W6	W7
64KB	Closed	Closed	1 & 2	1 & 2	1 & 2	Open	Open
128KB	Closed	Closed	2 & 3	1 & 2	2 & 3	1 & 2	Open
256KB	Closed	Closed	2 & 3	2 & 3	1 & 2	2 & 3	Open

Note: Pins designated should be in the closed position..

CACHE JUMPER CONFIGURATION (CON'T)								
Size	W13	W14	W15	W16	W17	W18	W19	W20
64KB	Open	Open	Open	Open	Open	Open	Open	Open
128KB	Closed	Open	Open	Open	Open	Open	Open	Closed
256KB	Closed	Closed	Open	Open	Open	Open	Closed	Closed

Note: The location of the above jumpers are unidentified.

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M B 4 8 6 G H E # 1 0

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CPU TYPE CONFIGURATION					
Type	W8	W9	W10	W11	W12
80486SX (PQFP)	1 & 2	Open	Open	1 & 2	Open
80486SX (PGA)	Open	Open	2 & 3	Open	2 & 3
ODP486SX	1 & 2	Open	1 & 2, 3 & 4	Open	1 & 2
80487SX	1 & 2	Open	1 & 2, 3 & 4	Open	1 & 2
80486DX (PQFP)	2 & 3	Closed	Closed	1 & 2	Open
80486DX (PGA)	2 & 3	Open	1 & 2, 3 & 4	Open	2 & 3
80486DX2	2 & 3	Open	1 & 2, 3 & 4	Open	2 & 3
Pentium Overdrive	1 & 2	Open	1 & 2, 3 & 4	Open	1 & 2

Note: Pins designated should be in the closed position. The locations of W8 & W9 are unidentified.

CPU SPEED CONFIGURATION	
Speed	JP3
25MHz	pins 2 & 3 closed
33MHz	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed
66iMHz	pins 1 & 2 closed

Note: The location of JP3 is unidentified.

VESA WAIT STATE CONFIGURATION	
Wait states	W22
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	W21
<= 33MHz	Open
> 33MHz	Closed