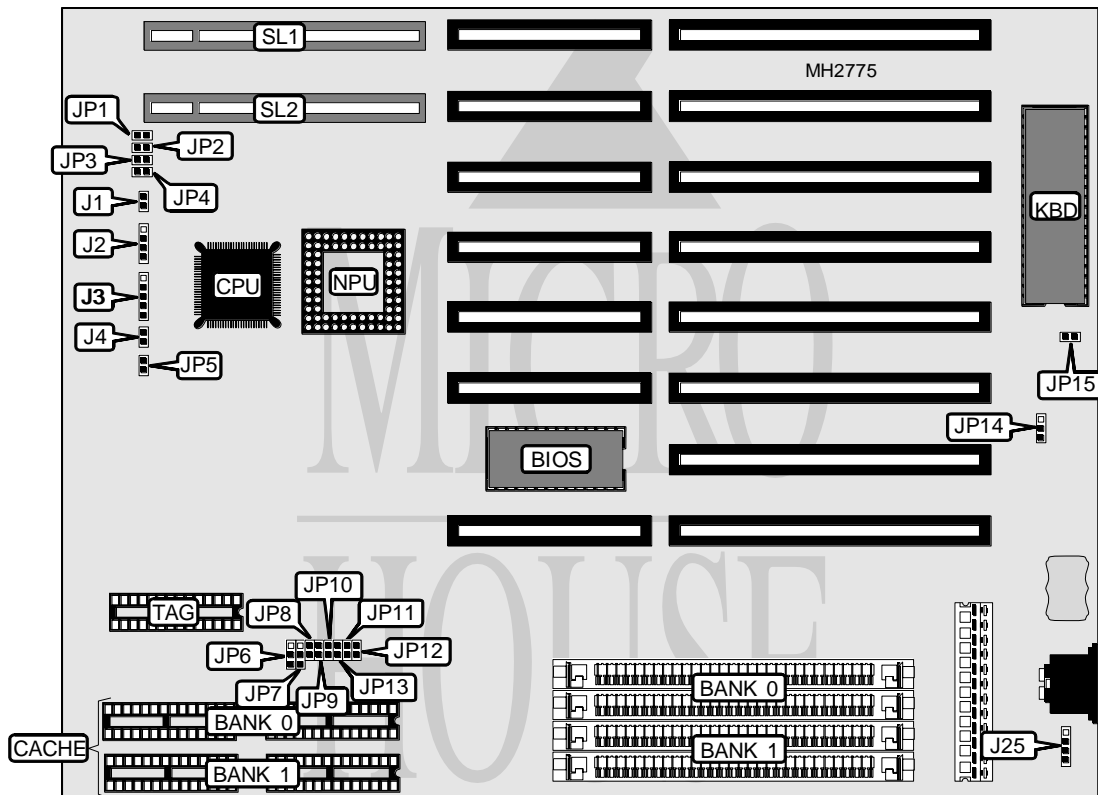# ALARIS
LEOPARD 486SLC2 REV. B

Processor   80486SLC2
Processor Speed   50(internal)/66(internal)MHz
Chip Set   OPTI
Max. Onboard DRAM   16MB
Cache   64/128KB
BIOS   AMI/MR
Dimensions   235mm x 207mm
I/O Options   32-bit VESA local bus slots (2)
NPU Options   80387SX

CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Turbo LED	JP5
Speaker	J2	External battery connector	J25
Power LED & keylock	J3	32-bit VESA local bus slots	SL1 & SL2
Turbo switch	J4		

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ALARIS
L E O P A R D 4 8 6 S L C 2 R E V . B

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	Open
í Factory configured - do not alter	JP2	Open
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP4	Closed
í Factory configured - do not alter	JP10	N/A
í Factory configured - do not alter	JP11	N/A
í Factory configured - do not alter	JP12	N/A
í Factory configured - do not alter	JP13	N/A
í CMOS normal operation	JP14	pins 2 & 3 closed
CMOS memory clear	JP14	pins 1 & 2 closed
í Factory configured - do not alter	JP15	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9
16MB	(2) 4M x 9	(2) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(2) 32K x 8	NONE	(1) 8K x 8
128KB	(2) 32K x 8	(2) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP6	JP7	JP8	JP9
64KB	pins 2 & 3 closed	pins 2 & 3 closed	Closed	Closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	Closed	Closed