MICRONICS COMPUTERS, INC.
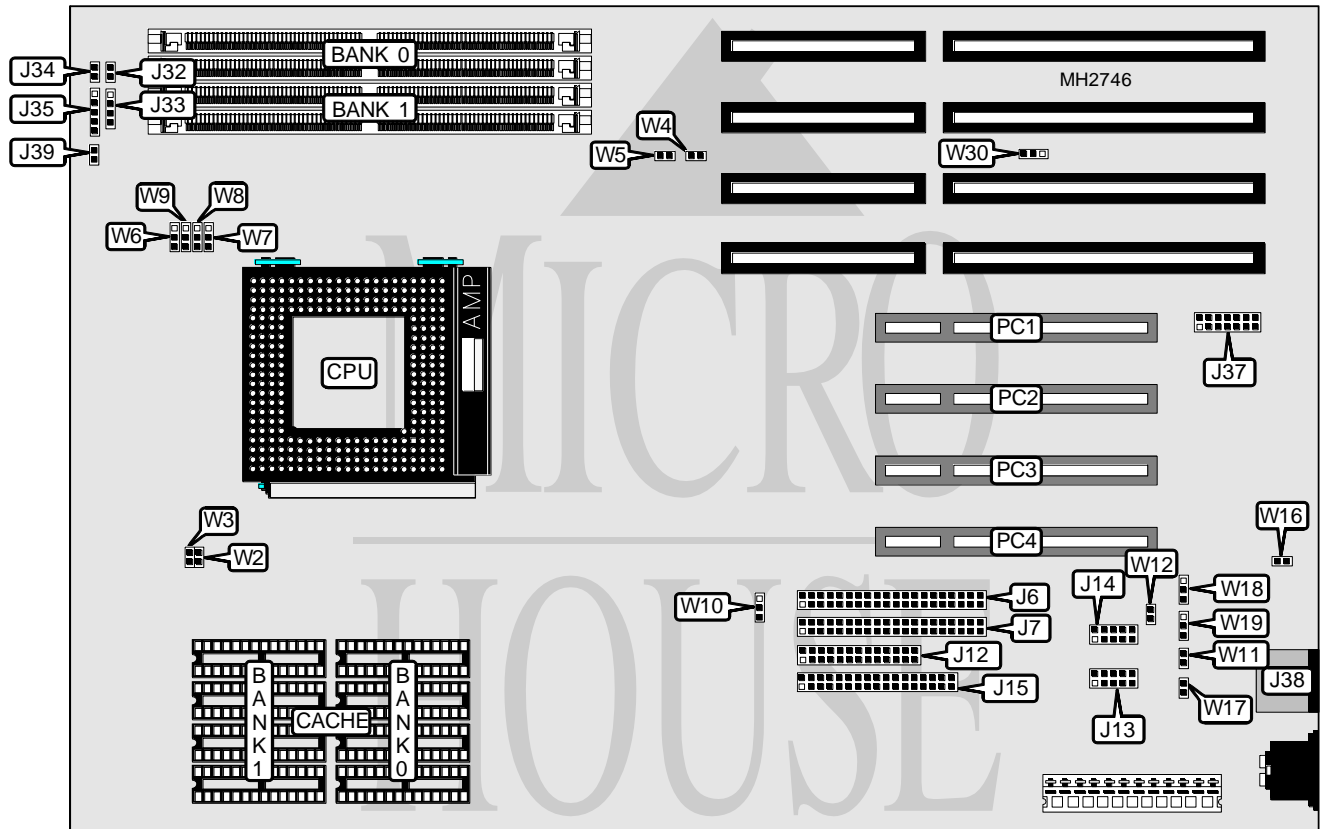# M54HI, M54HI (REV. 2A)

Processor	Pentium
Processor Speed	75/90/100/120MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2)
NPU Options	None

CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface (secondary)	J6	Speaker	J33
IDE interface (primary)	J7	IDE interface LED	J34
Parallel port	J12	Power LED & keylock	J35
Serial port 1	J13	PS/2 mouse port	J38
Serial port 2	J14	Turbo LED	J39
Floppy drive interface	J15	32-bit PCI slots	PC1 - PC4
Reset switch	J32		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J1	N/A
í Factory configured - do not alter	J37	N/A
í BIOS boot block select normal	W10	pins 2 & 3 closed
BIOS boot block select reversed	W10	pins 1 & 2 closed
í Flash BIOS write protect enabled	W11	Open
Flash BIOS write protect disabled	W11	Closed
í CMOS memory normal operation	W12	Open
CMOS memory clear	W12	Closed
í Monitor type select color	W16	Closed
Monitor type select monochrome	W16	Open
í PS/2 mouse not installed	W17	Open
PS/2 mouse installed	W17	Closed
Note: The location of J1 is unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	NONE
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(4) 32K x 8	(4) 32K x 8
512KB	(4) 64K x 8	(4) 64K x 8

CACHE JUMPER CONFIGURATION			
Size	W8	W9	W24
None	pins 1 & 2 closed	pins 1 & 2 closed	Open
256KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
Note: The location of W24 is unidentified.			

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CACHE TYPE CONFIGURATION		
Setting	W6	W7
Pipeline burst	pins 1 & 2 closed	pins 1 & 2 closed
Asynchronous	pins 1 & 2 closed	pins 2 & 3 closed
Burst	pins 2 & 3 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION			
Speed	W4	W5	W30
75MHz	Closed	Closed	pins 1 & 2 closed
90MHz	Open	Closed	pins 2 & 3 closed
100MHz	Closed	Open	pins 2 & 3 closed
120MHz	Open	Closed	pins 2 & 3 closed

CPU SPEED RATIO CONFIGURATION	
Speed	W2
1.5x	Open
2x	Closed

CPU VRE CONFIGURATION	
Setting	W3
VR	Open
VRE	Closed

DMA CONFIGURATION		
DMA	W18	W19
None	Open	Open
DMA1	pins 1 & 2 closed	pins 1 & 2 closed
DMA3	pins 2 & 3 closed	pins 2 & 3 closed