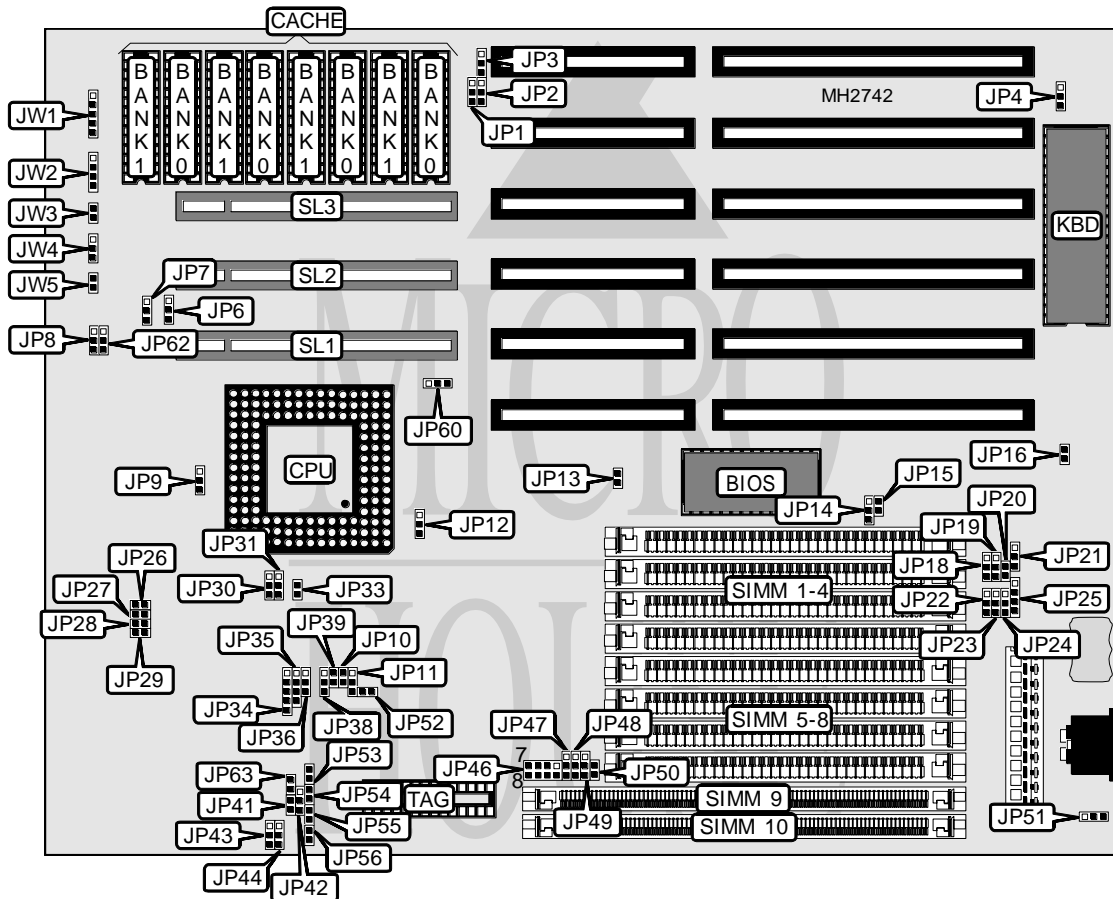


SILICON STAR INTERNATIONAL, INC.

AN4R2/AN4TR2 REV. 1.1 & REV. 1.2

Processor	SL80486SX/80486SX/CX486DX/SL80486DX/80486DX/CX486DX2/AM486DX2/SL80486DX2/80486DX2/80486DX4
Processor Speed	20/25/33/40/50(internal)/66(internal)/75(internal)/83(internal)/100(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	128/256/512KB
BIOS	AMI/Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP25	Reset switch	JW3
Green PC connector (monitor)	JP46 pins 1 - 3	Turbo switch	JW4
Green PC connector	JP46 pins 7 & 8	Turbo LED	JW5
Power LED & keylock	JW1	32-bit VESA local bus slots	SL1 - SL3
Speaker	JW2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP3	pins 2 & 3 closed
í Factory configured - do not alter	JP4	pins 1 & 2 closed
í Factory configured - do not alter	JP6	N/A
í Factory configured - do not alter	JP13	Closed
í Factory configured - do not alter	JP14	pins 1 & 2 closed
í Factory configured - do not alter	JP15	Open
í Factory configured - do not alter	JP16	Closed
í CMOS memory normal operation	JP21	pins 1 & 2 closed
CMOS memory clear	JP21	pins 2 & 3 closed
í Factory configured - do not alter	JP41	Open
í Factory configured - do not alter	JP49	pins 1 & 2 closed
í Factory configured - do not alter	JP52	N/A
í Factory configured - do not alter	JP60	N/A
í Factory configured - do not alter	JP62	N/A

DRAM CONFIGURATION				
Size	SIMM 1 - 4	SIMM 5 - 8	SIMM 9	SIMM 10
1MB	NONE	NONE	(1) 256K x 36	NONE
1MB	(4) 256K x 9	NONE	NONE	NONE
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	NONE	NONE	(1) 256K x 36	(1) 256K x 36
2MB	(4) 256K x 9	NONE	NONE	(1) 256K x 36
2MB	NONE	NONE	(1) 512K x 36	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	NONE	NONE	(1) 1M x 36	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
4MB	NONE	NONE	(1) 512K x 36	(1) 512K x 36
4MB	(4) 256K x 9	NONE	(1) 512K x 36	(1) 256K x 36
4MB	(4) 256K x 9	(4) 256K x 9	(1) 512K x 36	NONE
5MB	NONE	NONE	(1) 256K x 36	(1) 1M x 36
5MB	(4) 256K x 9	NONE	NONE	(1) 1M x 36
6MB	(4) 1M x 9	NONE	(1) 256K x 36	(1) 256K x 36
6MB	(4) 256K x 9	NONE	(1) 1M x 36	(1) 256K x 36
6MB	NONE	NONE	(1) 512K x 36	(1) 1M x 36
6MB	(4) 256K x 9	(4) 256K x 9	(1) 1M x 36	NONE
8MB	NONE	NONE	(1) 1M x 36	(1) 1M x 36
8MB	(4) 1M x 9	NONE	NONE	(1) 1M x 36
8MB	NONE	NONE	(1) 2M x 36	NONE
8MB	(4) 1M x 9	NONE	(1) 512K x 36	(1) 512K x 36
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 1M x 9	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
10MB	(4) 256K x 9	(4) 256K x 9	(1) 1M x 36	(1) 1M x 36
12MB	(4) 1M x 9	NONE	(1) 1M x 36	(1) 1M x 36

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DRAM CONFIGURATION (CON'T)				
Size	SIMM 1 - 4	SIMM 5 - 8	SIMM 9	SIMM 10
12MB	(4) 1M x 9	NONE	(1) 1M x 36	(1) 1M x 36
12MB	(4) 1M x 9	(4) 1M x 9	(1) 512K x 36	(1) 512K x 36
12MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	NONE	NONE	(1) 4M x 36	NONE
16MB	(4) 4M x 9	NONE	NONE	NONE
16MB	NONE	NONE	(1) 2M x 36	(1) 2M x 36
16MB	(4) 1M x 9	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36
16MB	(4) 4M x 9	NONE	NONE	NONE
17MB	NONE	NONE	(1) 256K x 36	(1) 4M x 36
17MB	(4) 256K x 9	NONE	NONE	(1) 4M x 36
18MB	(4) 4M x 9	NONE	(1) 256K x 36	(1) 256K x 36
18MB	(4) 256K x 9	NONE	(1) 4M x 36	(1) 256K x 36
18MB	NONE	NONE	(1) 512K x 36	(1) 4M x 36
18MB	(4) 256K x 9	(4) 256K x 9	(1) 4M x 36	NONE
20MB	NONE	NONE	(1) 1M x 36	(1) 4M x 36
20MB	(4) 1M x 9	NONE	NONE	(1) 4M x 36
20MB	(4) 4M x 9	NONE	(1) 512K x 36	(1) 512K x 36
24MB	(4) 4M x 9	NONE	(1) 1M x 36	(1) 1M x 36
24MB	(4) 1M x 9	NONE	(1) 4M x 36	(1) 1M x 36
24MB	(4) 1M x 9	(4) 4M x 9	(1) 512K x 36	(1) 512K x 36
24MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	NONE
32MB	NONE	NONE	(1) 4M x 36	(1) 4M x 36
32MB	(4) 4M x 9	NONE	NONE	(1) 4M x 36
32MB	NONE	NONE	(1) 8M x 36	NONE
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	NONE	(1) 1M x 36	(1) 4M x 36
36MB	(4) 1M x 9	NONE	(1) 4M x 36	(1) 4M x 36
36MB	(4) 4M x 9	(4) 4M x 9	(1) 512K x 36	(1) 512K x 36
36MB	(4) 1M x 9	NONE	(1) 8M x 36	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(1) 4M x 36	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(1) 1M x 36	(1) 1M x 36
40MB	(4) 1M x 9	NONE	(1) 8M x 36	(1) 1M x 36
40MB	(4) 1M x 9	(4) 1M x 9	(1) 8M x 36	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	NONE	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	NONE	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	NONE	(1) 8M x 36	NONE
48MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
64MB	NONE	NONE	(1) 16M x 36	NONE
64MB	(4) 16M x 9	NONE	NONE	NONE
64MB	NONE	NONE	(1) 8M x 36	(1) 8M x 36
64MB	(4) 4M x 9	NONE	(1) 8M x 36	(1) 4M x 36

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DRAM CONFIGURATION (CON'T)				
Size	SIMM 1 - 4	SIMM 5 - 8	SIMM 9	SIMM 10
64MB	(4) 4M x 9	(4) 4M x 9	(1) 8M x 36	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36
64MB	(4) 16M x 9	NONE	NONE	NONE
65MB	NONE	NONE	(1) 256K x 36	(1) 16M x 36
65MB	(4) 256K x 9	NONE	NONE	(1) 16M x 36
68MB	NONE	NONE	(1) 1M x 36	(1) 16M x 36
68MB	(4) 1M x 9	NONE	NONE	(1) 16M x 36
68MB	(4) 1M x 9	(4) 16M x 9	NONE	NONE
72MB	(4) 16M x 9	NONE	(1) 1M x 36	(1) 1M x 36
72MB	(4) 1M x 9	NONE	(1) 16M x 36	(1) 1M x 36
72MB	(4) 1M x 9	(4) 1M x 9	(1) 16M x 36	NONE
80MB	NONE	NONE	(1) 4M x 36	(1) 16M x 36
80MB	(4) 4M x 9	NONE	NONE	(1) 16M x 36
80MB	(4) 4M x 9	(4) 16M x 9	NONE	NONE
96MB	(4) 16M x 9	NONE	(1) 4M x 36	(1) 4M x 36
96MB	(4) 4M x 9	NONE	(1) 16M x 36	(1) 4M x 36
96MB	(4) 4M x 9	(4) 4M x 9	(1) 16M x 36	NONE
128MB	NONE	NONE	(1) 16M x 36	(1) 16M x 36
128MB	(4) 16M x 9	NONE	NONE	(1) 16M x 36
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE

DRAM JUMPER CONFIGURATION							
Bank 0	Bank 1	Bank 2	Bank 3	JP18	JP19	JP20	JP22
SIMM 1 - 4	SIMM 5 - 8	SIMM 9	SIMM 10	1 & 2	1 & 2	Open	1 & 2
SIMM 1 - 4	SIMM 5 - 8	SIMM 9	NONE	1 & 2	1 & 2	Open	1 & 2
SIMM 1 - 4	SIMM 9	NONE	NONE	1 & 2	Open	Open	1 & 2
SIMM 1 - 4	SIMM 10	SIMM 9	NONE	1 & 2	Open	Open	1 & 2
SIMM 9	SIMM 10	SIMM 1 - 4	SIMM 5 - 8	2 & 3	2 & 3	Open	2 & 3
SIMM 9	SIMM 10	NONE	NONE	Open	Open	Closed	2 & 3
SIMM 1 - 4	SIMM 10	SIMM 9	NONE	1 & 2	Open	Open	1 & 2
SIMM 9	SIMM 10	SIMM 1 - 4	SIMM 5 - 8	2 & 3	2 & 3	Open	2 & 3

Note: Pins designated should be in the closed position.

DRAM JUMPER CONFIGURATION (CON'T)							
Bank 0	Bank 1	Bank 2	Bank 3	JP23	JP24	JP50	JP51
SIMM 1 - 4	SIMM 5 - 8	SIMM 9	SIMM 10	Open	1 & 2	Open	Open
SIMM 1 - 4	SIMM 5 - 8	SIMM 9	NONE	1 & 2	Open	Open	Open
SIMM 1 - 4	SIMM 9	NONE	NONE	2 & 3	Open	Open	Open
SIMM 1 - 4	SIMM 10	SIMM 9	NONE	1 & 2	2 & 3	Open	Open
SIMM 9	SIMM 10	SIMM 1 - 4	SIMM 5 - 8	Open	2 & 3	Closed	2 & 3
SIMM 9	SIMM 10	NONE	NONE	2 & 3	Open	Open	1 & 2
SIMM 1 - 4	SIMM 10	SIMM 9	NONE	1 & 2	2 & 3	Open	Open
SIMM 9	SIMM 10	SIMM 1 - 4	SIMM 5 - 8	Open	2 & 3	Open	Open

Note: Pins designated should be in the closed position.

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB	(4) 64K x 8	NONE	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP1	JP2	JP42	JP44
128KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
512KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION					
Type	JP9	JP12	JP30	JP31	JP33
SL80486SX	2 & 3	2 & 3	2 & 3	Open	Open
80486SX	2 & 3	2 & 3	2 & 3	Open	Open
CX486DX	2 & 3	2 & 3	1 & 2	3 & 4	Closed
SL80486DX	2 & 3	2 & 3	1 & 2	3 & 4	Closed
80486DX	2 & 3	2 & 3	1 & 2	3 & 4	Closed
CX486DX2	1 & 2	1 & 2	1 & 2	3 & 4	Closed
AM486DX2	1 & 2	1 & 2	1 & 2	3 & 4	Closed
SL80486DX2	2 & 3	2 & 3	1 & 2	3 & 4	Closed
80486DX2	2 & 3	2 & 3	1 & 2	3 & 4	Closed
80486DX4	1 & 2	1 & 2	1 & 2	3 & 4	Closed

Note: Pins designated should be in the closed position. The 80486DX4 CPU is only available on the AN4TR2 mainboard.

CPU TYPE CONFIGURATION (CON'T)				
Type	JP34	JP35	JP36	JP38
SL80486SX	4 & 5	3 & 4	2 & 3	Open
80486SX	4 & 5	3 & 4	2 & 3	Open
CX486DX	2 & 3	2 & 3	1 & 2, 3 & 4	2 & 3
SL80486DX	4 & 5	3 & 4	2 & 3	Open
80486DX	4 & 5	3 & 4	2 & 3	Open
CX486DX2	2 & 3	2 & 3	1 & 2	2 & 3
AM486DX2	4 & 5	3 & 4	2 & 3	Open
SL80486DX2	4 & 5	3 & 4	2 & 3	Open
80486DX2	4 & 5	3 & 4	2 & 3	Open
80486DX4	4 & 5	3 & 4	2 & 3	Open

Note: Pins designated should be in the closed position. The 80486DX4 CPU is only available on the AN4TR2 mainboard.

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CPU TYPE CONFIGURATION (CON'T)					
Type	JP39	JP43	JP47	JP48	JP63
SL80486SX	Open	2 & 3	2 & 3	1 & 2	Open
80486SX	Open	2 & 3	2 & 3	1 & 2	Open
CX486DX	2 & 3	1 & 2	1 & 2	2 & 3	Open
SL80486DX	Open	2 & 3	2 & 3	1 & 2	Open
80486DX	Open	2 & 3	2 & 3	1 & 2	Open
CX486DX2	2 & 3	1 & 2	1 & 2	2 & 3	Open
AM486DX2	Open	2 & 3	2 & 3	1 & 2	1 & 2
SL80486DX2	Open	2 & 3	2 & 3	1 & 2	Open
80486DX2	Open	2 & 3	2 & 3	1 & 2	Open
80486DX4	Open	2 & 3	2 & 3	1 & 2	Open

Note: Pins designated should be in the closed position. The 80486DX4 CPU is only available on the AN4TR2 mainboard.

CPU SPEED CONFIGURATION (AVASEM AV9107)							
Speed	JP8	JP10	JP11	JP26	JP27	JP28	JP29
20MHz	2 & 3	Open	Open	Closed	Open	1 & 2	Closed
25MHz	2 & 3	Open	Open	Open	Closed	1 & 2	Closed
33MHz	2 & 3	Open	Open	Closed	Closed	1 & 2	Open
40MHz	2 & 3	Open	Open	Open	Open	1 & 2	Closed
50iMHz	2 & 3	Open	Open	Open	Closed	1 & 2	Closed
66iMHz	2 & 3	Open	Open	Closed	Closed	1 & 2	Open
75iMHz	2 & 3	Open	1 & 2	Open	Closed	Open	Closed
83iMHz	2 & 3	Closed	Open	Closed	Closed	Open	Open
100iMHz	2 & 3	Open	1 & 2	Closed	Closed	Open	Open

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION (MX8315)							
Speed	JP8	JP10	JP11	JP26	JP27	JP28	JP29
20MHz	1 & 2	Open	Open	Open	Open	1 & 2	Open
25MHz	1 & 2	Open	Open	Open	Closed	1 & 2	Open
33MHz	1 & 2	Open	Open	Closed	Closed	1 & 2	Closed
40MHz	1 & 2	Open	Open	Closed	Closed	1 & 2	Open
50iMHz	1 & 2	Open	Open	Open	Closed	1 & 2	Open
66iMHz	1 & 2	Open	Open	Closed	Closed	1 & 2	Closed
75iMHz	1 & 2	Open	1 & 2	Open	Closed	Open	Closed
83iMHz	1 & 2	Closed	Open	Closed	Closed	Open	Closed
100iMHz	1 & 2	Open	1 & 2	Closed	Closed	Open	Closed

Note: Pins designated should be in the closed position.

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CPU VOLTAGE CONFIGURATION				
Voltage	JP53	JP54	JP55	JP56
3.3v	pins 1 & 2 closed	Open	Open	Open
3.45v	Open	pins 1 & 2 closed	Open	Open
3.6v	Open	Open	pins 1 & 2 closed	Open
4v	Open	Open	Open	pins 1 & 2 closed

BUS SPEED CONFIGURATION	
CPU speed	JP7
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed