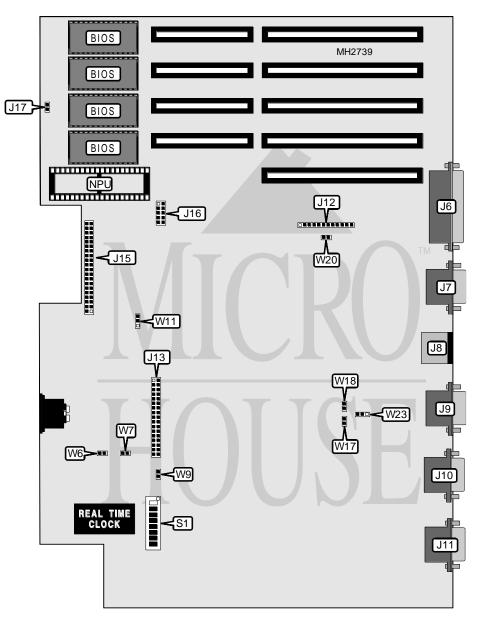
PHILIPS CONSUMER ELECTRONICS, CO. HEADSTART SERIES 300

Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options 80286 (exact location unknown) 8/12MHz Unidentified None None Unidentified 218mm x 330mm bus mouse port, floppy drive interface, game port, IDE interface, SCSI connector, parallel port, PS/2 mouse port, serial port, VGA port 80287

NPU Options



Continued on next page. . .

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CONNECTIONS						
Purpose	Location	Purpose Location				
Parallel port	J6 Power connector		J12			
Serial port 1	J7 Floppy drive interface		J13			
Bus mouse port	8L	Embedded IDE interface	J15			
Game port	J9	Floppy/IDE power connector	J16			
EGA/TTL port	J10	Reset switch	J17			
VGA/Analog port	J11					

USER CONFIGURABLE SETTINGS					
Function	Jumper/Switch	Position			
í Factory configured - do not alter	S1/5	N/A			
í On-board floppy disabled	S1/8	Closed			
On-board floppy enabled	S1/8	Open			
í Video mode normal	W7	Open			
Video mode slow	W7	Closed			
í I/O half speed enabled	W11	Open			
I/O half speed disabled on 12.5 MHz, enabled on 8MHz	W11	pins 1 & 2 closed			
I/O half speed disabled	W11	pins 2 & 3 closed			
í RAM delay line external	W17	Closed			
RAM delay line internal	W17	Open			
í Port IECH-IEFH access enabled	W18	Open			
Port IECH-IEFH access disabled	W18	Closed			
í CPU speed normal	W20	Open			
CPU speed half	W20	Closed			
í Factory configured - do not alter	W23	pins 2 & 3 closed			

MONITOR CONFIGURATION						
Size	S1/1	S1/2	S1/3	S1/4	S1/6	S1/7
MDA	Closed	Closed	Open	Closed	Closed	Open
CGA	Open	Closed	Open	Closed	Closed	Closed
EGA	Closed	Open	Open	Closed	Closed	Open
TTL	Open	Open	Open	Closed	Closed	Open
Analog	Open	Closed	Open	Open	Closed	Open
PS2	Closed	Closed	Open	Open	Closed	Open

MANUFACTURING MODE CONFIGURATION					
Select	W6	W9			
Enabled	Closed	Closed			
Disabled	Open	Open			