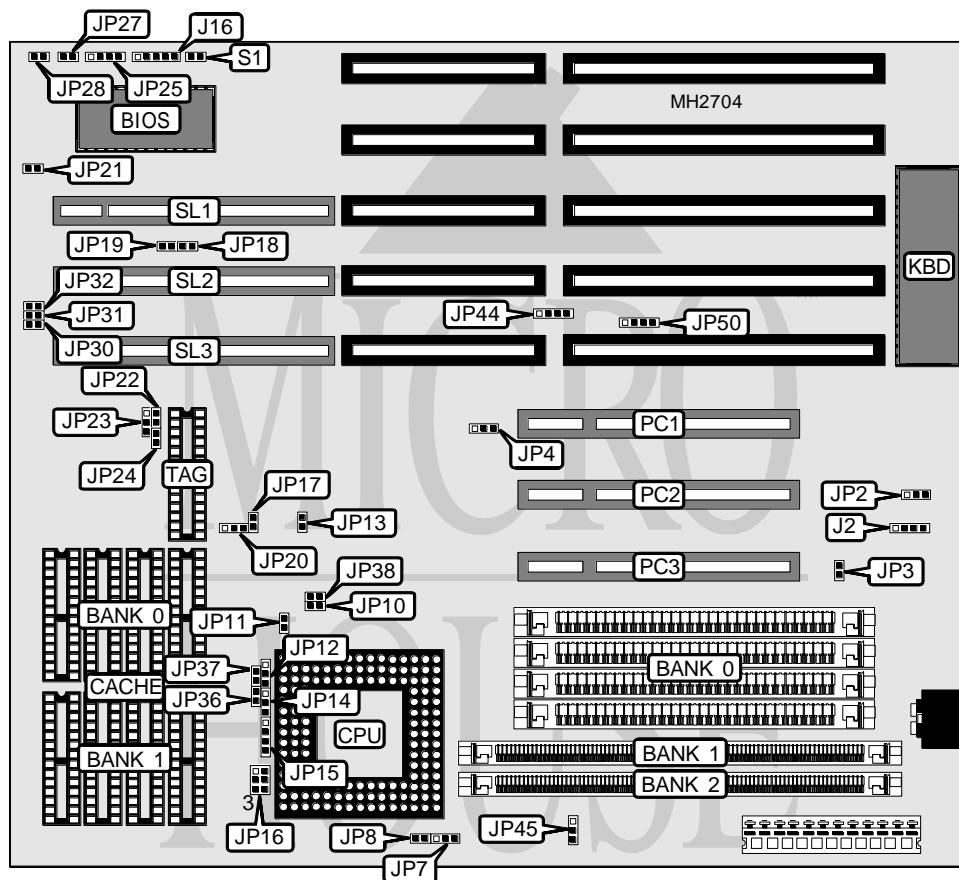


EDOM INTERNATIONAL CORPORATION

486VL3 (MP032F)

Processor	80486SX/80487SX/CX486M7/80486DX/AM486DX2/80486DX2/80486DX4
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256KB
BIOS	Award
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (3), 32-bit PCI slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo switch	JP28
Power LED & keylock	J16	Reset switch	S1
Green PC connector	JP3	32-bit VESA local bus slots	SL1 - SL3
Speaker	JP25	32-bit PCI slots	PC1 - PC3
Turbo LED	JP27		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP2	pins 1 & 2 closed
CMOS memory clear	JP2	pins 2 & 3 closed
í PCI asynchronous with CPU	JP4	pins 1 & 2 closed
PCI synchronous with CPU	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP14	pins 1 & 2 closed

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
2MB	(4) 256K x 9	(1) 256K x 36	NONE
4MB	(4) 1M x 9	NONE	NONE
4MB	NONE	(1) 1M x 36	NONE
4MB	NONE	(1) 512K x 36	(1) 512K x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
8MB	NONE	(1) 1M x 36	(1) 1M x 36
8MB	NONE	(1) 2M x 36	NONE
12MB	NONE	(1) 1M x 36	(1) 2M x 36
16MB	(4) 4M x 9	NONE	NONE
16MB	NONE	(1) 4M x 36	NONE
16MB	NONE	(1) 2M x 36	(1) 2M x 36
20MB	(4) 1M x 9	(1) 4M x 36	NONE
32MB	(4) 4M x 9	(1) 4M x 36	NONE
32MB	NONE	(1) 4M x 36	(1) 4M x 36
32MB	NONE	(1) 8M x 36	NONE
64MB	NONE	(1) 16M x 36	NONE
64MB	NONE	(1) 8M x 36	(1) 8M x 36
128MB	NONE	(1) 16M x 36	(1) 16M x 36

DRAM JUMPER CONFIGURATION	
Type	JP45
Bank 0 = 72 pin SIMM	pins 1 & 2 closed
Bank 1 = 72 pin SIMM	pins 2 & 3 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

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CACHE JUMPER CONFIGURATION				
Size	JP20	JP22	JP23	JP24
64KB	pins 2 & 3 closed	Open	pins 1 & 2 closed	Open
128KB	pins 1 & 2 closed	Closed	pins 1 & 2 closed	Open
256KB	pins 2 & 3 closed	Closed	pins 2 & 3 closed	Closed
256KB	pins 2 & 3 closed	Closed	pins 1 & 2 closed	Closed

CPU TYPE CONFIGURATION				
Type	JP7	JP8	JP10	JP11
80486SX	Open	Open	Open	Open
80487SX	pins 2 & 3 closed	Open	Open	Open
CX486M7	pins 1 & 2 closed	Open	Open	Open
80486DX	pins 1 & 2 closed	Open	Open	Open
80486DX2	pins 1 & 2 closed	Open	Open	Open
80486DX4	pins 1 & 2 closed	Open	Open	Open

CPU TYPE CONFIGURATION (CON'T)				
Type	JP15	JP36	JP37	JP38
80486SX	pins 2 & 3 closed	Closed	Open	Open
80487SX	pins 1 & 2, 3 & 4 closed	Closed	Open	Open
CX486M7	pins 1 & 2, 3 & 4 closed	Open	Closed	Open
80486DX	pins 1 & 2, 3 & 4 closed	Closed	Open	Open
80486DX2	pins 1 & 2, 3 & 4 closed	Closed	Open	Open
80486DX4	pins 1 & 2, 3 & 4 closed	Closed	Open	Open

CPU TYPE CONFIGURATION			
Type	JP13	JP17	JP21
Non SL-enhanced	Open	Open	Open
SL-enhanced	Closed	Closed	Closed

CPU SPEED CONFIGURATION			
Speed	JP30	JP31	JP32
25MHz	Open	Open	Open
33MHz	Closed	Closed	Open
40MHz	Open	Closed	Open
50iMHz	Open	Open	Open
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Open
75iMHz	Open	Open	Open
100iMHz	Closed	Closed	Open

CPU SPEED CONFIGURATION (80486DX4 ONLY)	
Speed	JP12
2x	pins 1 & 2 closed
3x	Open

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CPU VOLTAGE CONFIGURATION	
Voltage	JP16
3v	pins 2 & 3, 5 & 6 closed
5v	pins 1 & 2, 4 & 5 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP19
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP18
<= 33MHz	Open
> 33MHz	Closed

PCI TRIGGER CONFIGURATION		
Type	JP44	JP50
Level	pins 2 & 3 closed	pins 2 & 3 closed
Edge	pins 1 & 2, 3 & 4 closed	pins 1 & 2, 3 & 4 closed