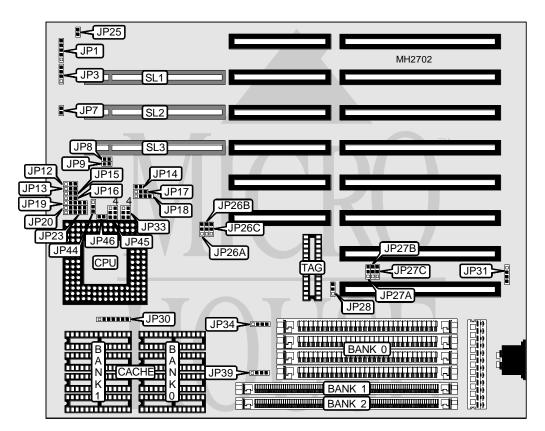
Processor

Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

CX486SX/AM486SX/UMC U5/80486SX/CX486DX/AM486DX/80486DX/ 80486DX2/80486DX4/Pentium Overdrive 25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz Hint 64MB 128/256/512/1024KB Award 254mm x 220mm 32-bit VESA local bus slots (3) None



CONNECTIONS					
Purpose Location Purpose Location					
Power LED & keylock	JP1	Turbo LED	JP25		
Speaker JP3 External battery JP31					
Reset switch	JP7	32-bit VESA local bus slots	SL1 - SL3		

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Factory configured - do not alter	JP17	N/A	
í CMOS memory normal operation	JP31	pins 1 & 2 closed	
CMOS memory clear	JP31	pins 2 & 3 closed	
Battery type select external	JP31	Closed	
í CPU type select Intel/AMD	JP44	pins 1 & 2 closed	
CPU type select Cyrix	JP44	pins 2 & 3 closed	
í CPU type select all	JP46	Open	
CPU type select AMD486DX2 only	JP46	Closed	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	
4MB	(4) 1M x 9	NONE	NONE	
4MB	NONE	(1) 1M x 36	NONE	
8MB	(4) 1M x 9	(1) 1M x 36	NONE	
8MB	NONE	(1) 2M x 36	NONE	
8MB	NONE	(1) 1M x 36	(1) 1M x 36	
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36	
12MB	NONE	(1) 2M x 36	(1) 1M x 36	
12MB	NONE	(1) 1M x 36	(1) 2M x 36	
12MB	(4) 1M x 9	NONE	(1) 2M x 36	
16MB	(4) 4M x 9	NONE	NONE	
16MB	(4) 1M x 9	(1) 1M x 36	(1) 2M x 36	
16MB	NONE	(1) 4M x 36	NONE	
16MB	NONE	(1) 2M x 36	(1) 2M x 36	
20MB	(4) 1M x 9	(1) 4M x 36	NONE	
20MB	NONE	(1) 4M x 36	(1) 1M x 36	
20MB	NONE	(1) 1M x 36	(1) 4M x 36	
24MB	(4) 1M x 9	(1) 1M x 36	(1) 4M x 36	
24MB	(4) 1M x 9	(1) 4M x 36	(1) 1M x 36	
24MB	(4) 4M x 9	NONE	(1) 2M x 36	
32MB	(4) 4M x 9	(1) 4M x 36	NONE	
32MB	NONE	(1) 8M x 36	NONE	
36MB	(4) 1M x 9	NONE	(1) 8M x 36	
36MB	(4) 4M x 9	(1) 4M x 36	(1) 1M x 36	
36MB	(4) 4M x 9	(1) 1M x 36	(1) 4M x 36	
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36	
36MB	NONE	(1) 8M x 36	(1) 1M x 36	
40MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36	
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36	
48MB	NONE	(1) 8M x 36	(1) 4M x 36	
48MB	(4) 4M x 9	NONE	(1) 8M x 36	
64MB	NONE	(1) 16M x 36	NONE	
64MB	(4) 16M x 9	NONE	NONE	

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DRAM JUMPER CONFIGURATION			
Туре ЈРЗ9			
Bank 0 = 30 pin SIMM	pins 2 & 3 closed		
Bank 0 = 72 pin SIMM	pins 1 & 2, 3 & 4 closed		

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	
256KB	(4) 64K x 8	NONE	(1) 8K x 8	
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8	
512KB	(4) 128K x 8	NONE	(1) 32K x 8	
1MB	(4) 128K x 8	(4) 128K x 8	(1) 32K x 8	

CACHE JUMPER CONFIGURATION			
Size	JP28	JP30	JP34
128KB	2&3	1 & 2, 3 & 4, 5 & 6	Open
256KB	1&2	1 & 2, 3 & 4, 5 & 6	Open
256KB	1&2	2 & 3, 4 & 5, 6 & 7	Open
512KB	1 & 2	1 & 2, 3 & 4, 5 & 6	3 & 4
512KB	1 & 2	2 & 3, 4 & 5, 6 & 7	3 & 4
1MB	1 & 2	2 & 3, 4 & 5, 6 & 7	1 & 2, 3 & 4
Note: Pins designated should be in the closed position.			

	CPU TYPE COI	NFIGURATION		
JP12	JP13	JP14	JP15	
1&2	Open	Open	Open	
1&2	Open	Open	Open	
2&3	2 & 3	Open	2 & 3	
	1 & 2 1 & 2	JP12 JP13 1 & 2 Open 1 & 2 Open	1 & 2 Open Open 1 & 2 Open Open 1 & 2 Open Open	JP12 JP13 JP14 JP15 1 & 2 Open Open Open 1 & 2 Open Open Open

1&2

1&2

1&2 Pins designated should be in the closed position. Note:

1&2

Intel SX

Intel DX

	CPU TYPE CONFIGURATION (CON'T)				
Туре	JP18	JP19	JP20	JP23	
AMD	pins1 & 2, 3 & 4 closed	pins 4 & 5 closed	pins 4 & 5 closed	pins 4 & 5 closed	
UMC	pins1 & 2, 3 & 4 closed	pins 4 & 5 closed	pins 4 & 5 closed	pins 4 & 5 closed	
Cyrix	pins1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	
Intel SX	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	
Intel DX	pins1 & 2, 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	

Open

Open

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JP16 Open Open 2&3

1&2

1&2

1&2

1&2

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CPU TYPE CONFIGURATION				
Туре	JP27A	JP27B	JP27C	
Cyrix	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	
AMD	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	
Intel SL	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	
Intel	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	
Pentium Overdrive	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	

	CPU SPEED CONFIGURATION (MX-8513)				
Speed	JP26A	JP26B	JP26C		
25MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed		
40MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
50iMHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed		
66iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed		
75iMHz	Open	pins 2 & 3 closed	Open		
100iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed		

	CPU SPEED CONFIGURATION (AV9107-03)				
Speed	JP26A	JP26B	JP26C		
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed		
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
40MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed		
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed		
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed		
66iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed		
75iMHz	pins 2 & 3 closed	Open	pins 2 & 3 closed		
100iMHz	pins 2 & 3 closed	pins 2 & 3 closed	Open		

CPU VOLTAGE CONFIGURATION		
Voltage	JP33	
3v	pins 1 & 2, 4 & 5 closed	
5v	pins 2 & 3, 5 & 6 closed	

CPU VOLTAGE CONFIGURATION		
Voltage	JP45	
3.3v	pins 3 & 6 closed	
3.45v	pins 2 & 5 closed	
4v	pins 1 & 4 closed	

VESA WAIT STATE CONFIGURATION		
Wait states	JP9	
0 wait states	Open	
1 wait state	Closed	

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BUS SPEED CONFIGURATION		
CPU speed	JP8	
<= 33MHz	Open	
> 33MHz	Closed	